

■ Features

- Low power loss, high efficiency.
- High surge current capability.
- Super fast recovery time for switching mode application.
- Low power loss.
- Glass passivated chip junctions.
- Suffix "G" indicates Halogen-free part, ex.ETF10005G.
- Lead-free parts meet environmental standards of MIL-STD-19500 /228

■ Mechanical data

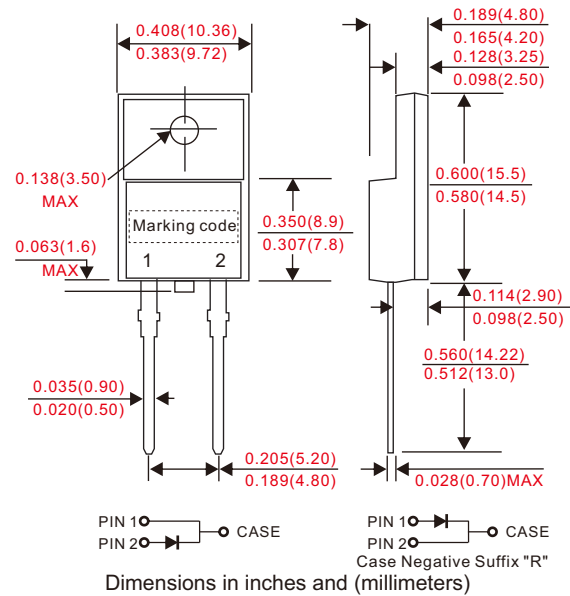
- Epoxy : UL94-V0 rated flame retardant.
- Case : JEDEC ITO-220AC molded plastic body.
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026.
- Polarity : As marked.
- Mounting Position : Any.
- Weight : Approximated 2.24 gram.

■ Maximum ratings and electrical characteristics

Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

■ Outline

ITO-220AC



Parameter	Conditions	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current		I_o			10	A
Forward surge current	8.3ms single half sine-wave superimposed on rate load (JEDEC method)	I_{FSM}			150	A
Reverse current	$V_R = V_{RRM}$ $T_A = 25^\circ C$	I_R			5.0	uA
	$V_R = V_{RRM}$ $T_A = 125^\circ C$				100	
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	C_j		62		pF
Storage temperature		T_{STG}	-55		+150	°C

Symbol	Marking code	Max. repetitive peak reverse voltage V_{RRM} (V)	Max. RMS voltage V_{RMS} (V)	Max. DC blocking voltage V_R (V)	Max. forward voltage @10A, $T_A = 25^\circ C$ V_F (V)	Max. reverse recovery time(1) T_{rr} (ns)	Operating temperature T_J (°C)
ETF10005	ETF10005	50	35	50	0.95	35	-55 ~ +150
ETF1001	ETF1001	100	70	100			
ETF1002	ETF1002	200	140	200			
ETF1004	ETF1004	400	280	400	1.30	50	
ETF1006	ETF1006	600	420	600			

Note : 1. $I_F = 0.5A$, $I_R = 1.0A$, $I_{RR} = 0.25A$

Rating and characteristic curves

Fig.1 - Forward Current Derating Curve

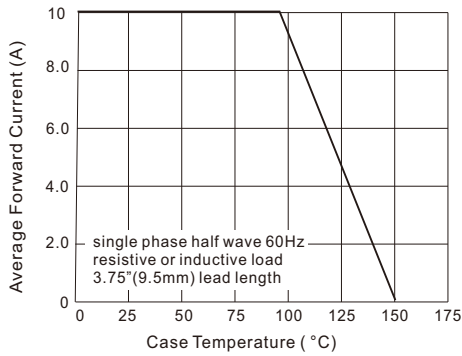


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

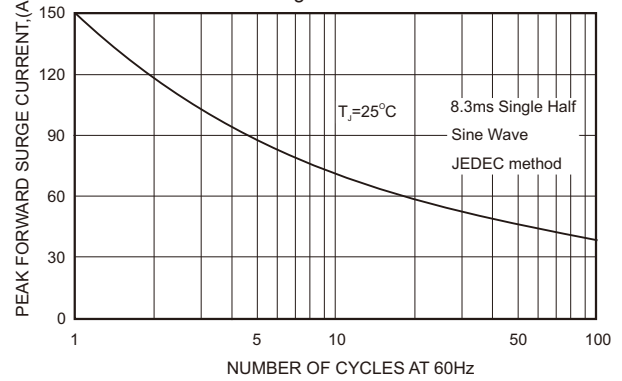


Fig. 3 - Typical Instantaneous Forward Characteristics

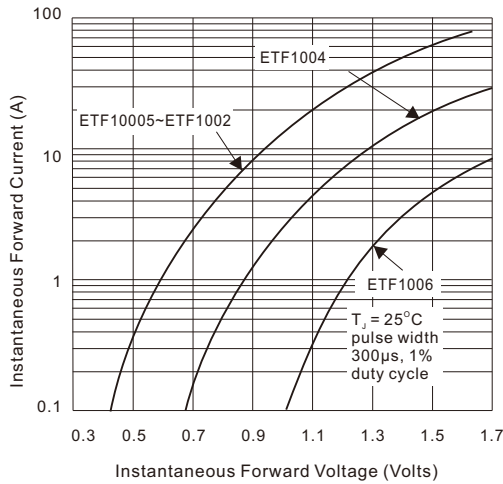


Fig. 4 - Typical Reverse Characteristics

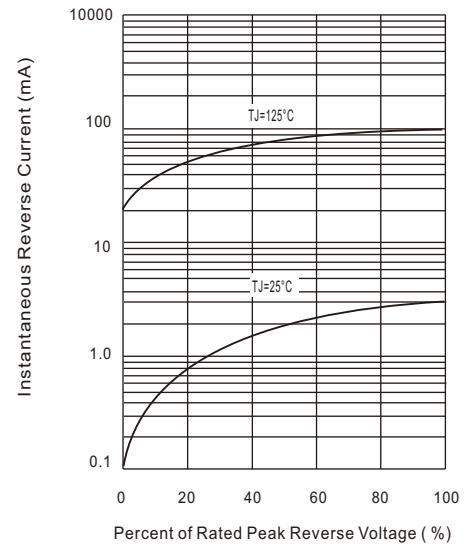
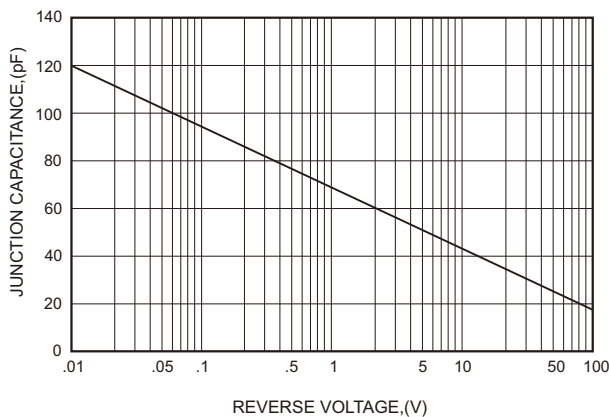


Fig. 5 - Typical Junction Capacitance



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