

## **ER2005 THRU ER210**

### 2A Leaded Type Super Fast Rectifiers

#### ■ Features

- Axial lead type devices for through hole design.
- · High current capability.
- Superfast recovery time for switching mode application.
- High surge current capability.
- Glass passivated chip junction.
- Suffix "G" indicates Halogen free parts, ex. ER2005G
- Lead-free parts meet environmental standards of MIL-STD-19500/228

#### ■ Mechanical data

• Epoxy:UL94-V0 rated flame retardant

· Case: Molded plastic, DO-204AC / DO-15

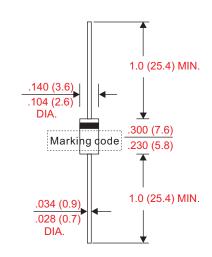
 Lead: Axial leads, solderable per MIL-STD-202, Method 208 guranteed

• Polarity: Color band denotes cathode end

• Weight: Approximated 0.39 gram

#### Outline

DO-15(DO-204AC)



Dimensions in inches and (millimeters)

#### ■ Maximum ratings and electrical characteristics

Rating at  $25^{\circ}$ C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

| Parameter                  | Conditions   | Symbol           | MIN. | TYP. | MAX. | UNIT |
|----------------------------|--|------------------|------|------|------|------|
| Forward rectified current  |  | Io               |      |      | 2.0  | Α    |
| Forward surge current      | 8.3ms single half sine-wave superimposed on rate load (JEDEC method) |                  |      |      | 50   | А    |
| _                          | $V_R = V_{RRM} T_A = 25^{\circ}C$                                    |                  |      |      | 1.0  | uA   |
| Reverse current            | $V_R = V_{RRM} T_A = 125^{\circ}C$                                   | I <sub>R</sub>   |      |      | 300  |      |
| Diode junction capacitance | f=1MHz and applied 4V DC reverse voltage                             | C,               |      | 50   |      | pF   |
| Storage temperature        |  | T <sub>STG</sub> | -50  |      | +150 | °C   |

| Symbol            | Marking code | Max. repetitive peak reverse voltage V <sub>RRM</sub> (V) | Max.<br>RMS voltage<br>V <sub>RMS</sub> (V) | Max. DC blocking voltage $V_{\mathbb{R}}(V)$ | Max.<br>forward voltage<br>@2A, T <sub>A</sub> = 25°C<br>V <sub>F</sub> (V) | Max. reverse recovery time(1) T <sub>π</sub> (ns) | Operating temperature T <sub>J</sub> (°C) |
|-------------------|--------------|---|---|--|---|---|---|
| ER2005            | ER2005       | 50  | 35  | 50   |   |   | -50 ~ +150                                |
| ER201             | ER201        | 100   | 70  | 100  | 0.95  | 35  |   |
| ER202             | ER202        | 200   | 140   | 200  |   |   |   |
| ER204             | ER204        | 400   | 280   | 400  | 1.25  |   |   |
| ER206             | ER206        | 600   | 420   | 600  |   |   |   |
| ER208             | ER208        | 800   | 560   | 800  | 1.70  | 7.5   |   |
| ER210             | ER210        | 1000  | 700   | 1000   |   | 75  |   |
| Note : 1 I = 0.5A | I = 1 0      |   |   |  |   |   |   |

Note : 1.  $I_F = 0.5A$ ,  $I_R = 1.0A$ ,  $I_{RR} = 0.25A$ 

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#### ■ Rating and characteristic curves

FIG.1-TYPICAL FORWARD

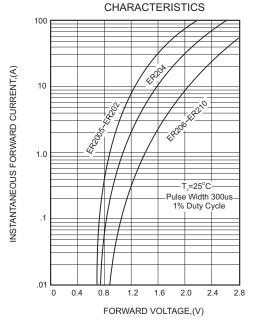
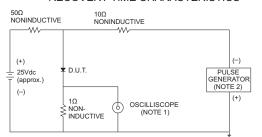


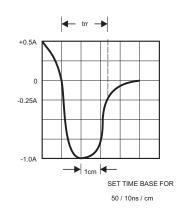
FIG.3- TEST CIRCUIT DIAGRAM AND REVERSE

#### RECOVERY TIME CHARACTERISTICS

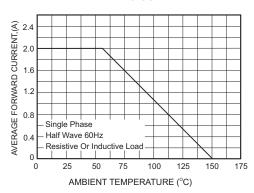


NOTES: 1. Rise Time= 7ns max., Input Impedance= 1 megohm.22pF.

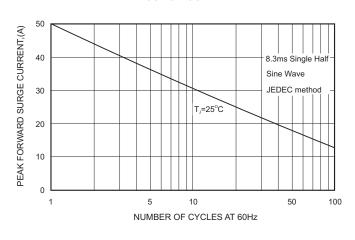
2. Rise Time= 10ns max., Source Impedance= 50 ohms.



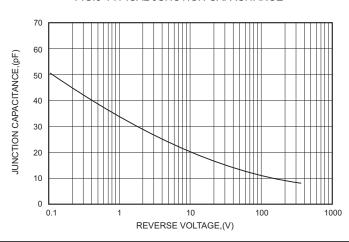
# FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE



## FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT



#### FIG.5-TYPICAL JUNCTION CAPACITANCE



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