

### ■ Features

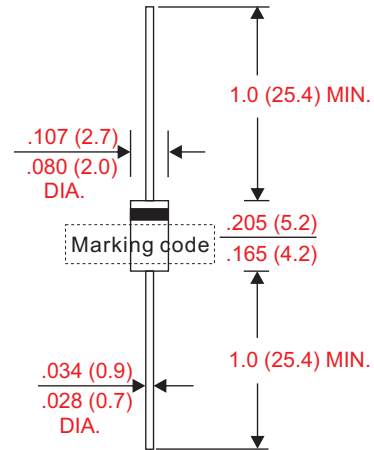
- Electrostatic discharge (ESD) test under IEC6100-4-2 standard >16KV(SR12~SR16). standard >10KV(SR110~SR120).
- Axial lead type devices for through hole design.
- Low power loss, high efficiency.
- High current capability, low forward voltage drop.
- High surge capability.
- Ultra high-speed switching.
- Silicon epitaxial planar chip, metal silicon junction.
- Suffix "G" indicates Halogen-free part, ex. SR12G.
- Lead-free parts meet environmental standards of MIL-STD-19500 /228

### ■ Mechanical data

- Epoxy: UL94-V0 rated flame retardant
- Case : Molded plastic, DO-204AL / DO-41
- Lead : Axial leads, solderable per MIL-STD-202, Method 208 guaranteed
- Polarity : Color band denotes cathode end
- Weight : Approximated 0.33 gram

### ■ Outline

DO-41(DO-204AL)



Dimensions in inches and (millimeters)

### ■ Maximum ratings and electrical characteristics

Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter	Conditions	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.1	$I_o$			1.0	A
Forward surge current	8.3ms single half sine-wave superimposed on rate load (JEDEC method)	$I_{FSM}$			30	A
Reverse current	$V_R = V_{RRM}$ $T_A = 25^\circ\text{C}$	$I_R$			0.5	mA
	$V_R = V_{RRM}$ $T_A = 100^\circ\text{C}$				20	
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	$C_j$		120		pF
Thermal resistance	Junction to ambient	$R_{BJA}$		88		°C/W
Storage temperature		$T_{STG}$	-55		+175	°C

Symbol	Marking code	Max. repetitive peak reverse voltage $V_{RRM}$ (V)	Max. RMS voltage $V_{RMS}$ (V)	Max. DC blocking voltage $V_R$ (V)	Max. forward voltage @1A, $T_A = 25^\circ\text{C}$ $V_F$ (V)	Operating temperature $T_J$ (°C)
SR12	SR12	20	14	20	0.45	-50 ~ +150
SR14	SR14	40	28	40	0.50	
SR16	SR16	60	42	60	0.70	
SR110	SR110	100	70	100	0.81	
SR115	SR115	150	105	150	0.87	-50 ~ +175
SR120	SR120	200	140	200	0.90	

### Rating and characteristic curves

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

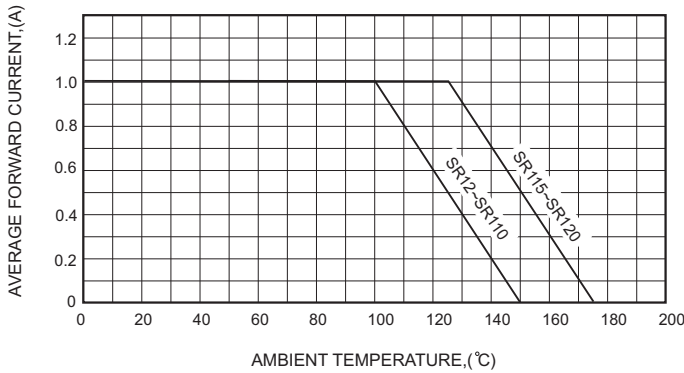


FIG.2-TYPICAL FORWARD CHARACTERISTICS

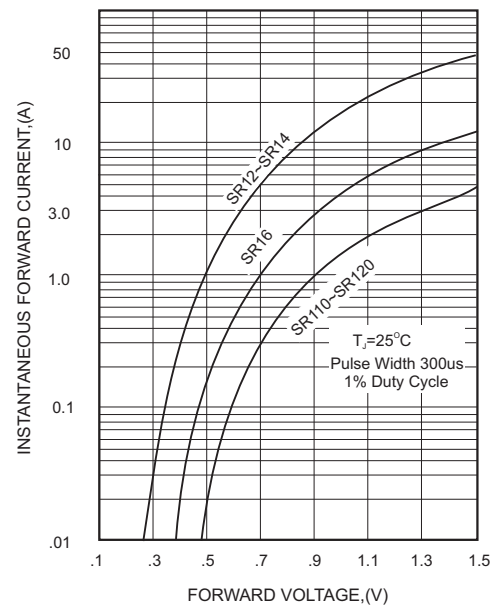


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

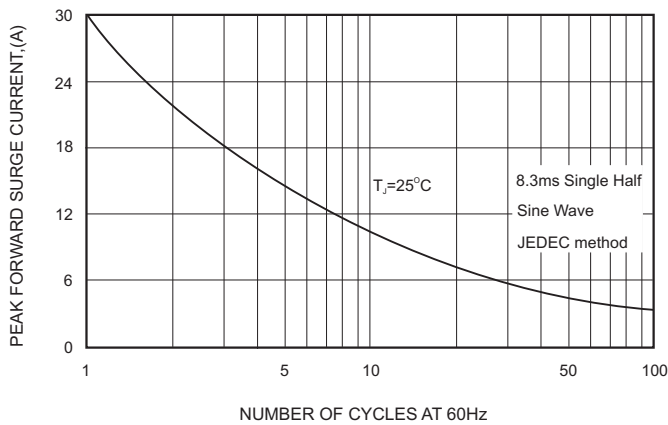


FIG.5 - TYPICAL REVERSE CHARACTERISTICS

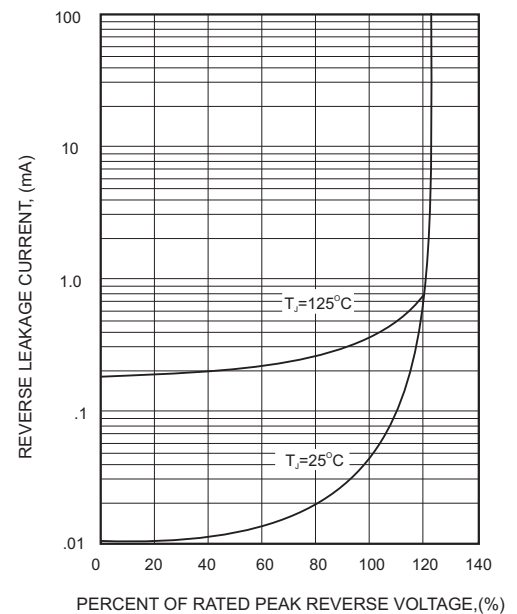
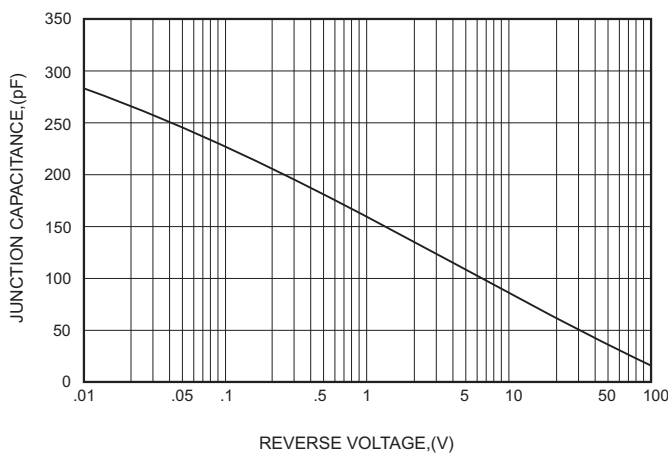


FIG.4-TYPICAL JUNCTION CAPACITANCE



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