

SKS22A THRU SKS26A

2A Surface Mount Schottky Barrier Rectifiers

■ Features

- Low profile surface mounted application in order to optimize board space.
- Low power loss, high efficiency.
- High current capability, low forward voltage drop.
- Ultra high-speed switching.
- Silicon epitaxial planar chip, metal silicon junction.
- Suffix "G" indicates Halogen-free part, ex.SKS22AG.
- Lead-free parts meet environmental standards of MIL-STD-19500 /228

■ Mechanical data

• Epoxy:UL94-V0 rated flame retardant

· Case: Molded plastic, DO-214AC / SMA

• Terminals : Solder plated, solderable per

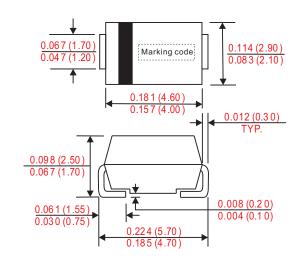
MIL-STD-750, Method 2026

• Polarity: Indicated by cathode band

• Weight: 0.002 ounce, 0.055 gram

■ Outline

SMA(DO-214AC)



Dimensions in inches and (millimeters)

■ Maximum ratings and electrical characteristics

Rating at 25° C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter	Conditions	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.1	Io			2.0	Α
Forward surge current	8.3ms single half sine-wave superimposed on rate load (JEDEC method)	ed on I _{FSM}			50	Α
Barrana	$V_R = V_{RRM} T_A = 25^{\circ}C$				0.5	mA
Reverse current	$V_R = V_{RRM} T_A = 100^{\circ}C$	I _R			20	
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	C,		30		pF
Thermal resistance	Junction to ambient	R _{eJA}		50		°C/W
Storage temperature		T _{stg}	-55		+175	°C

Symbol	Marking code	Max. repetitive peak reverse voltage V_RRM (V)	Max. RMS voltage V _{RMS} (V)	Max. DC blocking voltage $V_{_{R}}(V)$	Max. forward voltage $@2A, T_A = 25^{\circ}C$ $V_F(V)$	Operating temperature $T_{\downarrow}(^{\circ}C)$
SKS22A	KS22	20	14	20	0.35	
SKS24A	KS24	40	28	40	0.40	-55 ~ +150
SKS26A	KS26	60	42	60	0.50	

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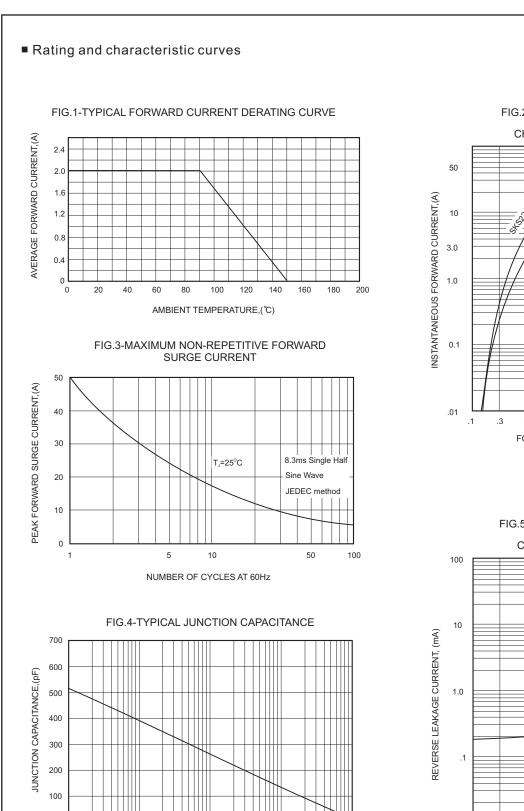


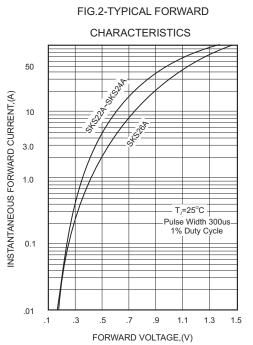
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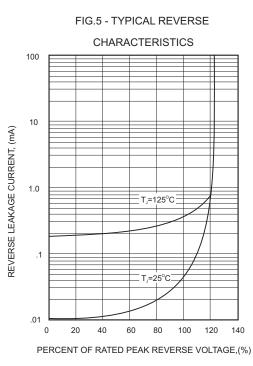
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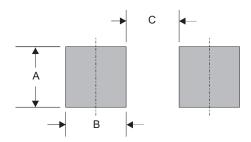
REVERSE VOLTAGE,(V)



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■ SMA foot print



А		В		С	
0.068 (1.	70)	0.104 (2.60)	0.060 (1.50)	

Dimensions in inches and (millimeters)

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