

SKL12B THRU SKL120B

1A Surface Mount Schottky Barrier Rectifiers

■ Features

- Low profile surface mounted application in order to optimize board space.
- Low power loss, high efficiency.
- High current capability, low forward voltage drop.
- Ultra high-speed switching.
- Silicon epitaxial planar chip, metal silicon junction.
- Suffix "G" indicates Halogen-free part, ex.SKL12BG.
- Lead-free parts meet environmental standards of MIL-STD-19500/228

■ Mechanical data

• Epoxy:UL94-V0 rated flame retardant

· Case: Molded plastic, DO-214AA/SMB

• Terminals : Solder plated, solderable per

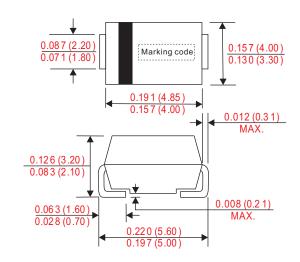
MIL-STD-750, Method 2026

• Polarity : Indicated by cathode band

• Weight: 0.003 ounce, 0.091 gram

Outline

SMB(DO-214AA)



Dimensions in inches and (millimeters)

■ Maximum ratings and electrical characteristics

Rating at 25° C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter	Conditions	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.1	Io			1.0	Α
Forward surge current	8.3ms single half sine-wave superimposed on rate load (JEDEC method)	I _{FSM}			50	Α
D	$V_R = V_{RRM} T_A = 25^{\circ}C$	_			0.5	
Reverse current	$V_R = V_{RRM} T_A = 100^{\circ}C$	I _R			20	mA
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	C,		120		pF
Thermal resistance	Junction to ambient	R _{eJA}		88		°C/W
Storage temperature		T _{STG}	-55		+175	°C

Marking code	Max. repetitive peak reverse voltage V _{RRM} (V)	Max. RMS voltage V _{RMS} (V)	Max. DC blocking voltage V _R (V)	Max. forward voltage $@1A, T_A = 25^{\circ}C$ $V_F(V)$	Operating temperature T _J (°C)			
KL12	20	14	20	0.40				
KL14	40	28	40	0.45	-55 ~ +150			
KL16	60	42	60	0.55				
KL110	100	70	100	0.75				
KL115	150	105	150	0.82	-55 ~ +175			
KL120	200	140	200	0.85				
	KL12 KL14 KL16 KL110 KL115	Marking code repetitive peak reverse voltage VRRM (V) KL12 20 KL14 40 KL16 60 KL110 100 KL115 150	Marking code repetitive peak reverse voltage V _{RRM} (V) Max. RMS voltage V _{RMS} (V) KL12 20 14 KL14 40 28 KL16 60 42 KL110 100 70 KL115 150 105	Marking code repetitive peak reverse voltage V _{RMS} (V) Max. BC blocking voltage V _{RMS} (V) KL12 20 14 20 KL14 40 28 40 KL16 60 42 60 KL110 100 70 100 KL115 150 105 150				

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■ Rating and characteristic curves



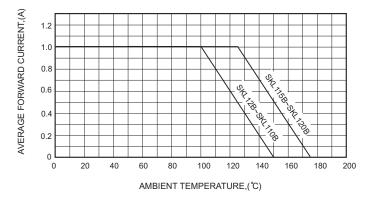


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

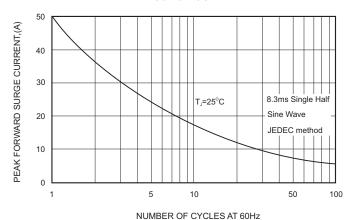


FIG.4-TYPICAL JUNCTION CAPACITANCE

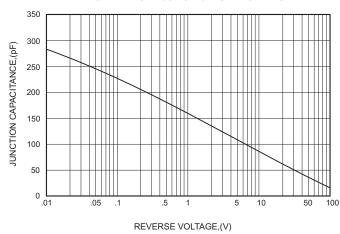


FIG.2-TYPICAL FORWARD

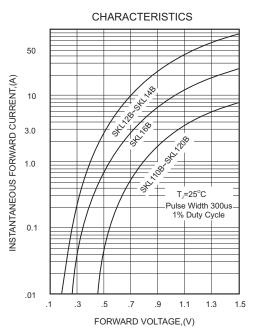
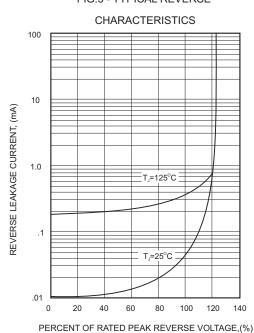


FIG.5 - TYPICAL REVERSE



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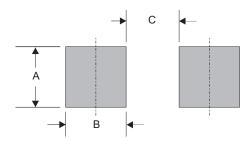
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■ SMB foot print



А		В	С
0.091 (2	2.30)	0.098 (2.50)	0.071 (1.80)

Dimensions in inches and (millimeters)

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