

## MBRD820/S - MBRD8100/S

### 8.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

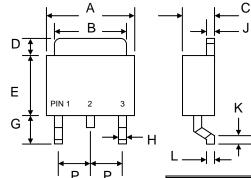


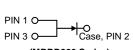
#### **Features**

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- High Surge Current Capability
- Low Power Loss, High Efficiency
- Ideally Suited for Automatic Assembly
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications

#### **Mechanical Data**

- Case: DPAK/TO-252, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Weight: 0.3 grams (approx.)
- Mounting Position: Any
- Marking: Device Code, See Page 3
- Lead Free: For RoHS / Lead Free Version, Add "-LF" Suffix to Part Number, See Page 4





(MBRD820 Series)

PIN 3 O → O Case, PIN 2 (MBRD820S Series)

DPAK/TO-252						
Dim	Min Max					
Α	6.05	6.65				
В	5.05	5.55				
С	2.25	2.40				
D	1.05	1.25				
Е	5.48	6.08				
G	2.55	3.00				
Н	0.55	0.90				
J	0.49	0.55				
K	0.95	1.25				
L	0.49	0.55				
Р	2.30 Typical					
All Dimensions in mm						

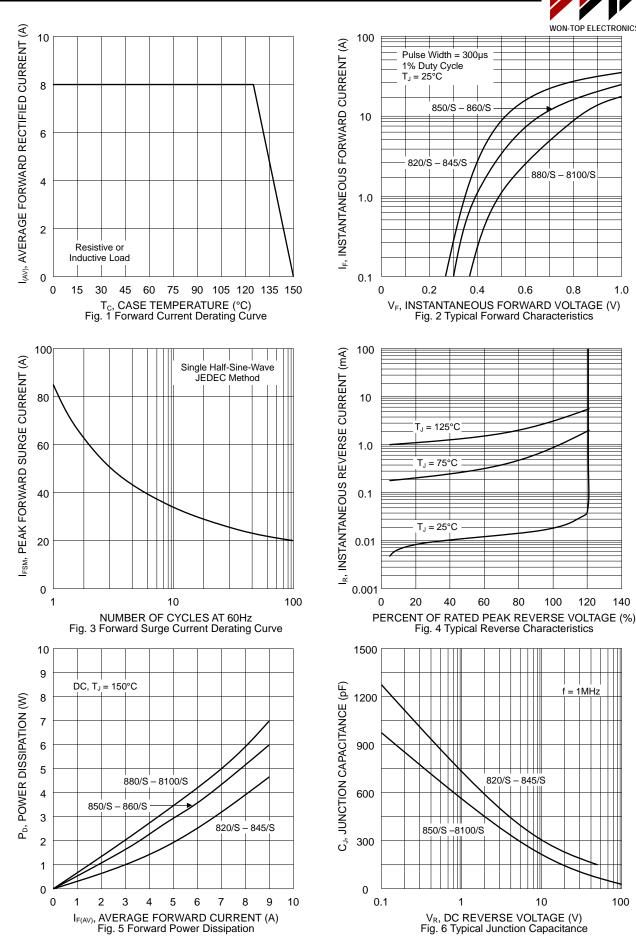
### Maximum Ratings and Electrical Characteristics @T<sub>A</sub>=25°C unless otherwise specified

Characteristic	Symbol	MBRD 820/S	MBRD 830/S	MBRD 840/S	MBRD 845/S	MBRD 850/S	MBRD 860/S	MBRD 880/S	MBRD 8100/S	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vr	20	30	40	45	50	60	80	100	V
RMS Reverse Voltage	VR(RMS)	14	21	28	32	35	42	56	70	V
Average Rectified Output Current $@T_c = 125^{\circ}C$	lo	8.0						А		
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	IFSM	85					A			
Forward Voltage @I <sub>F</sub> = 8.0A	Vfm	0.55 0.75 0.85			85	V				
Peak Reverse Current $@T_J = 25^{\circ}C$ At Rated DC Blocking Voltage $@T_J = 100^{\circ}C$	Irm	0.2 20					mA			
Typical Junction Capacitance (Note 1)	Сл	450 350				pF				
Thermal Resistance, Junction to Ambient (Note 2) Thermal Resistance, Junction to Case (Note 2)	R JA R JC	80 3.0						°C/W		
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150					°C			

Note: 1. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.

2. Mounted on FR-4 PC board with minimum recommended pad layout.

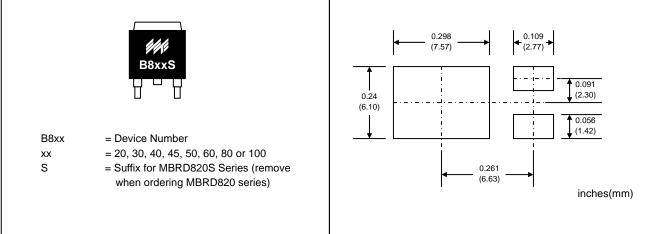
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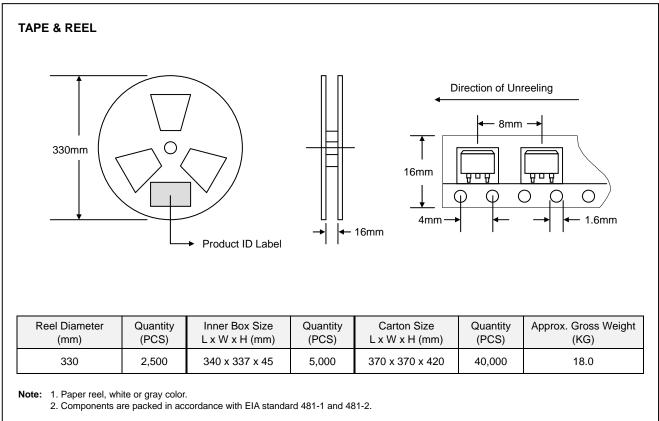
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# MARKING INFORMATION RECOMMENDED FOOTPRINT



### PACKAGING INFORMATION





Product No.	Package Type	Shipping Quantity				
MBRD820/S-T3	DPAK	2500/Tape & Reel				
MBRD830/S-T3	DPAK	2500/Tape & Reel				
MBRD840/S-T3	DPAK	2500/Tape & Reel				
MBRD845/S-T3	DPAK	2500/Tape & Reel				
MBRD850/S-T3	DPAK	2500/Tape & Reel				
MBRD860/S-T3	DPAK	2500/Tape & Reel				
MBRD880/S-T3	DPAK	2500/Tape & Reel				
MBRD8100/S-T3	DPAK	2500/Tape & Reel				

#### **ORDERING INFORMATION**

1. Shipping quantity given is for minimum packing quantity only. For minimum order quantity, please consult the Sales Department.

2. To order RoHS / Lead Free version (with Lead Free finish), add "-LF" suffix to part number above. For example, MBRD820-T3-LF.

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WARNING: DO NOT USE IN LIFE SUPPORT EQUIPMENT. WTE power semiconductor products are not authorized for use as critical components in life support devices or systems without the express written approval.

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