



STM8500A

SamHop Microelectronics Corp.

Ver 2.0

Dual Enhancement Mode Field Effect Transistor (N and P Channel)

PRODUCT SUMMARY (N-Channel)		
V _{DSS}	I _D	R _{DSON} (mΩ) Typ
55V	4.6A	46 @ V _{GS} =10V
		56 @ V _{GS} =4.5V

PRODUCT SUMMARY (P-Channel)		
V _{DSS}	I _D	R _{DSON} (mΩ) Typ
-55V	-3.5A	85 @ V _{GS} =-10V
		105 @ V _{GS} =-4.5V



ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise noted)

Symbol	Parameter	N-Channel	P-Channel	Units
V _{spilke^d}	Drain-Source Voltage Rating	60	-60	V
V _{DS}	Drain-Source Voltage	55	-55	V
V _{GS}	Gate-Source Voltage	±20	±20	V
I _D	Drain Current-Continuous ^a	4.6	-3.5	A
	T _A =25°C	3.7	-2.8	A
I _{DM}	-Pulsed ^b	20	-15	A
P _D	Maximum Power Dissipation ^a	2	1.28	W
T _J , T _{STG}	Operating Junction and Storage Temperature Range	-55 to 150		°C

THERMAL CHARACTERISTICS

R _{θ JA}	Thermal Resistance, Junction-to-Ambient ^a	62.5	°C/W
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Details are subject to change without notice.

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N-Channel ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	55			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=44\text{V}$, $V_{\text{GS}}=0\text{V}$			1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$			± 100	nA
ON CHARACTERISTICS						
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	1.0	1.9	3.0	V
$\text{R}_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=4.6\text{A}$		46	56	m ohm
		$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=4\text{A}$		56	75	m ohm
g_{FS}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_{\text{D}}=4.6\text{A}$		9		S
DYNAMIC CHARACTERISTICS ^c						
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$ $f=1.0\text{MHz}$		510	590	pF
C_{oss}	Output Capacitance			67	78	pF
C_{rss}	Reverse Transfer Capacitance			40	50	pF
SWITCHING CHARACTERISTICS ^c						
$t_{\text{D}(\text{ON})}$	Turn-On Delay Time	$V_{\text{DD}}=30\text{V}$ $I_{\text{D}}=1\text{A}$ $V_{\text{GS}}=10\text{V}$ $R_{\text{GEN}}=6 \text{ ohm}$		5.5	6.5	ns
t_{r}	Rise Time			9.4	13	ns
$t_{\text{D}(\text{OFF})}$	Turn-Off Delay Time			11.7	13.8	ns
t_{f}	Fall Time			4.4	5.1	ns
Q_{g}	Total Gate Charge	$V_{\text{DS}}=30\text{V}$, $I_{\text{D}}=4.6\text{A}$, $V_{\text{GS}}=10\text{V}$		11.9	14	nC
		$V_{\text{DS}}=30\text{V}$, $I_{\text{D}}=4.6\text{A}$, $V_{\text{GS}}=4.5\text{V}$		6.1	7.1	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=30\text{V}$, $I_{\text{D}}=4.6\text{A}$, $V_{\text{GS}}=4.5\text{V}$		1.8	2.2	nC
Q_{gd}	Gate-Drain Charge			2.8	3.3	nC
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
I_{s}	Maximum Continuous Drain-Source Diode Forward Current				1.3	A
V_{SD}	Diode Forward Voltage ^b	$V_{\text{GS}}=0\text{V}$, $I_{\text{s}}=1.3\text{A}$		0.8	1.2	V

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P-Channel ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$	-55			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=-44\text{V}$, $V_{\text{GS}}=0\text{V}$			-1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm20\text{V}$, $V_{\text{DS}}=0\text{V}$			±100	nA
ON CHARACTERISTICS						
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=-250\mu\text{A}$	-1.0	-1.9	-3.0	V
$\text{R}_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-3.5\text{A}$		85	105	m ohm
		$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-2.9\text{A}$		105	145	m ohm
g_{FS}	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$, $I_{\text{D}}=-3.5\text{A}$		7.8		S
DYNAMIC CHARACTERISTICS ^c						
C_{iss}	Input Capacitance	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}$ $f=1.0\text{MHz}$		830	960	pF
C_{oss}	Output Capacitance			75	87	pF
C_{rss}	Reverse Transfer Capacitance			45	52	pF
SWITCHING CHARACTERISTICS ^c						
$t_{\text{D}(\text{ON})}$	Turn-On Delay Time	$V_{\text{DD}}=-30\text{V}$ $I_{\text{D}}=-1\text{A}$ $V_{\text{GS}}=-10\text{V}$ $R_{\text{GEN}}=6\text{ ohm}$		6.5	7.7	ns
t_{r}	Rise Time			14.3	17	ns
$t_{\text{D}(\text{OFF})}$	Turn-Off Delay Time			43.2	51	ns
t_{f}	Fall Time			17.7	21	ns
Q_{g}	Total Gate Charge	$V_{\text{DS}}=-30\text{V}, I_{\text{D}}=-3.5\text{A}, V_{\text{GS}}=-10\text{V}$		15.5	17.8	nC
		$V_{\text{DS}}=-30\text{V}, I_{\text{D}}=-3.5\text{A}, V_{\text{GS}}=-4.5\text{V}$		7.1	8.2	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=-30\text{V}, I_{\text{D}}=-3.5\text{A},$ $V_{\text{GS}}=-4.5\text{V}$		2.4	2.8	nC
Q_{gd}	Gate-Drain Charge			3.2	3.8	nC
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
I_{s}	Maximum Continuous Drain-Source Diode Forward Current				-1.3	A
V_{SD}	Diode Forward Voltage ^b	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=-1.3\text{A}$		-0.79	-1.2	V
Notes						
a.Surface Mounted on FR4 Board, $t \leq 10\text{sec}$.						
b.Pulse Test:Pulse Width $\leq 300\text{us}$, Duty Cycle $\leq 2\%$.						
c.Guaranteed by design, not subject to production testing.						
d.Guaranteed when external $R_g=6\text{ ohm}$ and $t_f < t_{\text{f max}}$.						

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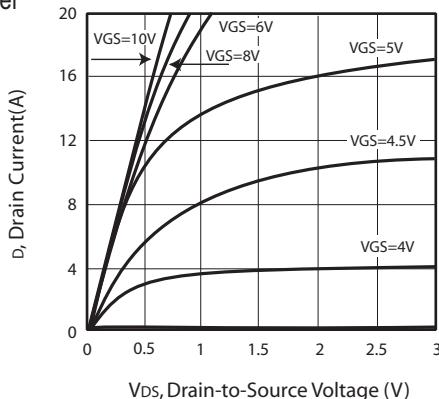


Figure 1. Output Characteristics

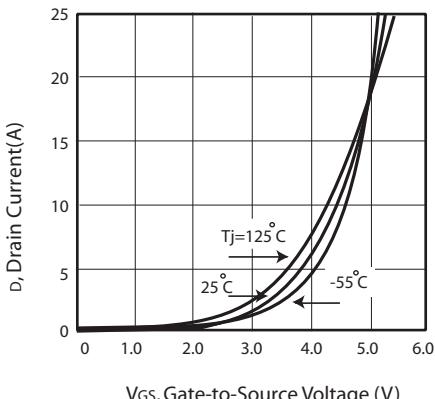


Figure 2. Transfer Characteristics

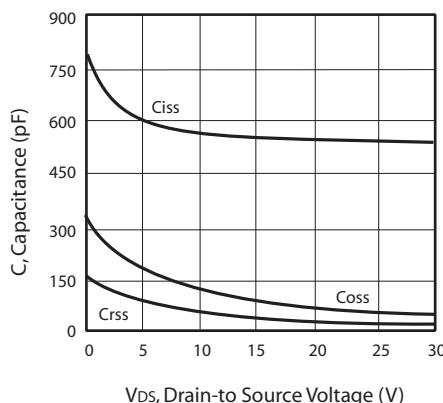


Figure 3. Capacitance

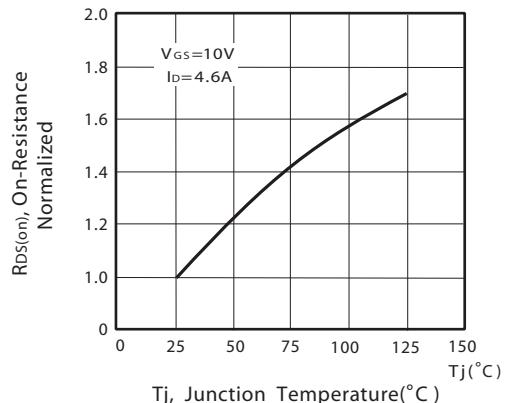


Figure 4. On-Resistance Variation with Drain Current and Temperature

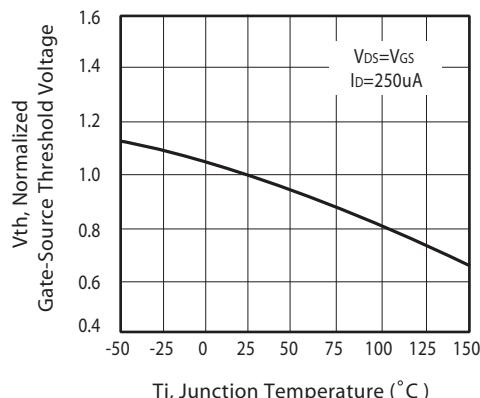


Figure 5. Gate Threshold Variation with Temperature

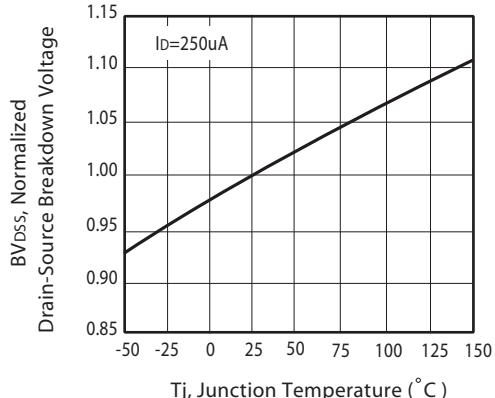


Figure 6. Breakdown Voltage Variation

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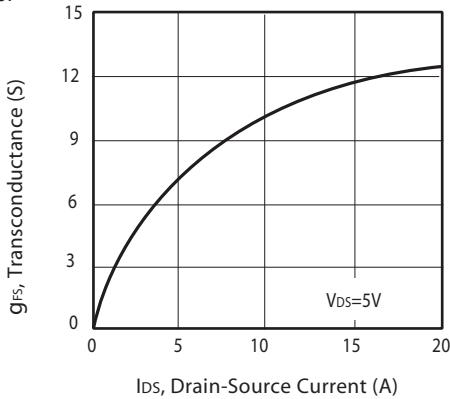


Figure 7. Transconductance Variation with Drain Current

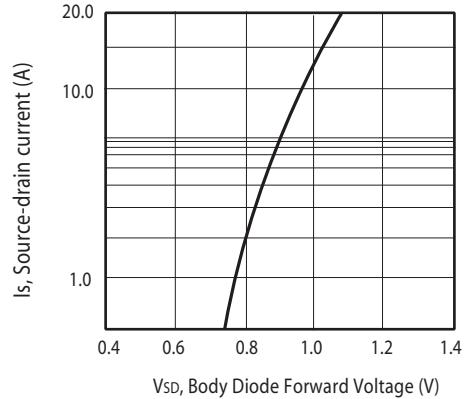


Figure 8. Body Diode Forward Voltage Variation with Source Current

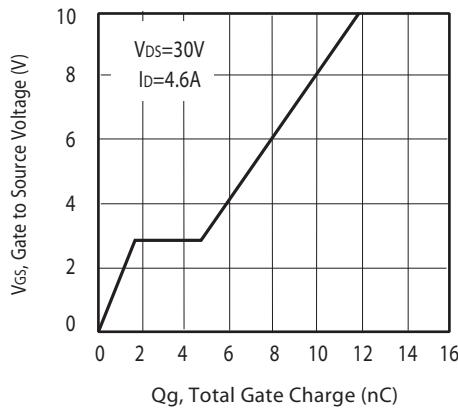


Figure 9. Gate Charge

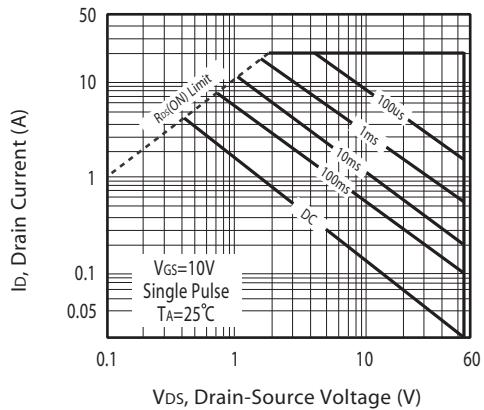
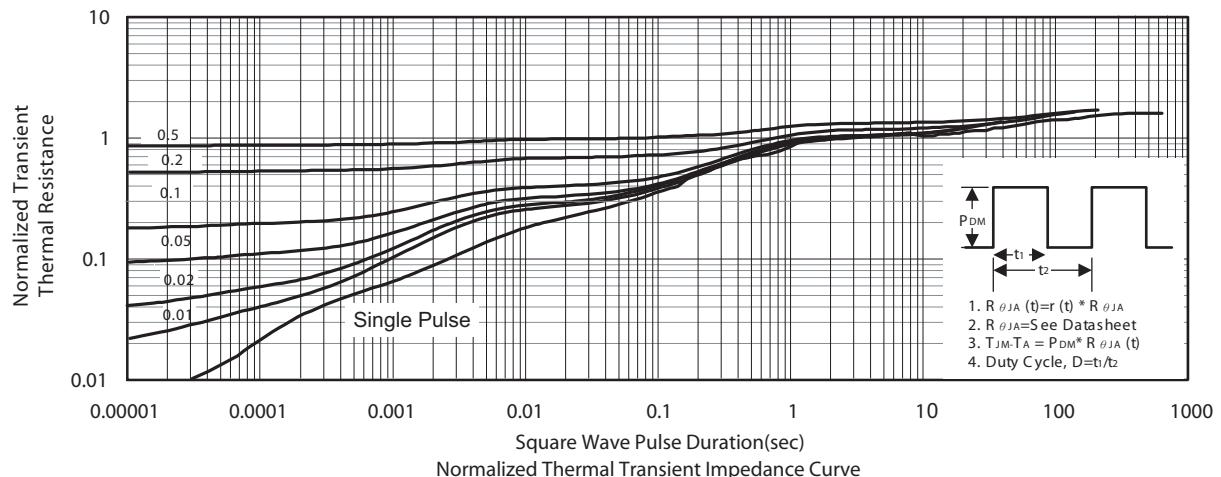


Figure 10. Maximum Safe Operating Area



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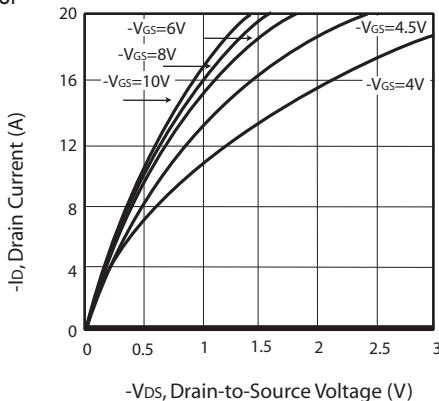


Figure 1. Output Characteristics

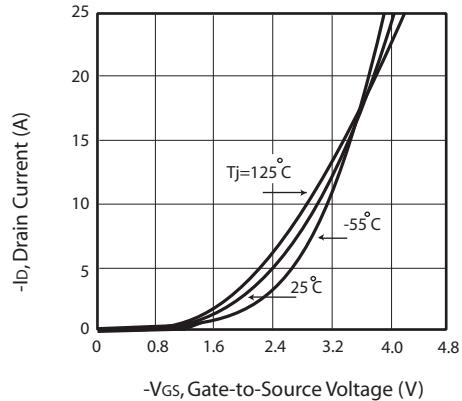


Figure 2. Transfer Characteristics

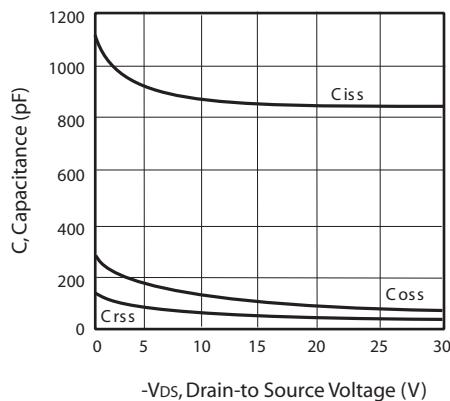


Figure 3. Capacitance

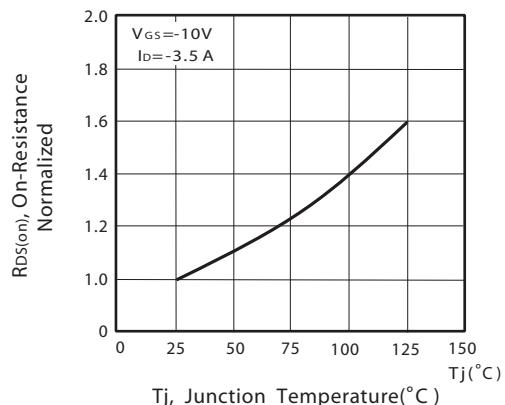


Figure 4. On-Resistance Variation with Drain Current and Temperature

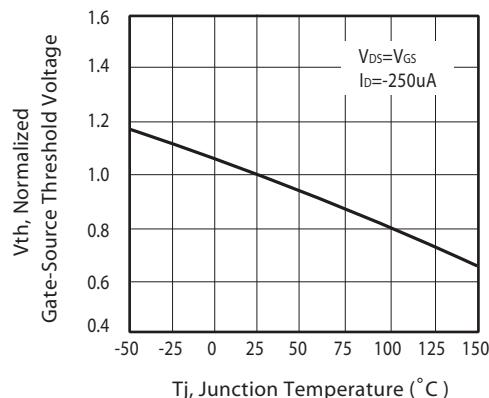


Figure 5. Gate Threshold Variation with Temperature

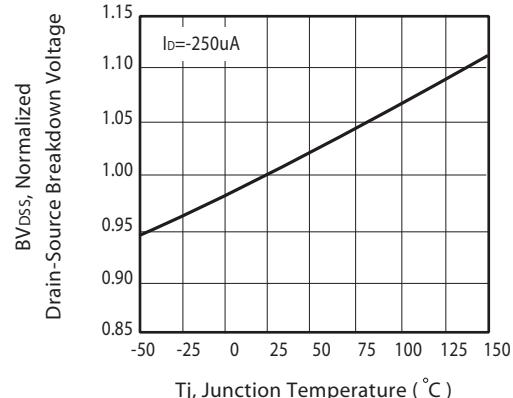


Figure 6. Breakdown Voltage Variation with Temperature

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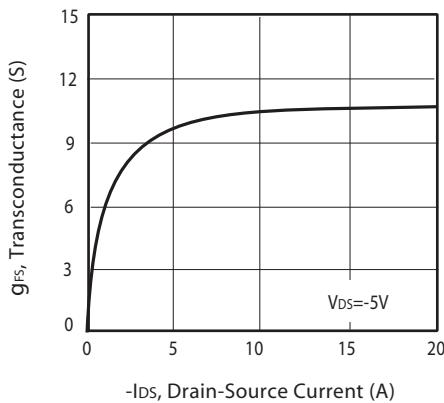


Figure 7. Transconductance Variation with Drain Current

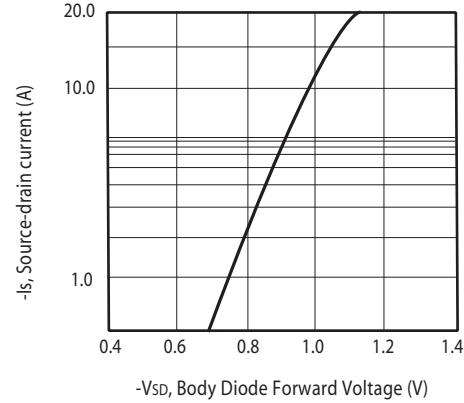


Figure 8. Body Diode Forward Voltage Variation with Source Current

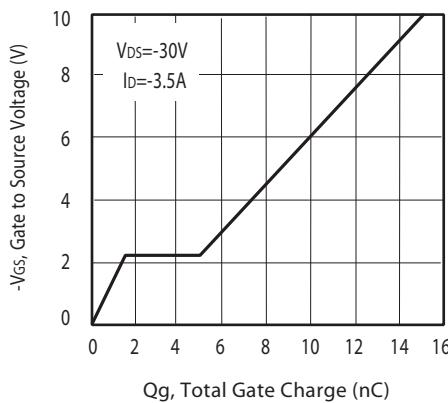


Figure 9. Gate Charge

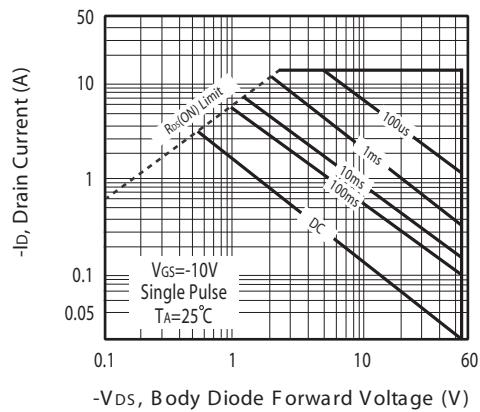
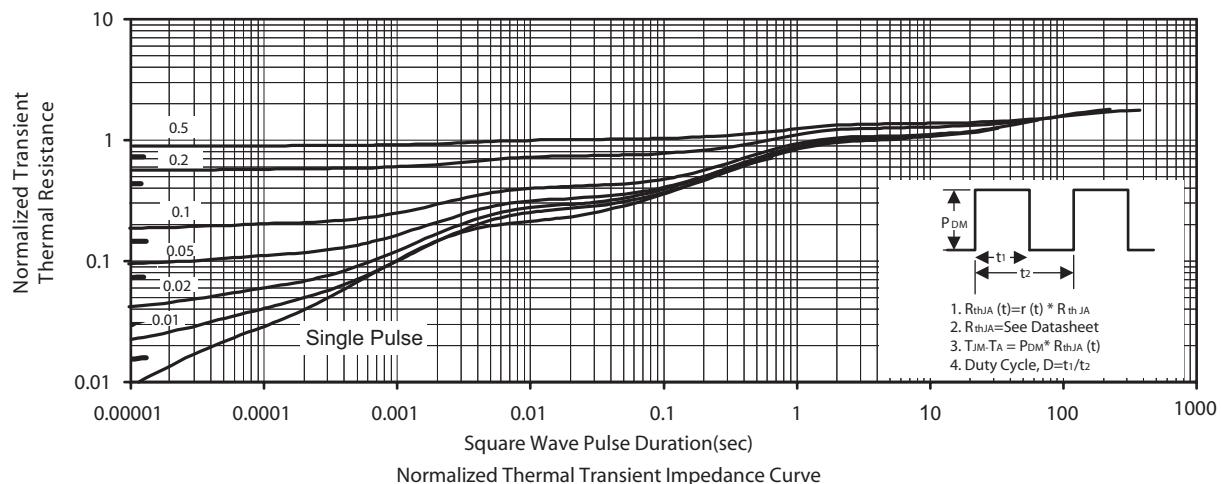


Figure 10. Maximum Safe Operating Area



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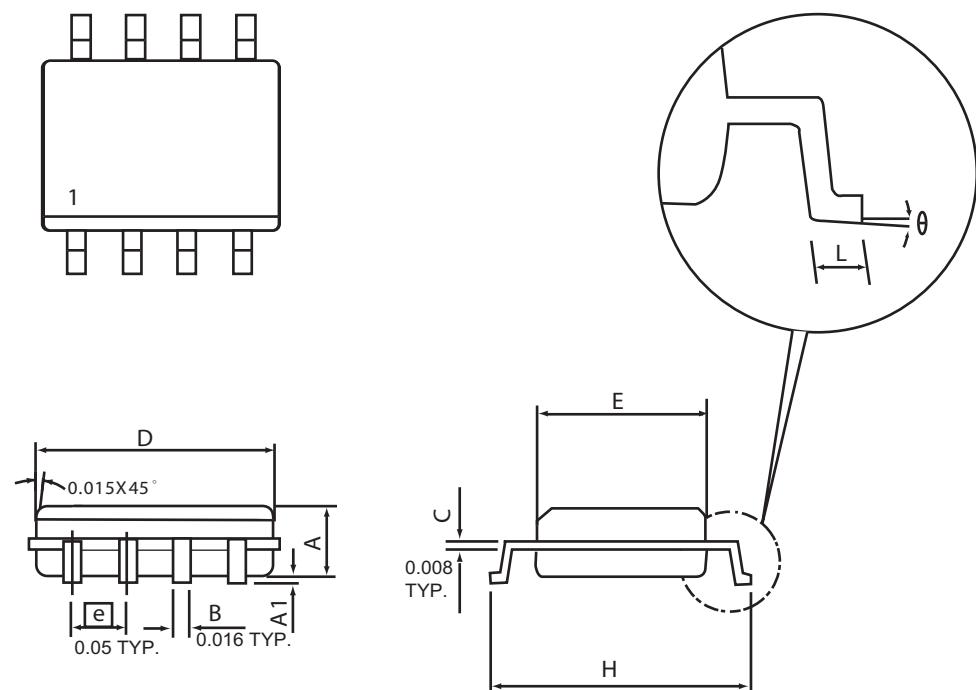
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PACKAGE OUTLINE DIMENSIONS

SO-8



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
D	4.80	4.98	0.189	0.196
E	3.81	3.99	0.150	0.157
H	5.79	6.20	0.228	0.244
L	0.41	1.27	0.016	0.050
θ	0°	8°	0°	8°

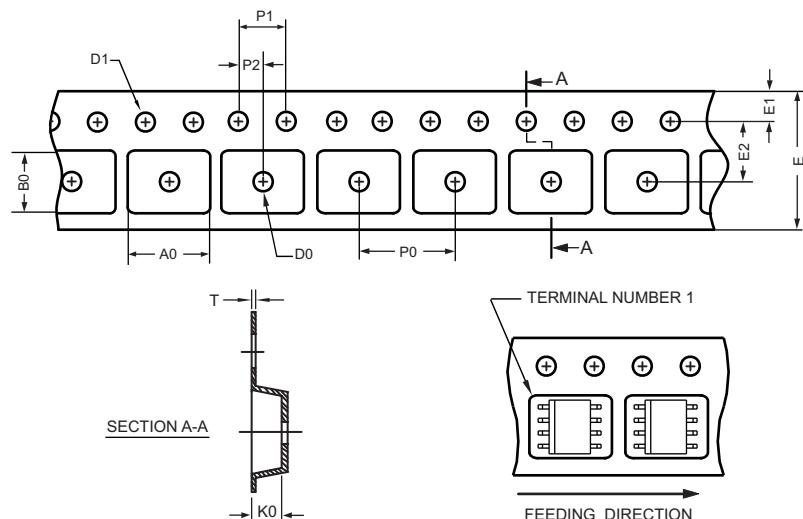
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SO-8 Tape and Reel Data

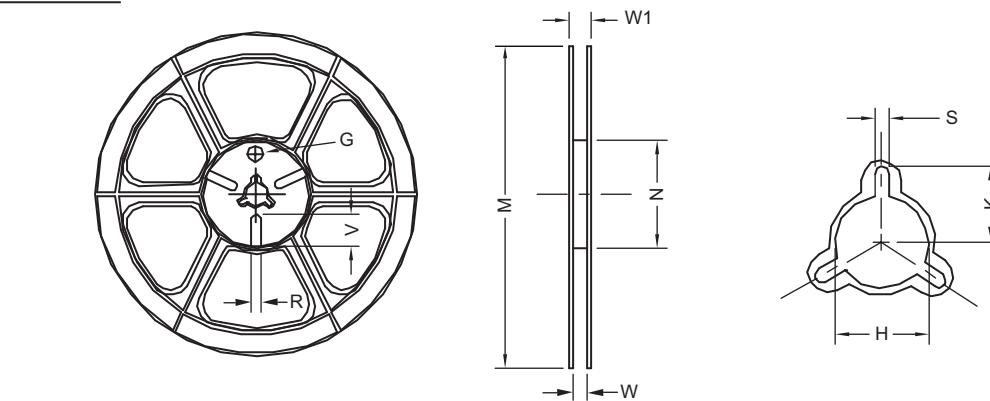
SO-8 Carrier Tape



unit:mm

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SOP 8N 150mil	6.50 ± 0.15	5.25 ± 0.10	2.10 ± 0.10	$\phi 1.5$ (MIN)	$\phi 1.55$ ± 0.10	12.0 $+0.3$ -0.1	1.75 ± 0.10	5.5 ± 0.10	8.0 ± 0.10	4.0 ± 0.10	2.0 ± 0.10	0.30 ± 0.013

SO-8 Reel



UNIT:mm

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12 mm	$\phi 330$	330 ± 1	62 ± 1.5	$12.4 + 0.2$	$16.8 - 0.4$	$\phi 12.75 + 0.15$	---	2.0 ± 0.15	---	---	---

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