

Epitaxial planar PNP silicon transistor

Description

• Dual chip digital transistor

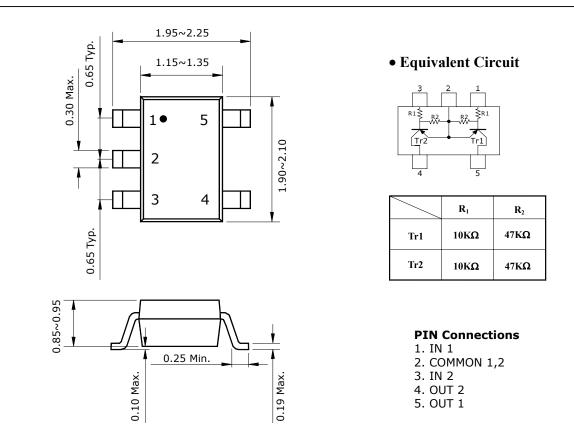
Features

- Two SRA2207 chips in SOT-353 package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

Ordering Information

Type NO.	Marking	Package Code		
SUR534H	34H	SOT-353		

Outline Dimensions



unit : mm

SUR534H

(Ta=25°C)

Absolute Maximum Ratings [Tr1,Tr2]

Absolute Maximum Ratings [Tr1	(Ta=25°C)		
Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	VI	-30, 6	V
Output current	I _O	-100	mA
Power dissipation	₽ _D [*]	200	mW
Junction temperature	Tյ	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

*: Total rating

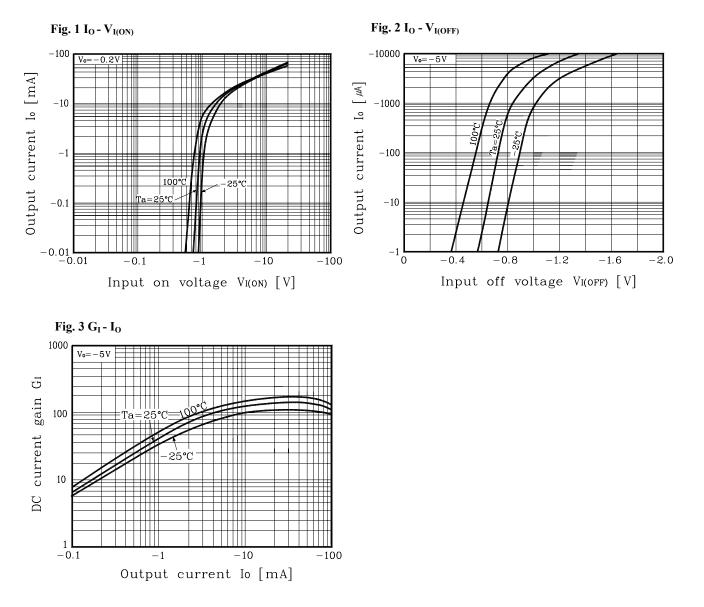
Electrical Characteristics [Tr1.Tr2]

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit	
Output cut-off current	$I_{O(OFF)}$	V ₀ =-50V, V _I =0	-	-	-500	nA	
DC current gain	GI	V ₀ =-5V, I ₀ =-10mA	80	150	-	-	
Output voltage	V _{O(ON)}	I_0 =-10mA, I_I =-0.5mA	-	-0.1	-0.3	V	
Input voltage (ON)	V _{I(ON)}	V ₀ =-0.2V, I ₀ =-5mA	-	-	-1.8	V	
Input voltage (OFF)	V _{I(OFF)}	V ₀ =-5V, I ₀ =-0.1mA	-0.5	-	-	V	
Transition frequency	f _T *	V_0 =-10V, I_0 =-5mA, f=1MHz	-	200	-	MHz	
Input current	II	V _I =-5V, I _O =0	-	-	-0.88	mA	
Input resistor (Input to base)	R ₁	-	7	10	13	KΩ	
Input resistor (Base to common)	R ₂	-	33	47	61	KΩ	

* : Characteristic of transistor only

SUR534H

Electrical Characteristic Curves [Tr1,Tr2]



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