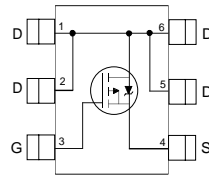
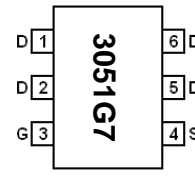


Main Product Characteristics:

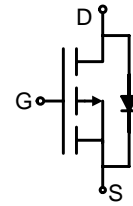
V_{DSS}	-30V
$R_{DS(on)}$	45mohm(typ.)
I_D	-4A



SOT23-6



Marking and pin
Assignment



Schematic diagram

Features and Benefits:

- Advanced trench MOSFET process technology
- Special designed for battery protection, load switching and general power management
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature

Description:

It utilizes the latest trench processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in battery protection, power switching application and a wide variety of other applications

Absolute max Rating:

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	±25	V
Drain Current-Continuous@ Current-Pulsed (Note 1)	I_D	-4	A
	I_{DM}	-25	A
Maximum Power Dissipation	P_D	1.7	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

Thermal Resistance

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	75	°C/W
Thermal Resistance, Junction-to-Case(Note 2)	$R_{\theta JC}$	30	°C/W



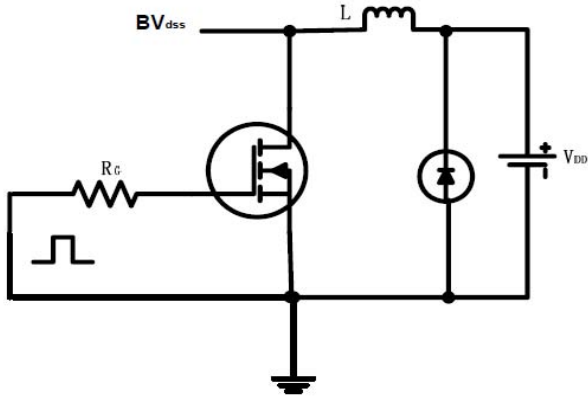
FTK3051

Electrical Characterizes @T_A=25°C unless otherwise specified

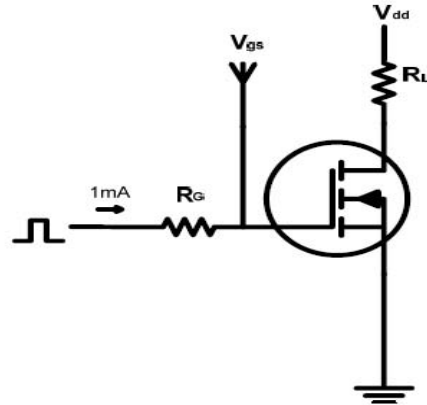
Parameter	Symbol	Condition	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250μA	-30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V, V _{GS} =0V			-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±25V, V _{DS} =0V			±100	nA
ON CHARACTERISTICS (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1	-1.6	-3	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-4A		45	51	mΩ
		V _{GS} =-4.5V, I _D =-3.4A		65	85	mΩ
Forward Transconductance	g _{FS}	V _{DS} =-5V, I _D =-4A		8.5		S
DYNAMIC CHARACTERISTICS (Note4)						
Input Capacitance	C _{iss}	V _{DS} =-15V, V _{GS} =0V, F=1.0MHz		520		PF
Output Capacitance	C _{oss}			94		PF
Reverse Transfer Capacitance	C _{rss}			73		PF
SWITCHING CHARACTERISTICS (Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =-15V, I _D =-1A V _{GS} =-10V, R _{GEN} =6Ω		8.9		nS
Turn-on Rise Time	t _r			4.0		nS
Turn-Off Delay Time	t _{d(off)}			22.6		nS
Turn-Off Fall Time	t _f			5.5		nS
Total Gate Charge	Q _g	V _{DS} =-5V, I _D =-4A, V _{GS} =-5V		7.1		nC
Gate-Source Charge	Q _{gs}			0.86		nC
Gate-Drain Charge	Q _{gd}			3.9		nC
DRAIN-SOURCE DIODE CHARACTERISTICS						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V, I _S =-1.3A		-0.8	-1.2	V
Diode Forward Current (Note 2)	I _S				-4	A
Reverse Recovery Time	t _{rr}	T _j =25°C, I _F =-4A, di/dt=-100A/uS		10.3		nS
Reverse Recovery Charge	Q _{rr}				4.3	

Test circuits and Waveforms

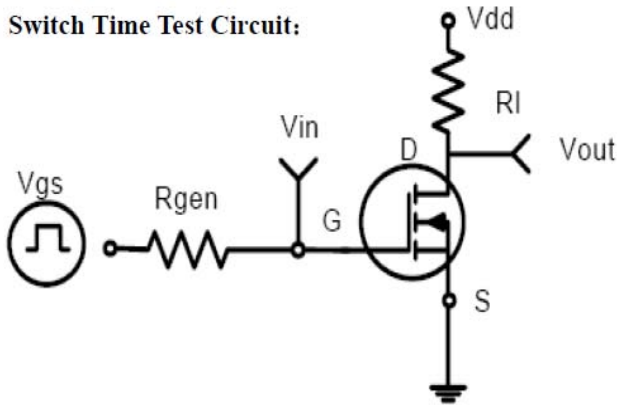
EAS test circuits:



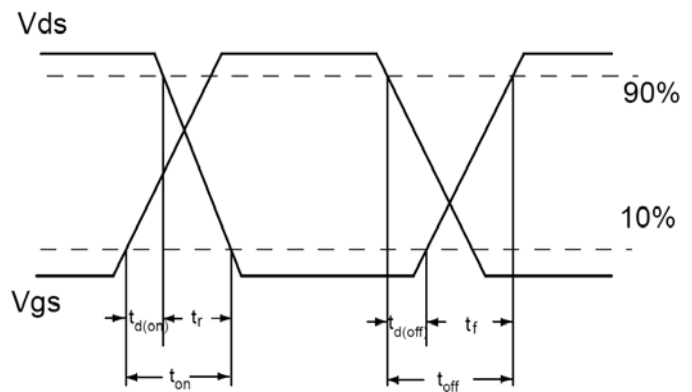
Gate charge test circuit:



Switch Time Test Circuit:



Switch Waveforms:



NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production testing.

Typical electrical and thermal characteristics

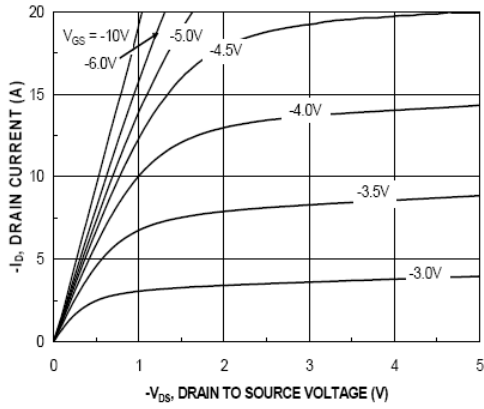


Figure 1: Typical Output Characteristics

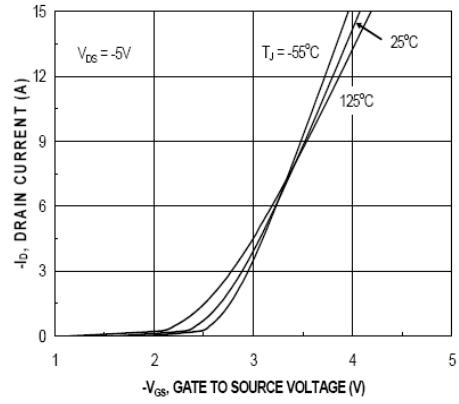


Figure 2: Transfer Characteristics

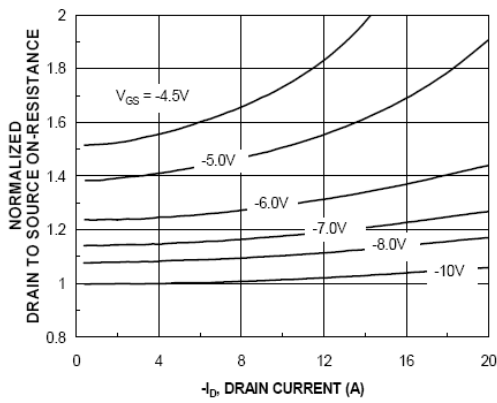


Figure 3: Drain-Source On-Resistance

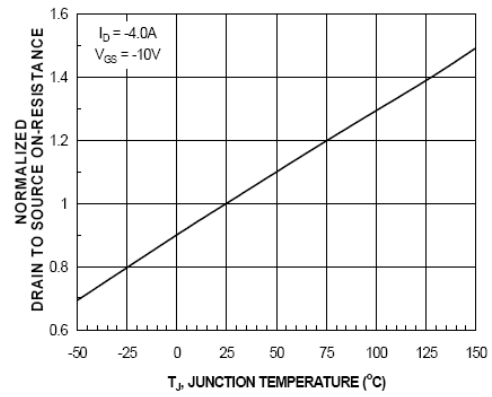


Figure 4: Drain-Source On-Resistance

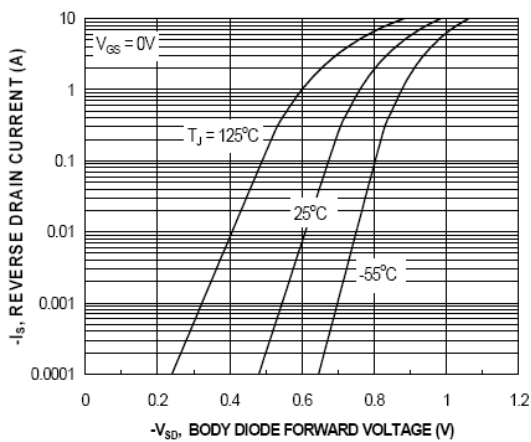


Figure 5 : Source- Drain Diode Forward

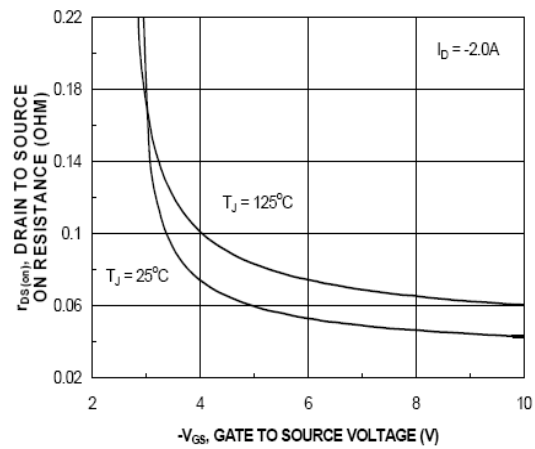


Figure 6: Rdson vs Vgs

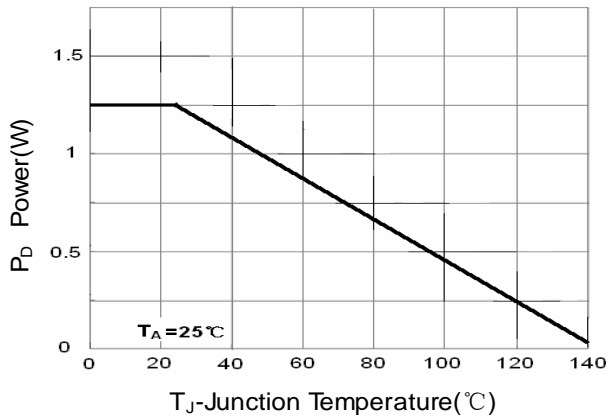


Figure 7: Power Dissipation

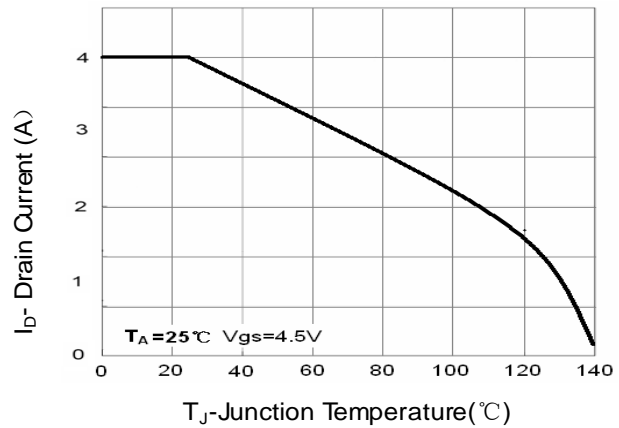


Figure 8: Drain Current

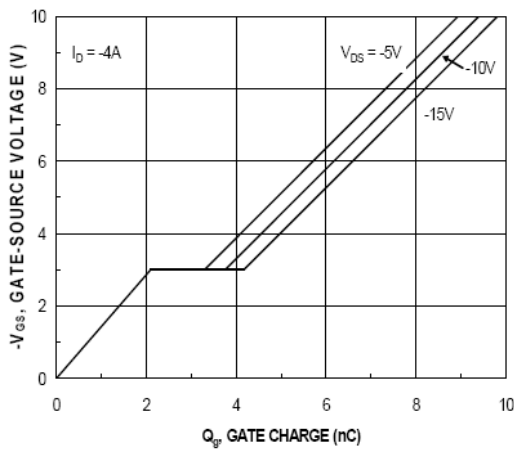


Figure 9: Gate Charge

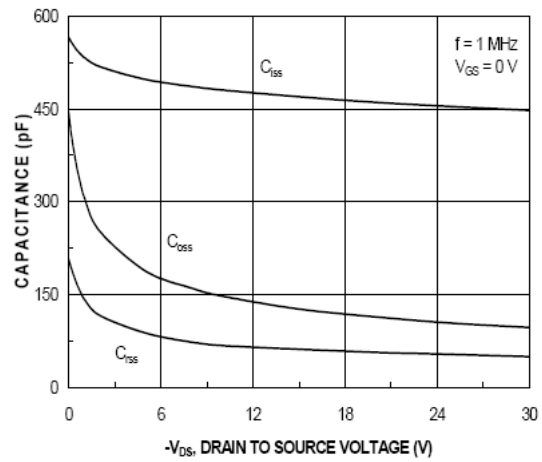


Figure 10: Capacitance vs Vds

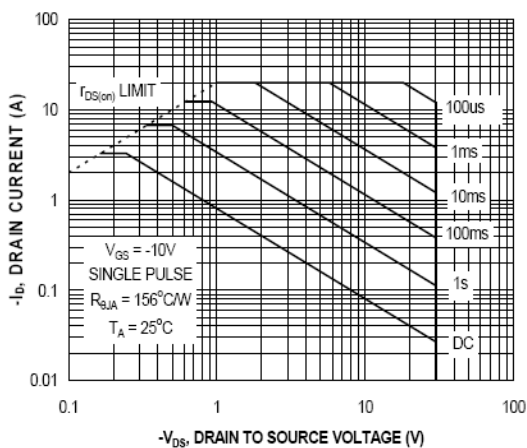


Figure 11: Safe Operation Area

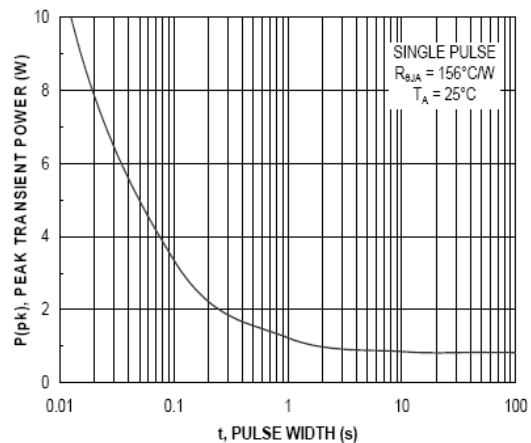


Figure 12: Single Pulse Maximum Power Dissipation

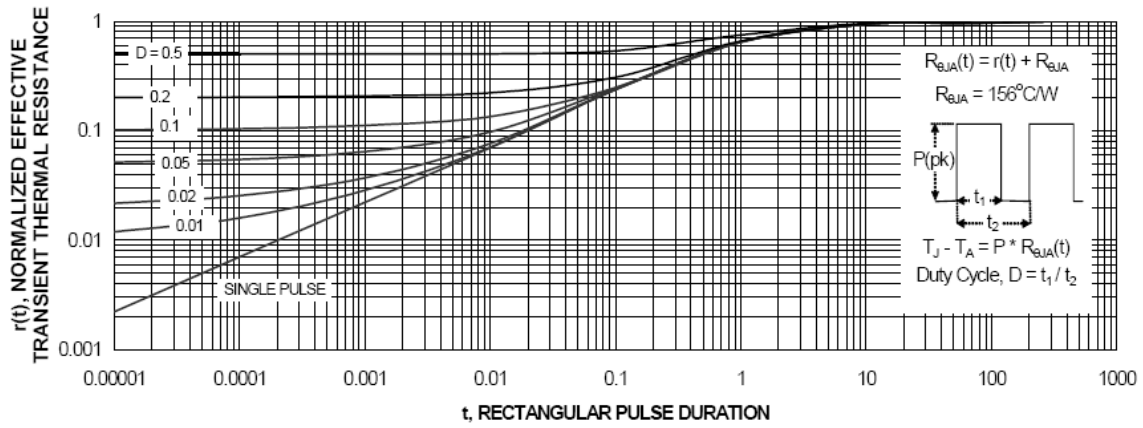
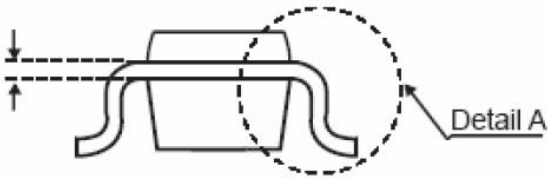
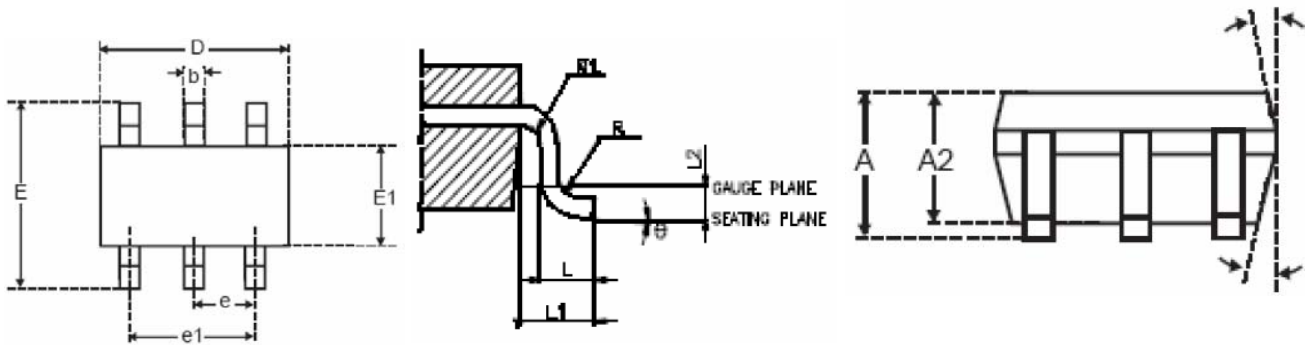


Figure 13: Normalized Maximum Transient Thermal Impedance

Mechanical Data:

SOT23-6 Dimensions in Millimeters (UNIT:mm)



SYMBOLS	MILLMETERS		
	MIN.	NOM.	MAX.
A			1.45
A1			0.15
A2	0.90	1.15	1.30
b	0.30		0.50
c	0.08		0.22
D	2.90 BSC.		
E	2.80 BSC.		
E1	1.60 BSC.		
e	0.95 BSC.		
e1	1.90 BSC.		
L	0.30	0.45	0.60
L1	0.60 REF		
L2	0.25 BSC.		
R	0.10		
R1	0.10		0.25
θ	0°	4°	8°
$\theta 1$	5°	10°	15°

NOTES:

1. All dimensions are in millimeters.
2. Dimensions are inclusive of plating
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 6 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.