

DESCRIPTION

The FTK4503 uses advanced trench technology MOSFET to provide excellent $R_{DS(ON)}$ and low gate charge. The complementary MOSFET may be used in power inverters, and other applications.

GENERAL FEATURES

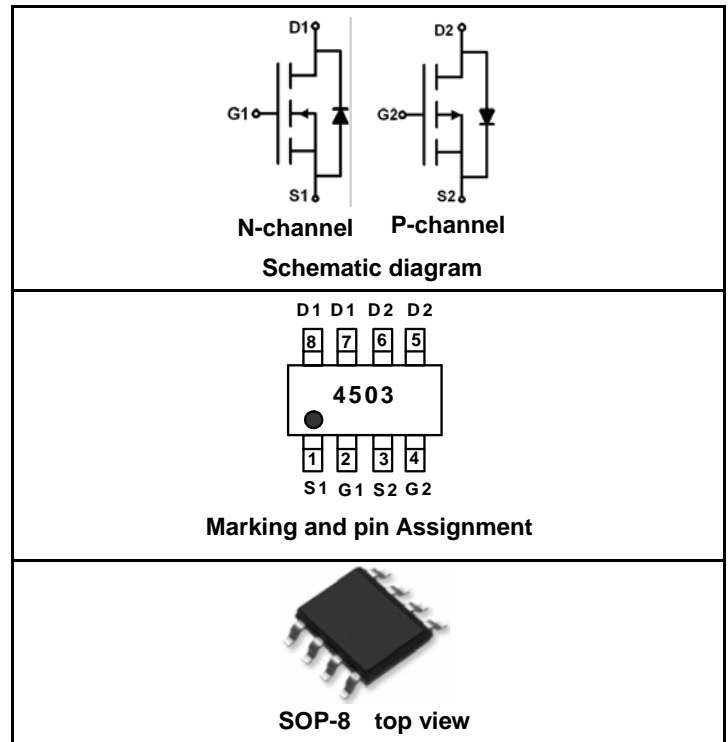
●N-Channel

$V_{DS} = 30V, I_D = 6.9A$
 $R_{DS(ON)} < 42m\Omega @ V_{GS}=4.5V$
 $R_{DS(ON)} < 28m\Omega @ V_{GS}=10V$

●P-Channel

$V_{DS} = -30V, I_D = -6.3A$
 $R_{DS(ON)} < 55m\Omega @ V_{GS}=-4.5V$
 $R_{DS(ON)} < 36m\Omega @ V_{GS}=-10V$

- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package



ABSOLUTE MAXIMUM RATINGS(TA=25°C unless otherwise noted)

Parameter		Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage		V_{DS}	30	-30	V
Gate-Source Voltage		V_{GS}	±20	±20	V
Continuous Drain Current (Note a)	$T_A=25^\circ C$	I_D	6.9	-6.3	A
	$T_A=70^\circ C$		5.5	-5.0	
Pulsed Drain Current (Note b)		I_{DM}	20	-20	A
Maximum Power Dissipation	$T_A=25^\circ C$	P_D	2.0	2.0	W
	$T_A=70^\circ C$		1.35	1.35	
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 To 150	-55 To 150	°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient (Note2)	$R_{\theta JA}$	N-Ch	62.5	°C/W
		P-Ch	62.5	

Note :

- a. These tests are performed with infinite heat sink.
- b. Pulse width by Max. junction temperature.



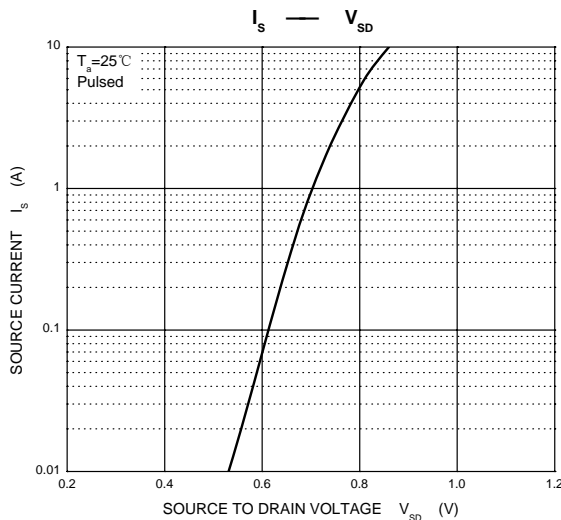
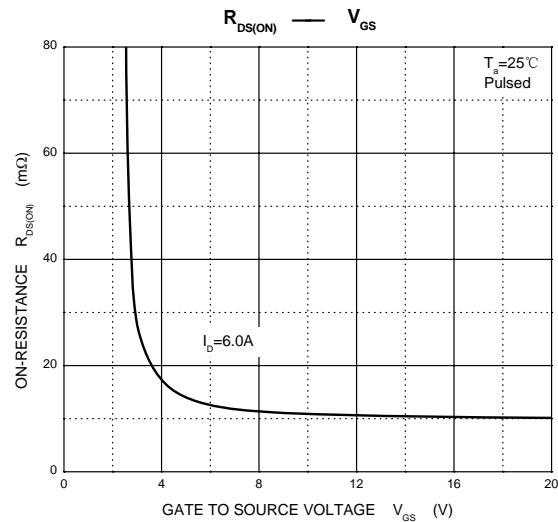
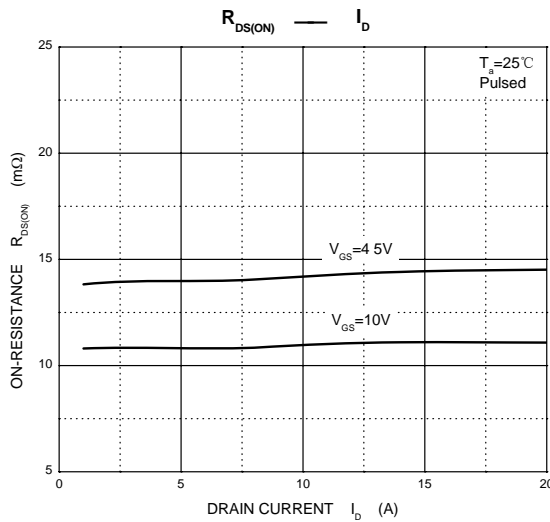
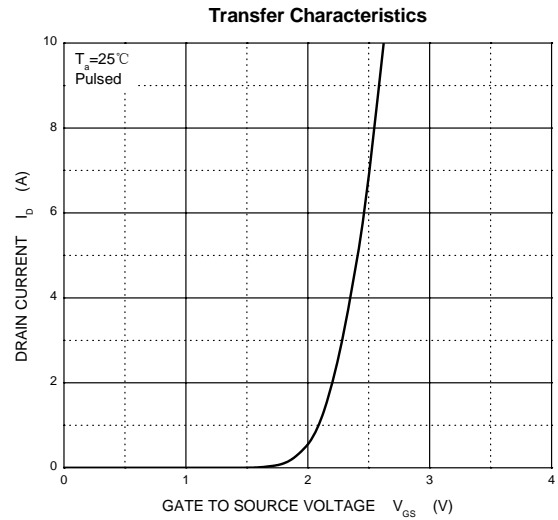
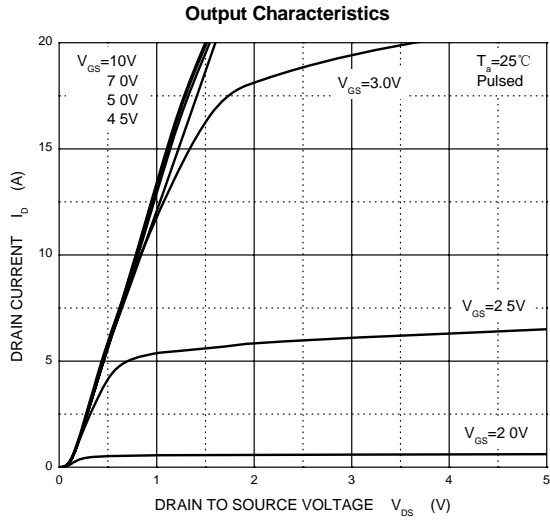
FTK4503

ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	N-Ch	30		V	
		V _{GS} =0V, I _D =-250μA	P-Ch	-30			
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	N-Ch		1	μA	
		V _{DS} =-30V, V _{GS} =0V	P-Ch		-1		
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	N-Ch		±100	nA	
			P-Ch		±100		
ON CHARACTERISTICS (Note 3)							
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	N-Ch	1	1.9	3	V
		V _{DS} =V _{GS} , I _D =-250μA	P-Ch	-1	-1.8	-3	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =6.0A	N-Ch		22	28	mΩ
		V _{GS} =-10V, I _D =-6.0A	P-Ch		32	36	
		V _{GS} =4.5V, I _D =4A	N-Ch		38	42	
		V _{GS} =-4.5V, I _D =-4A	P-Ch		50	55	
Forward Transconductance	g _{FS}	V _{DS} =10V, I _D =6.0A	N-Ch	4		S	
		V _{DS} =-10V, I _D =-6.0A	P-Ch	4			
DYNAMIC PARAMETERS							
Input Capacitance	C _{ISS}	N-Ch V _{GS} =0V, V _{DS} =25V, f=1MHz P-Ch V _{GS} =0V, V _{DS} =-25V, f=1MHz	N-Ch			770	pF
			P-Ch			1380	
Output Capacitance	C _{OSS}		N-Ch			80	
			P-Ch			150	
Reverse Transfer Capacitance	C _{RSS}		N-Ch			75	
			P-Ch			140	
SWITCHING CHARACTERISTICS (Note 4)							
Turn-on Delay Time	t _{d(on)}	N-Ch V _{DD} =20V, R _L =20Ω V _{GEN} =10V, R _{GEN} =3Ω P-Ch V _{DD} =-15V, R _L =15Ω V _{GEN} =-10V, R _{GEN} =3Ω	N-Ch		5.0	nS	
			P-Ch		8.0		
Turn-on Rise Time	t _r		N-Ch		8.0	nS	
			P-Ch		7.0		
Turn-Off Delay Time	t _{d(off)}		N-Ch		18.5	nS	
			P-Ch		34		
Turn-Off Fall Time	t _f		N-Ch		9.0	nS	
			P-Ch		26		
Total Gate Charge	Q _g	N-Ch V _{DS} =24V, I _D =6.0A, V _{GS} =4.5V P-Ch V _{DS} =-24V, I _D =-6A, V _{GS} =-4.5V	N-Ch		13.5	nC	
			P-Ch		20		
Gate-Source Charge	Q _{gs}		N-Ch		1.4	nC	
			P-Ch		2		
Gate-Drain Charge	Q _{gd}		N-Ch		4.7	nC	
			P-Ch		7		
DRAIN-SOURCE DIODE CHARACTERISTICS							
Diode Forward Voltage	V _{SD}		V _{GS} =0V, I _S =1.7A	N-Ch		1.2	V
		V _{GS} =0V, I _S =-1.7A	P-Ch		-1.2	V	



Typical Characteristics N-Ch



Typical Characteristics P-Ch

