

## **CYStech Electronics Corp.**

Spec. No. : C760 Issued Date : 2014.12.04

Revised Date : Page No. : 1/1

## **Glass passivated Single Phase Bridge Rectifiers**

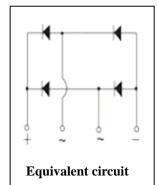
# GBL4A thru GBL4M

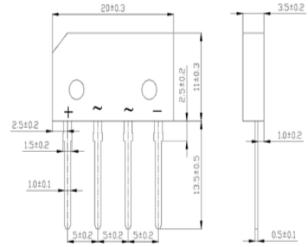
Reverse Voltage 50 to 1000V Forward Current 4 Amps



#### **Features**

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Ideal for printed circuit boards
- Glass passivated chip junction
- High surge current capability





Package outline dimensions in millimeters

#### **Mechanical Data**

- Case: GBL(2S) Molded plastic body
- Terminals: Matte tin plated leads, solderable per MIL-STD-750, method 2026
- High temperature soldering guaranteed:
  260°C/10 seconds, 0.375"(9.5mm) lead length, 5lbs(2.3kg) tension
- Mounting position: Any.
- Polarity : shown on front side of case, positive lead by beveled corner.
- Weight: 2.1 gram, 0.074 oz.

## **Maximum Ratings and Electrical Characteristics**

(Rating at 25°C ambient temperature unless otherwise specified.)

Parameter	Symbol	Туре							T I 14
		GBL4A	GBL4B	GBL4D	GBL4G	GBL4J	GBL4K	GBL4M	Units
Repetitive peak reverse voltage	Vrrm	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V <sub>RMS</sub>	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	$V_{DC}$	50	100	200	400	600	800	1000	V
Maximum instantaneous forward voltage drop per leg, IF=1A	$V_{\mathrm{F}}$	1							V
Maximum average forward $T_{C}=50^{\circ}C$ rectified output current at $T_{A}=40^{\circ}C$	I <sub>F</sub> (AV)	4.0 (Note 1) 3.0 (Note 2)							A
Peak forward surge current @ 8.3ms single half sine wave superimposed on rated load (JEDEC method)	Ifsm	150							A
Rating for fusing (t<8.3ms)	I <sup>2</sup> t	93							A <sup>2</sup> s
	$I_R$	10 500							μΑ
Typical thermal resistance per leg	$R_{ heta JA} \ R_{ heta JL}$	32 (Note 2) 3.5 (Note 1)						°C /W	
Storage temperature range	Tstg	-55 ~ +150							°C
Operating junction temperature range	TJ	-55 ∼ +150						°C	

Notes: 1.Unit mounted on 3.0"x3.0"x0.11" thick (7.5 cmx7.5cmx0.3cm) Al plate

2.Unit mounted on PCB at 0.375"(9.5mm) lead length and 0.5"x0.5"(13mmx13mm) copper pads.



# **CYStech Electronics Corp.**

Spec. No. : C760 Issued Date : 2014.12.04 Revised Date :

Revised Date : Page No. : 2/2

### **Typical Characteristics**

Ratings and Characteristics Curves (TA = 25°C unless otherwise noted)

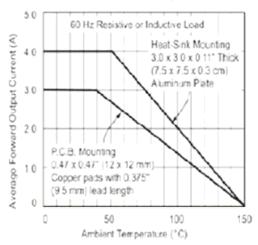


Figure 1. Derating Curves Output Rectified Current

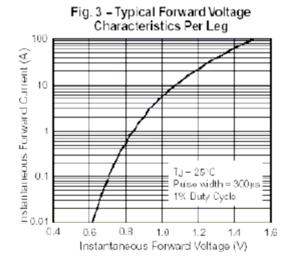
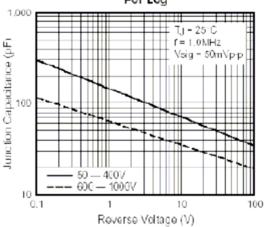


Fig. 5 -- Typical Junction Capacitance Por Log



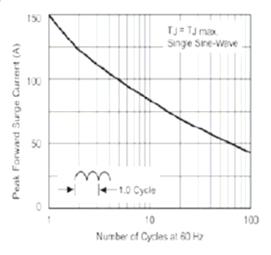


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current Per Leg



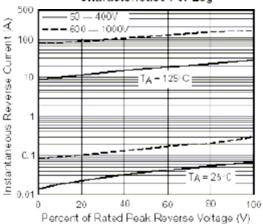


Fig. 6 -- Typical Transient Thermal Impedance Per Leg

