

**10Amp. Schottky Barrier Rectifiers**

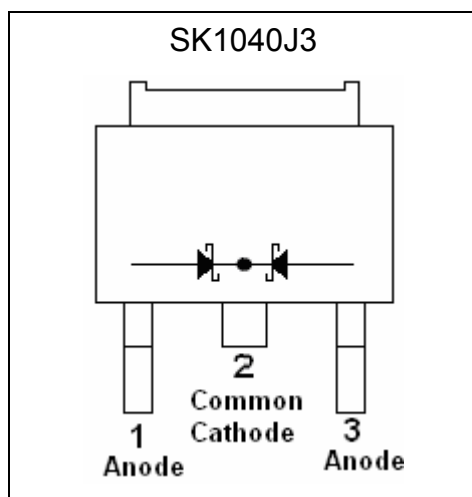
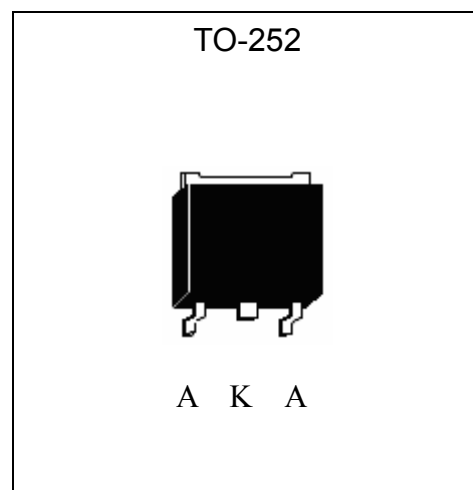
# SK1040J3

**Features**

- Excellent power dissipation offers better reverse leakage current and thermal resistance
- Low profile surface mount application in order to optimize board space
- Metal silicon junction, major carrier conduction
- 10A total (5A per diode leg)
- Guardring for over voltage protection
- Low power loss, high efficiency
- High surge capability
- High current capability, low forward voltage drop
- Ultra-high speed switching
- Lead-free parts meet environmental standard MIL-STD-19500 /228

**Mechanical Data**

- Case: Molded plastic, TO-252
- Mounting Position: Any
- Weight: 0.34 grams approximately
- Terminals: Pure tin plated, lead-free, solderable per MIL-STD-750 method 2026
- Epoxy: UL 94V-0 rate flame retardant
- Polarity : Indicated by diode symbol

**Equivalent Circuit****Outline**

## Maximum Ratings and Electrical Characteristics (Per Diode Leg)

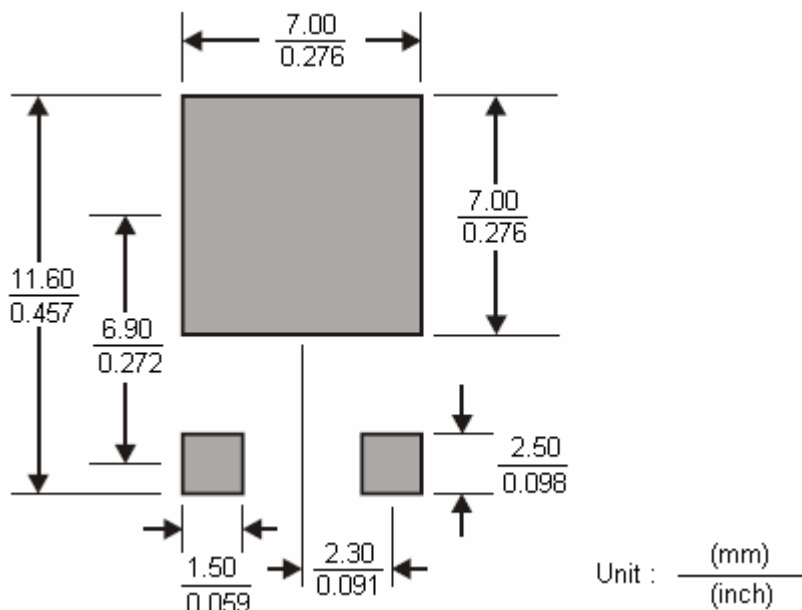
(Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.)

Parameter	Symbol	Limits	Units
Maximum Recurrent peak reverse voltage	V <sub>RRM</sub>	40	V
Maximum RMS voltage	V <sub>RMS</sub>	28	V
Maximum DC blocking voltage	V <sub>DC</sub>	40	V
Maximum instantaneous forward voltage at I <sub>F</sub> =5A	V <sub>F</sub>	0.55	V
Maximum Average forward rectified current	Per Diode	5	A
	Per Device	10	
Non-repetitive peak forward surge current @8.3ms single half sine wave superimposed on rated load (JEDEC method)	I <sub>FSM</sub>	100	A
Maximum instantaneous reverse current at V <sub>R</sub> =40V, T <sub>A</sub> = 25°C V <sub>R</sub> =40V, T <sub>A</sub> = 100°C	I <sub>R</sub>	0.5	mA
		20	
Maximum Thermal Resistance, Junction-to-case	R <sub>θJC</sub>	3	°C/W
Maximum Thermal Resistance, Junction-to-ambient	R <sub>θJA</sub>	125	°C/W
Storage temperature range	T <sub>stg</sub>	-65 ~ +175	°C
Operating junction temperature range	T <sub>J</sub>	-55 ~ +125	°C

## Ordering Information

Device	Package	Shipping	Marking
SK1040J3	TO-252 (Pb-free lead plating package)	2500 pcs / Tape & Reel	SK1040

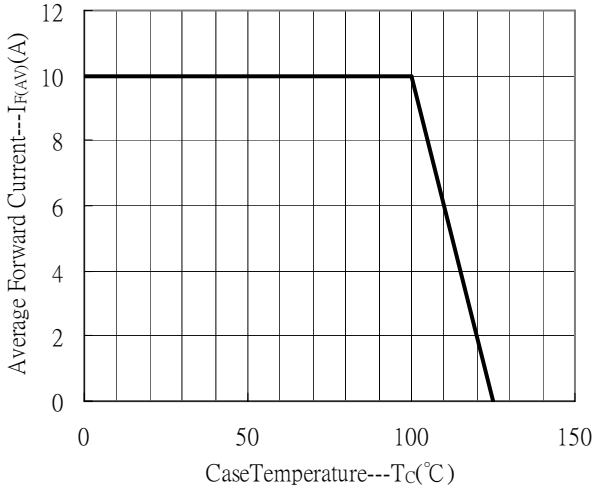
## Recommended soldering footprint



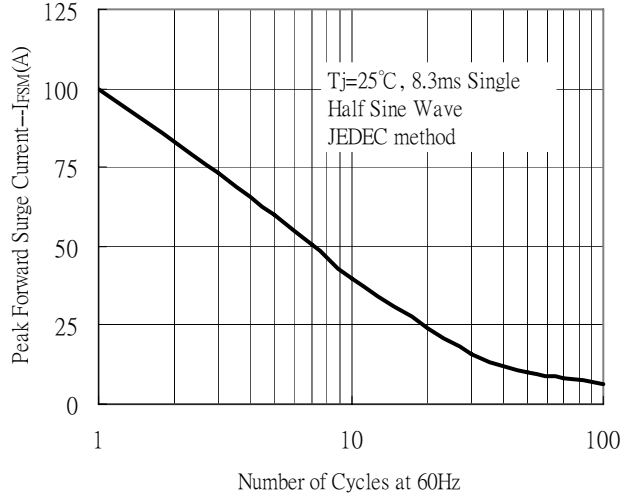


### Characteristic Curves

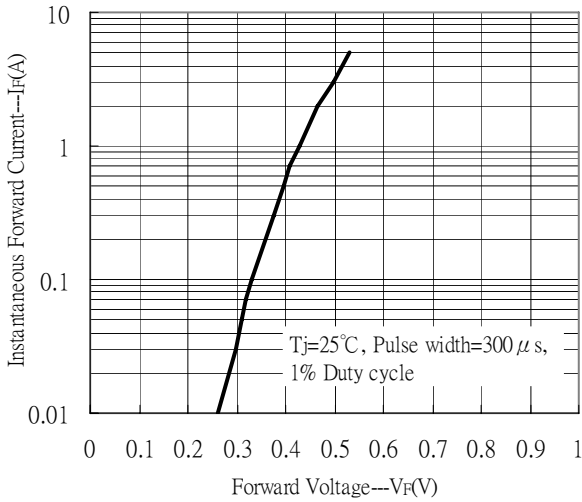
Forward Current Derating Curve



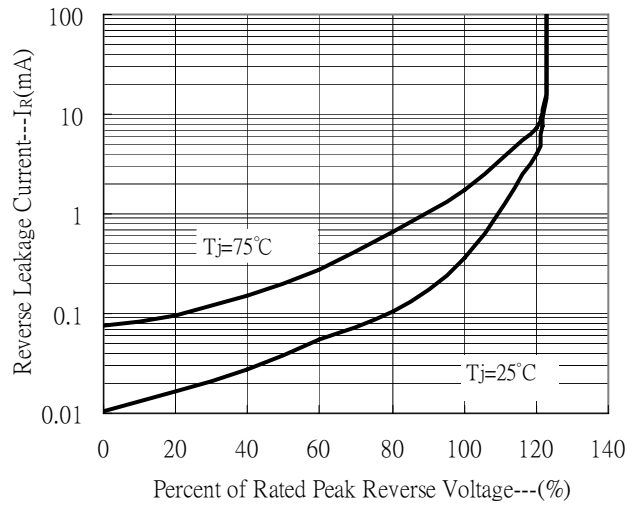
Maximum Non-Repetitive Forward Surge Current



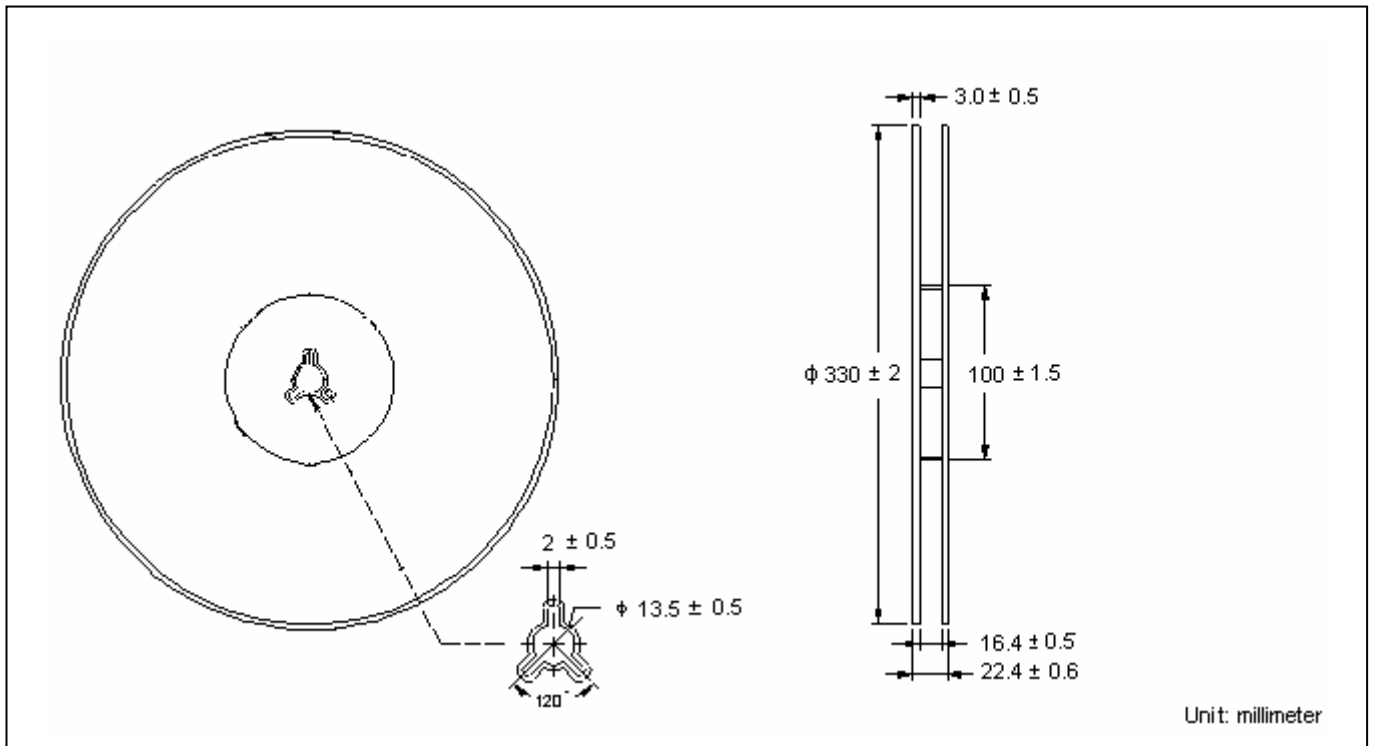
Forward Current vs Forward Voltage



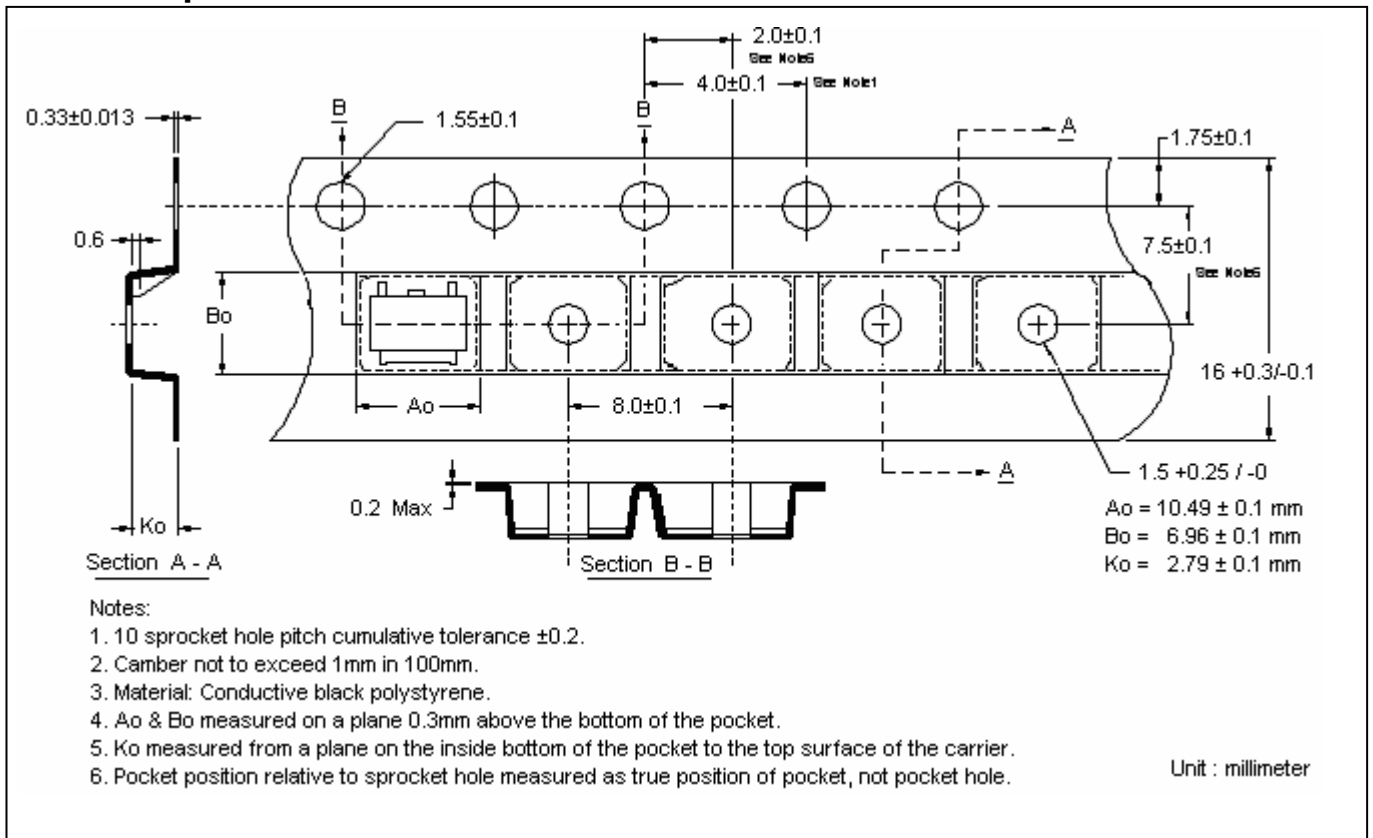
Reverse Leakage Current vs Reverse Voltage



**Reel Dimension**



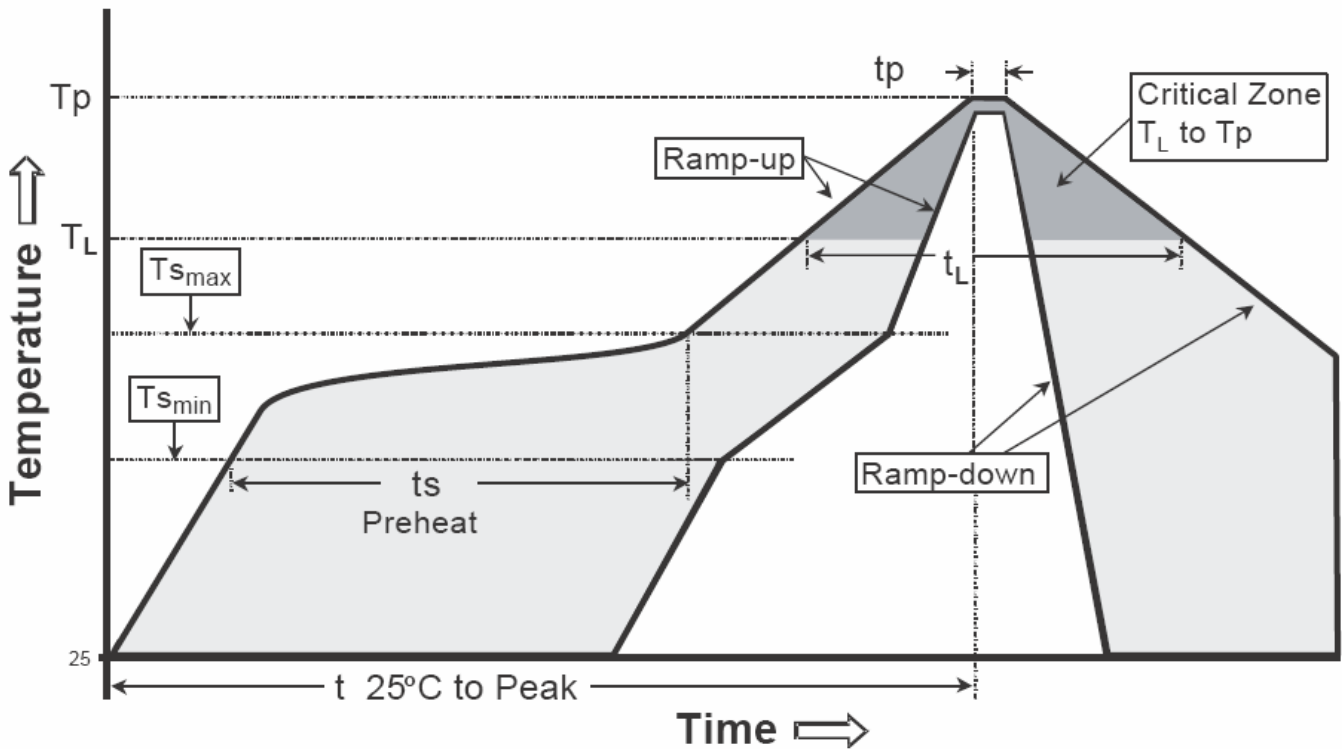
**Carrier Tape Dimension**



**Recommended wave soldering condition**

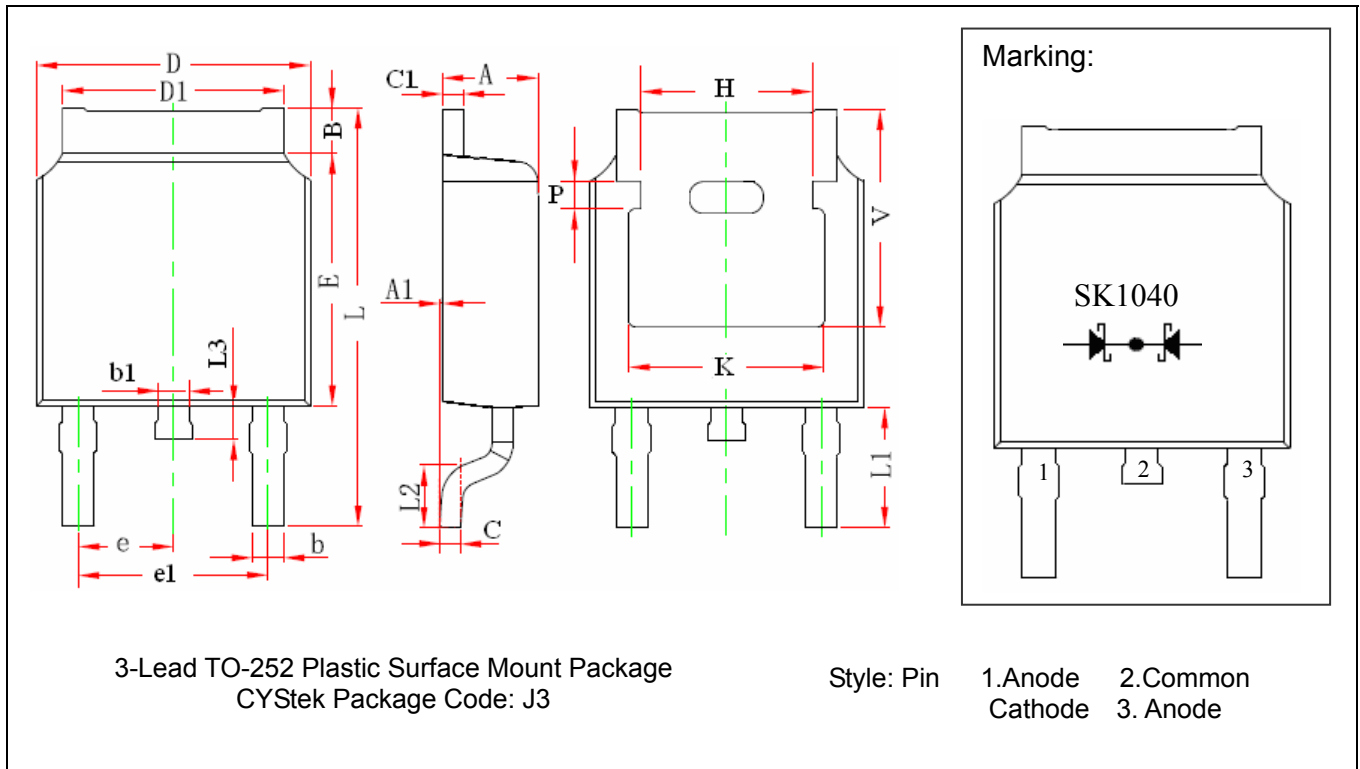
Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

**Recommended temperature profile for IR reflow**



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T <sub>s min</sub> )	100°C	150°C
-Temperature Max(T <sub>s max</sub> )	150°C	200°C
-Time(t <sub>s min</sub> to t <sub>s max</sub> )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60-150 seconds	60-150 seconds
Peak Temperature(T <sub>p</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

## TO-252 Dimension



3-Lead TO-252 Plastic Surface Mount Package  
 CYStek Package Code: J3

Style: Pin    1.Anode    2.Common  
                  Cathode    3. Anode

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.087	0.094	2.200	2.400	e	0.086	0.094	2.186	2.386
A1	0.000	0.005	0.000	0.127	e1	0.172	0.188	4.372	4.772
B	0.039	0.048	0.990	1.210	H	0.163	REF	4.140	REF
b	0.026	0.034	0.660	0.860	K	0.190	REF	4.830	REF
b1	0.026	0.034	0.660	0.860	L	0.386	0.409	9.800	10.400
C	0.018	0.023	0.460	0.580	L1	0.114	REF	2.900	REF
C1	0.018	0.023	0.460	0.580	L2	0.055	0.067	1.400	1.700
D	0.256	0.264	6.500	6.700	L3	0.024	0.039	0.600	1.000
D1	0.201	0.215	5.100	5.460	P	0.026	REF	0.650	REF
E	0.236	0.244	6.000	6.200	V	0.211	REF	5.350	REF

- Notes: 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead : Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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