

N -Channel Logic Level Enhancement Mode Power MOSFET

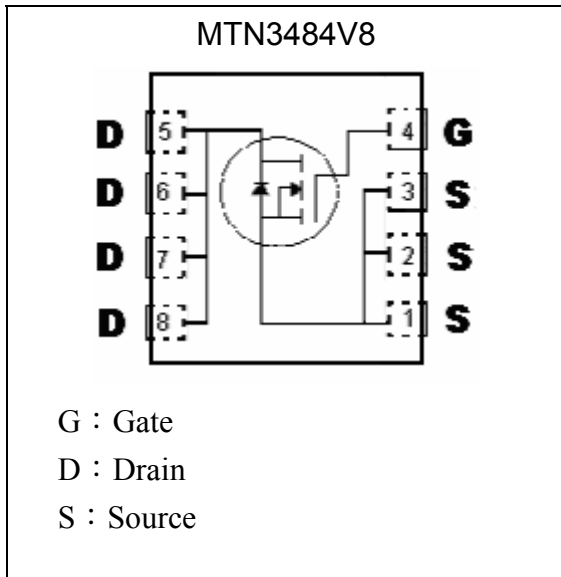
MTN3484V8

BV _{DSS}		100V
I _D		3.5A
R _{DSON(TYP)}	V _{GS} =10V, I _D =3.5A	92mΩ
	V _{GS} =4.5V, I _D =3.5A	97mΩ

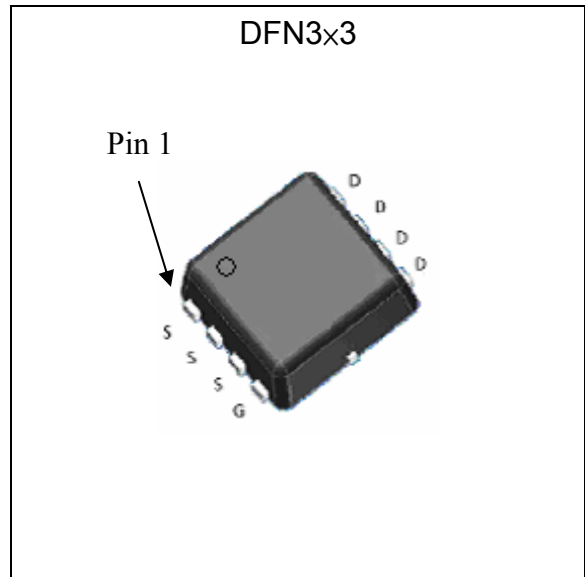
Features

- Low Gate Charge
- Simple Drive Requirement
- Pb-free lead plating package

Equivalent Circuit



Outline



Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _{GS}	±20	
Continuous Drain Current @ T _A =25°C, V _{GS} =10V	I _D	3.5	A
Continuous Drain Current @ T _A =100°C, V _{GS} =10V	I _D	2.2	
Pulsed Drain Current *1 & 2	I _{DM}	14	
Total Power Dissipation @ T _C =25°C	P _D	8	W
Total Power Dissipation @ T _A =25°C		2.5 *3	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150	°C

Note : *1. Pulse width limited by maximum junction temperature.
 *2. Duty cycle ≤ 1%.
 *3. Surface mounted on a 1 in² pad of FR-4board with 2oz copper, t≤10s.



Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{th,j-c}	16	°C/W
Thermal Resistance, Junction-to-ambient, max	R _{th,j-a}	50 *	°C/W

* Surface mounted on a 1 in² pad of FR-4board with 2oz copper, t_s≤10s.

Electrical Characteristics (T_j=25°C, unless otherwise noted)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
B _V DSS	100	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	1	1.9	3	V	V _{DS} =V _{GS} , I _D =250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	1	μA	V _{DS} =100V, V _{GS} =0V
	-	-	25		V _{DS} =80V, V _{GS} =0V, T _j =125°C
*R _D S(ON)	-	92	120	mΩ	V _{GS} =10V, I _D =3.5A
	-	97	125		V _{GS} =4.5V, I _D =3.5A
*G _{FS}	-	10	-	S	V _{DS} =5V, I _D =3.5A
Dynamic					
C _{iss}	-	1638	-	pF	V _{DS} =25V, V _{GS} =0, f=1MHz
C _{oss}	-	42	-		
C _{rss}	-	28	-		
t _{d(ON)}	-	17	-	ns	V _{DS} =50V, I _D =1A, V _{GS} =10V, R _G =6Ω
t _r	-	5	-	ns	
t _{d(OFF)}	-	45	-	ns	
t _f	-	9	-	ns	
Q _g	-	20	-	nC	V _{DS} =80V, I _D =3.5A, V _{GS} =10V,
Q _{gs}	-	5.9	-	nC	
Q _{gd}	-	6.4	-	nC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

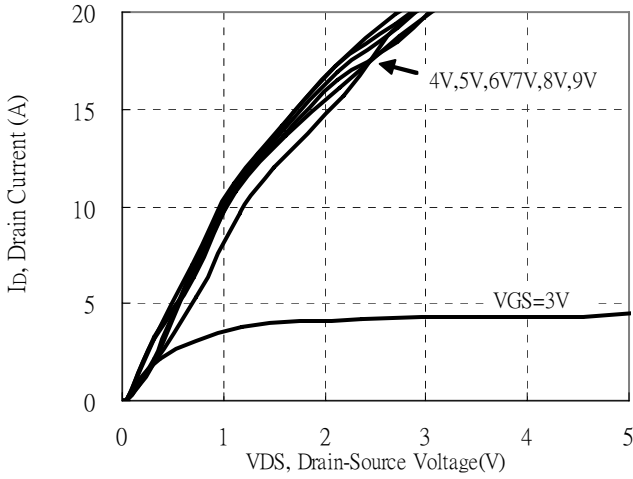
Source Drain Diode

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
*I _S	-	-	2.3	A	
*I _{SM}	-	-	9.2		
*V _{SD}	-	0.8	1.3	V	I _S =2.3A, V _{GS} =0V
*T _{rr}	-	15	-	ns	I _S =2.3A, V _{GS} =0V, dI/dt=100A/μs
Q _{rr}	-	30	-	nC	

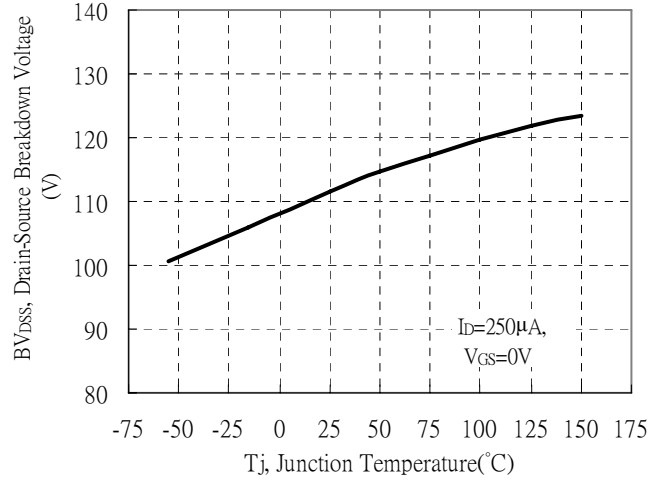
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Typical Characteristics

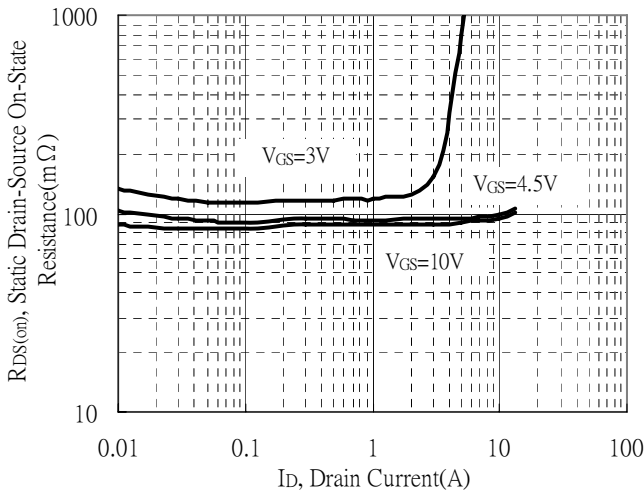
Typical Output Characteristics



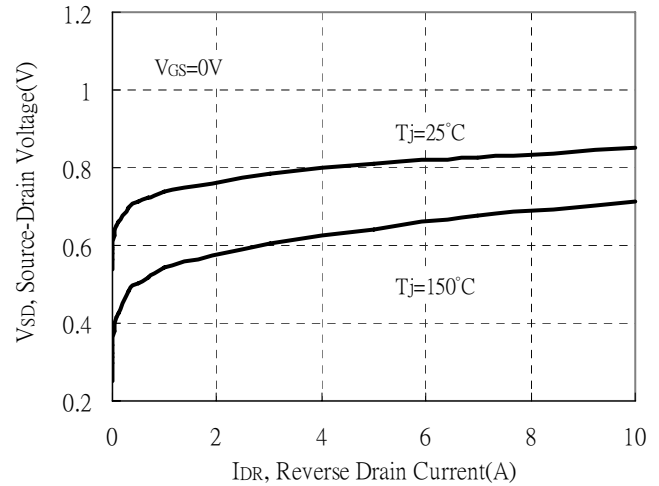
Brekdwn Voltage vs Ambient Temperature



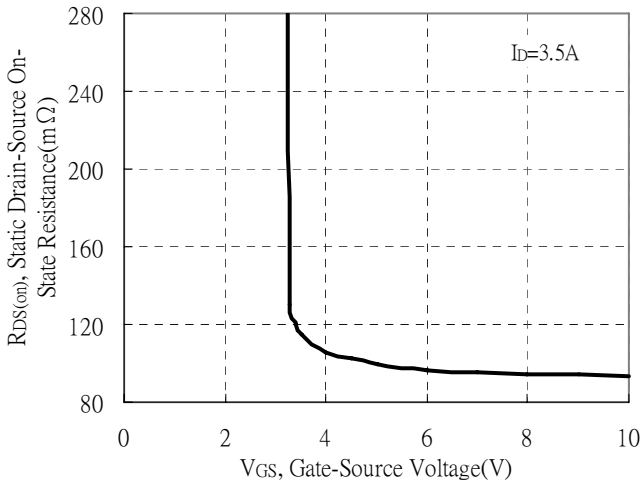
Static Drain-Source On-State resistance vs Drain Current



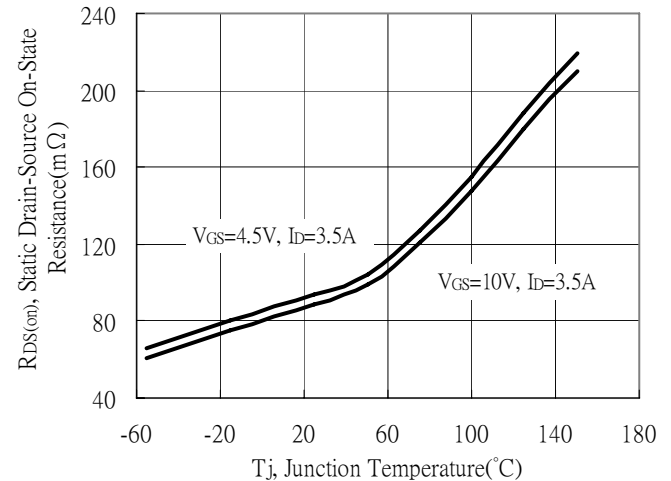
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

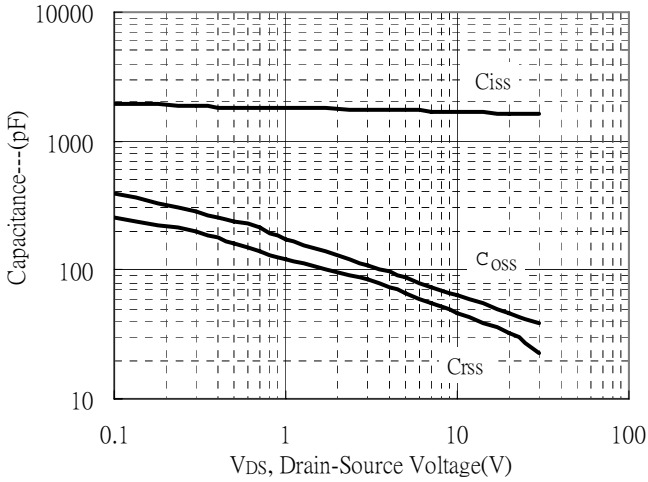


Drain-Source On-State Resistance vs Junction Temperature

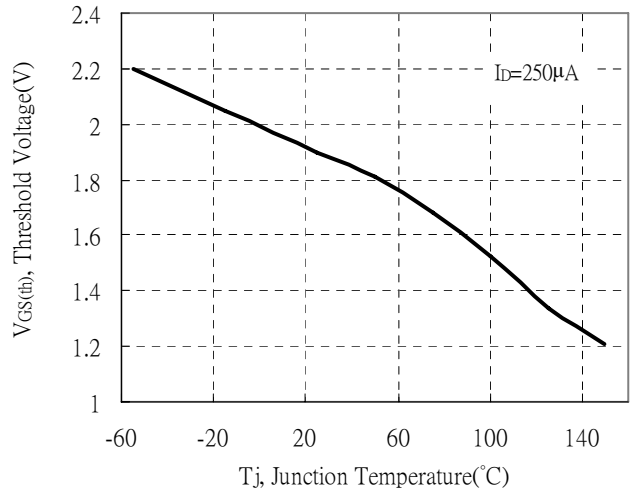


Typical Characteristics(Cont.)

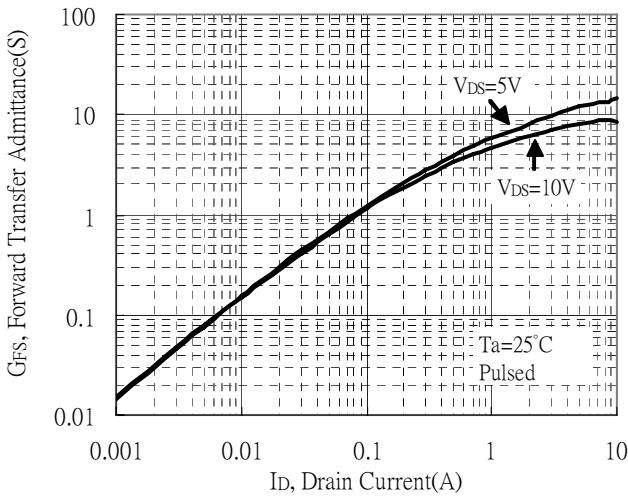
Capacitance vs Drain-to-Source Voltage



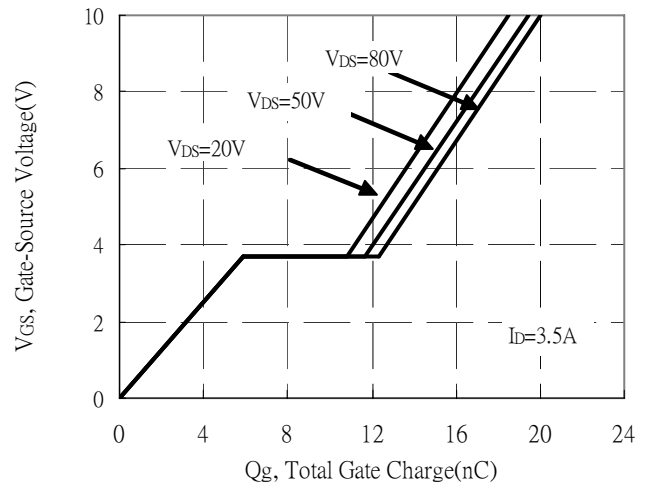
Threshold Voltage vs Junction Temperature



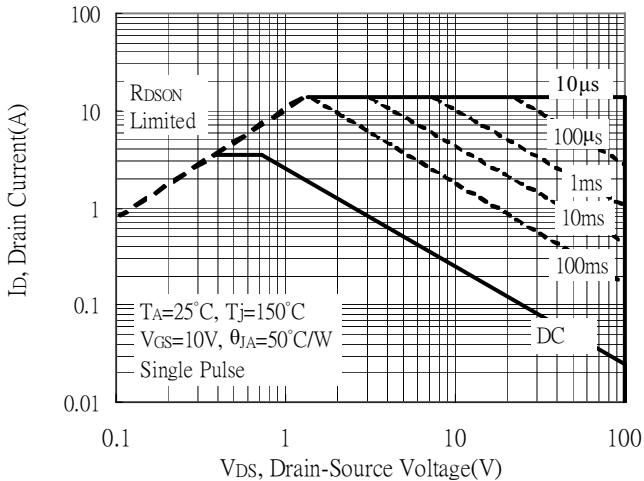
Forward Transfer Admittance vs Drain Current



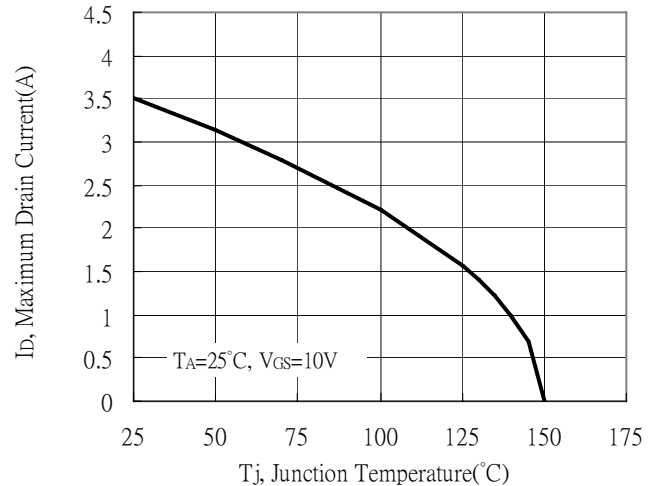
Gate Charge Characteristics



Maximum Safe Operating Area

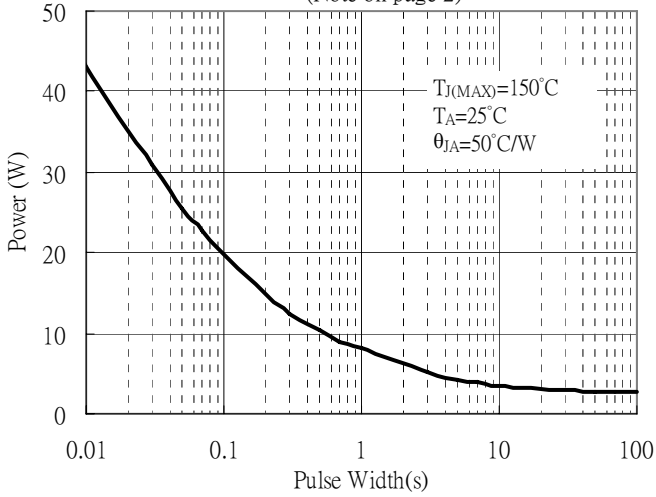


Maximum Drain Current vs Case Temperature

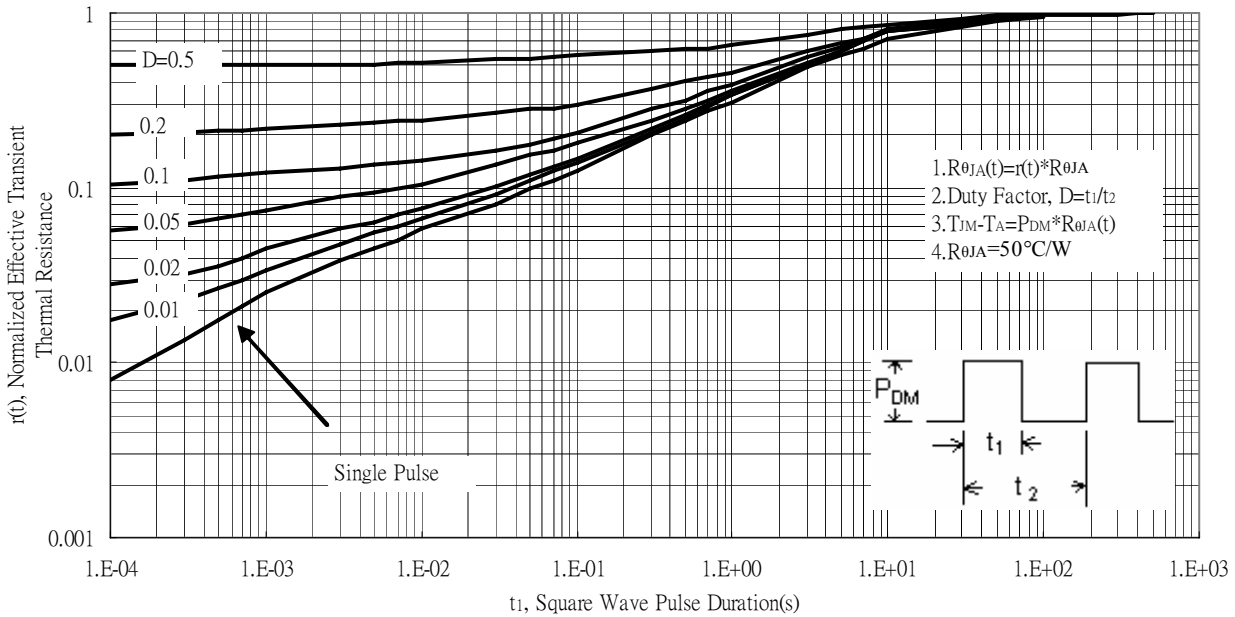


Typical Characteristics(Cont.)

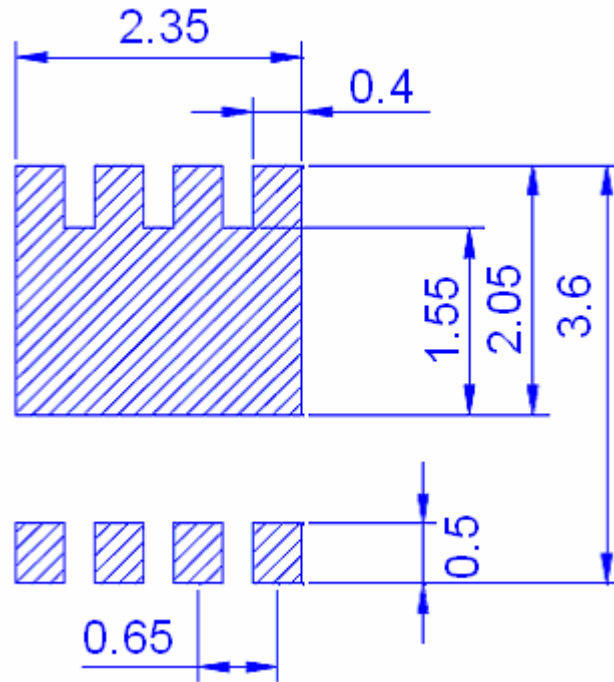
Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)



Transient Thermal Response Curves



Recommended Soldering Footprint

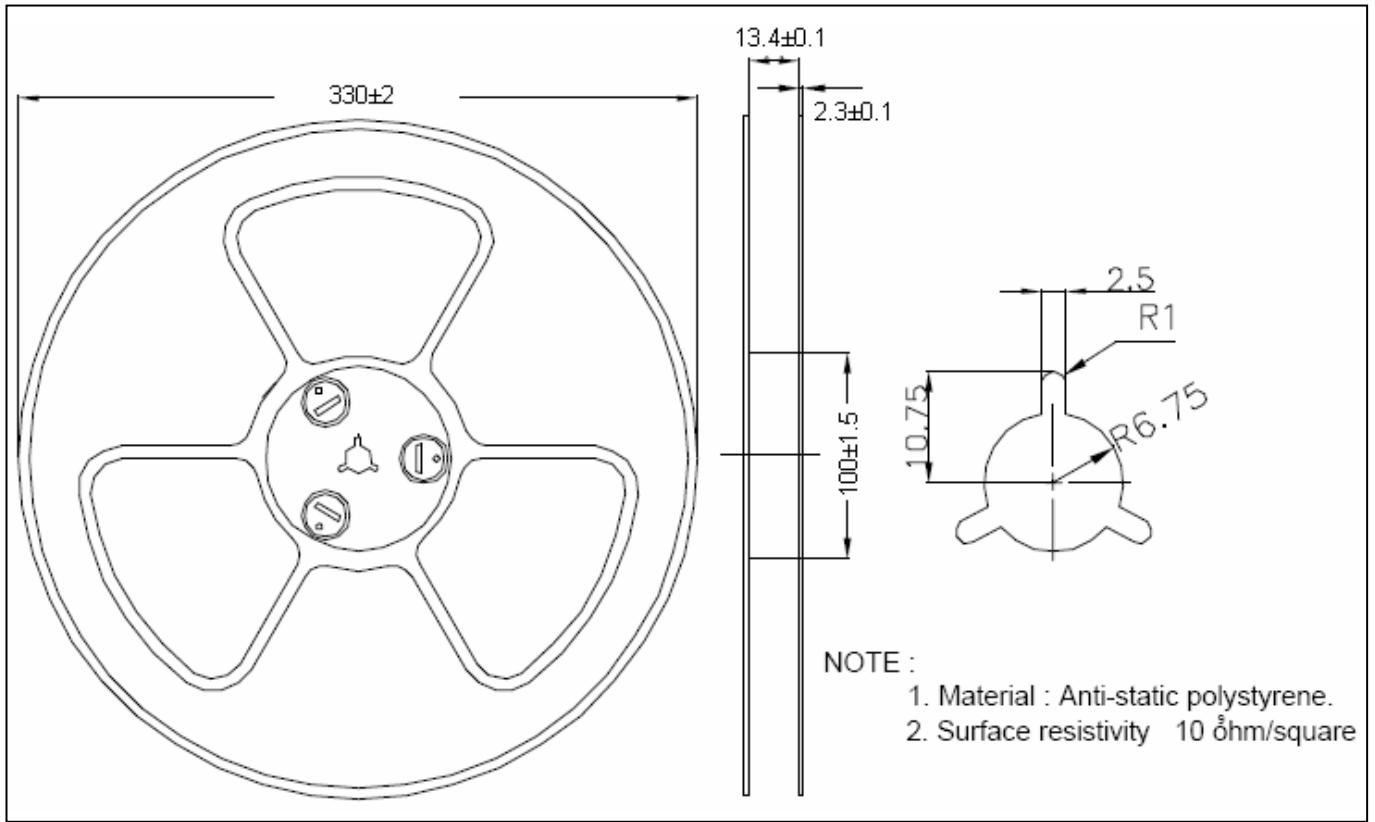


unit : mm

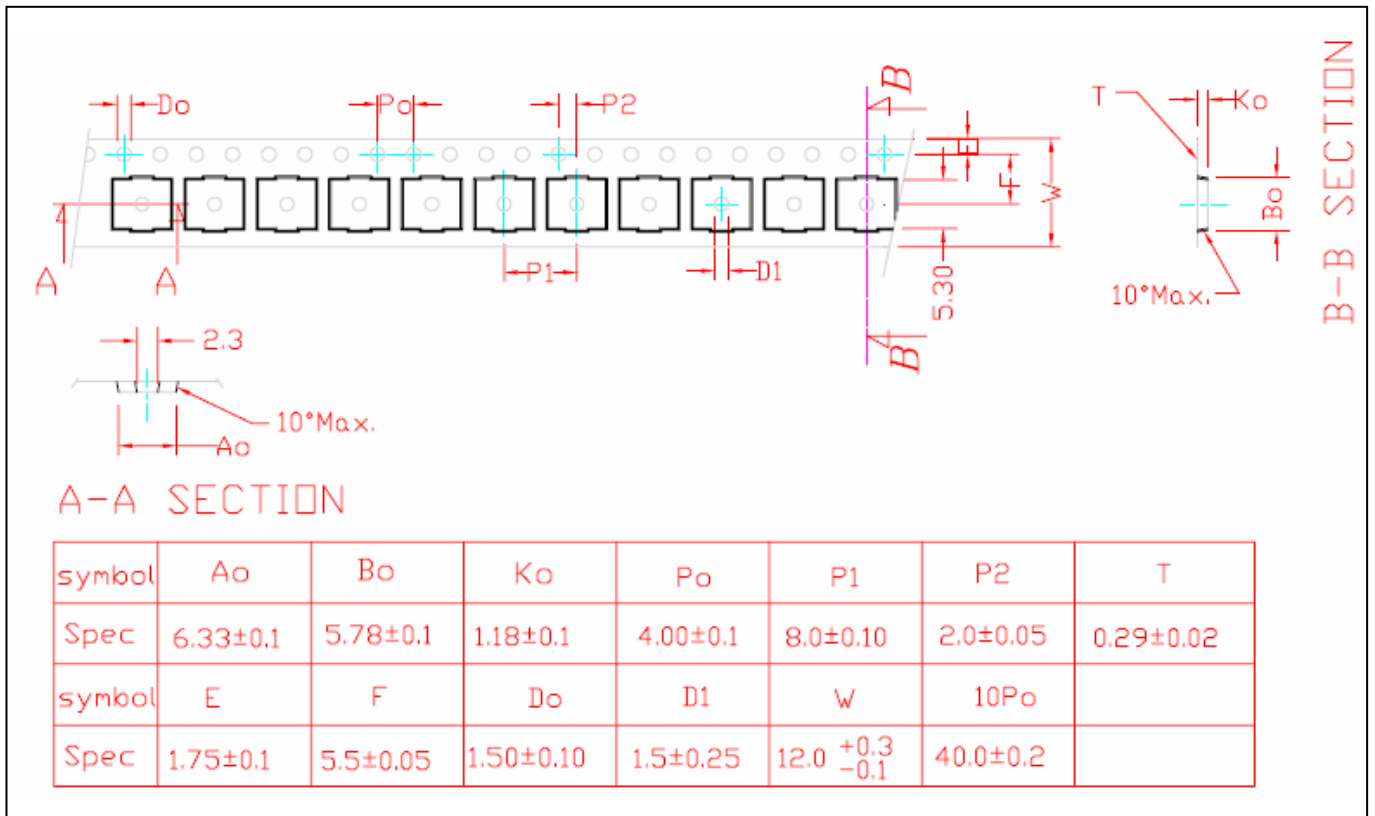
Ordering Information

Device	Package	Shipping	Marking
MTN3484V8	DFN3x3 (Pb-free lead plating package)	3000 pcs / Tape & Reel	3484

Reel Dimension



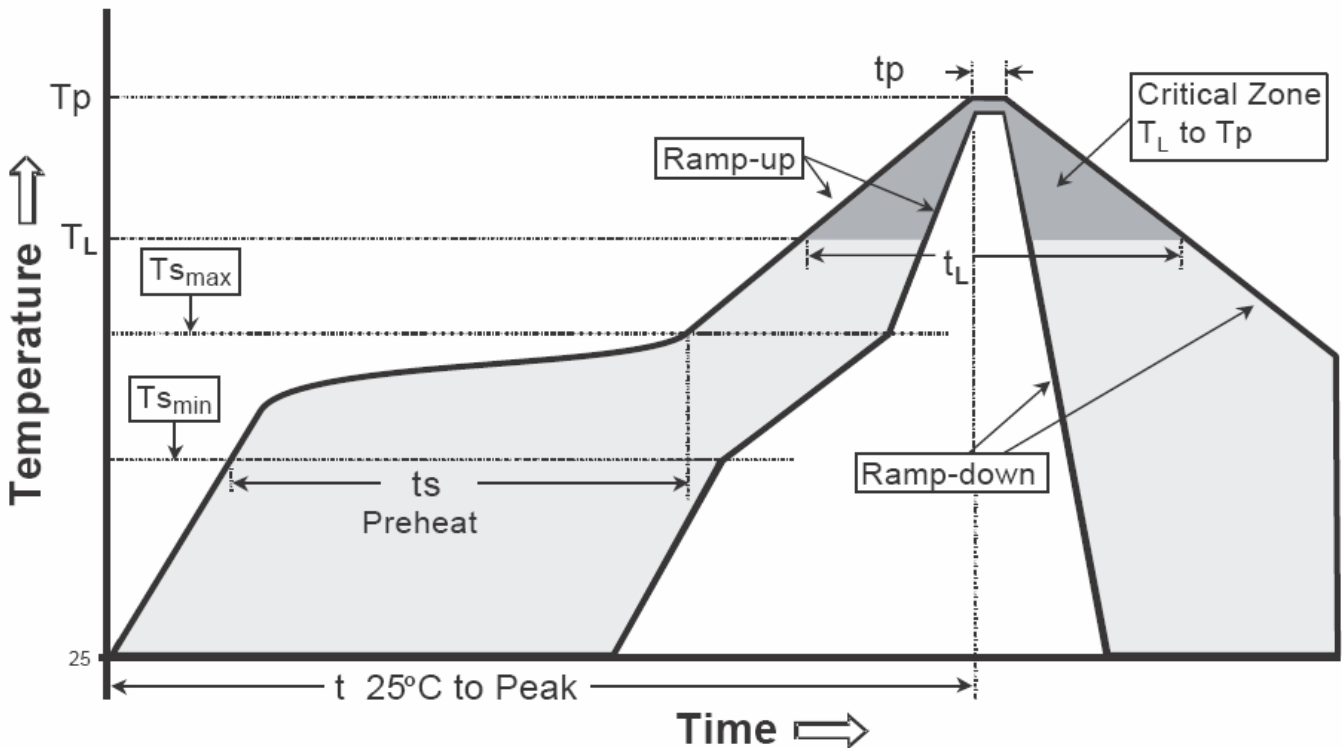
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

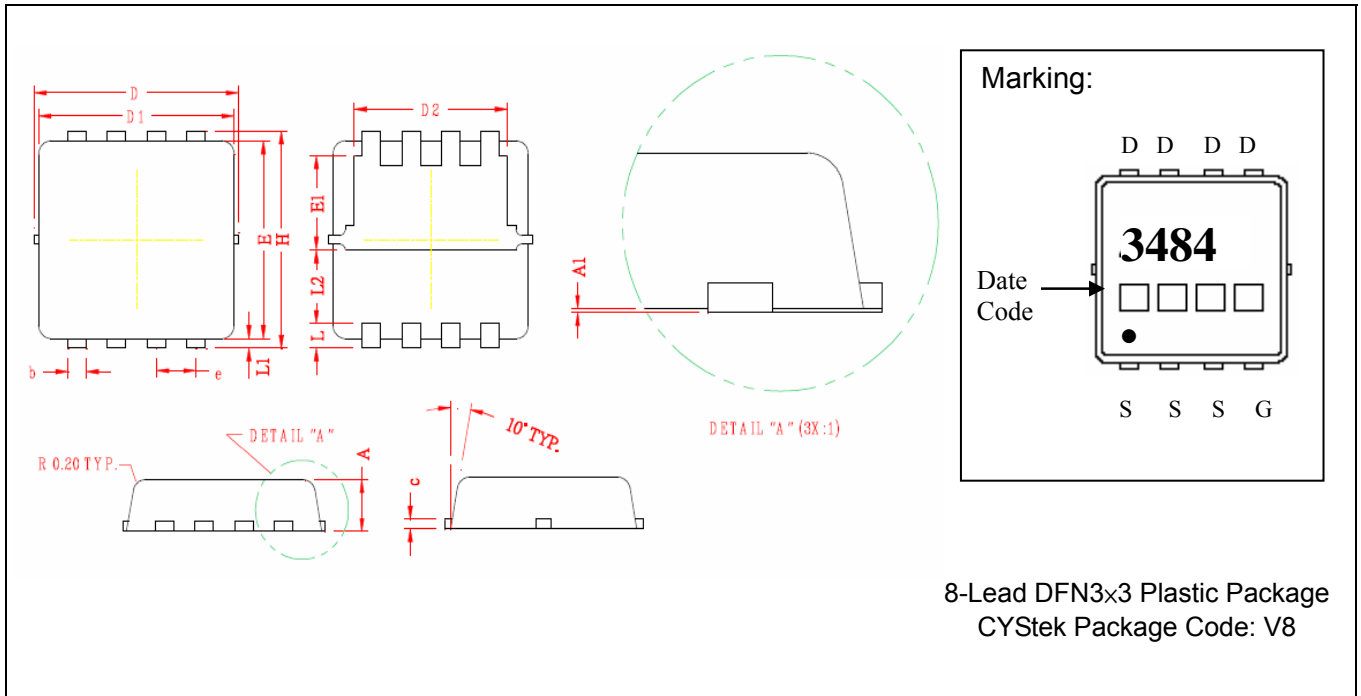
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

DFN3x3 Dimension



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.0276	0.0354	0.70	0.90	E	0.1181	0.1260	3.00	3.20
A1	0.0000	0.0197	0.00	0.50	E1	0.0531	0.0610	1.35	1.55
b	0.0094	0.0138	0.24	0.35	e	0.0256	BSC	0.65	BSC
c	0.0039	0.0079	0.10	0.20	H	0.1260	0.1339	3.20	3.40
D	0.1280	0.1339	3.25	3.40	L	0.0118	0.0197	0.30	0.50
D1	0.1201	0.1280	3.05	3.25	L1	0.0039	0.0079	0.10	0.20
D2	0.0945	0.1024	2.40	2.60	L2	0.0445	REF	1.13	REF

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.