

N- AND P-Channel Logic Level Enhancement Mode MOSFET

MTC2402Q8

	N-CH	P-CH
BV_{DSS}	20V	-20V
I_D	8.5A	-7.9A
$R_{DSON}(typ.) @V_{GS}=(-)4.5V$	20m Ω	25m Ω
$R_{DSON}(typ.) @V_{GS}=(-)2.5V$	28m Ω	35m Ω

Description

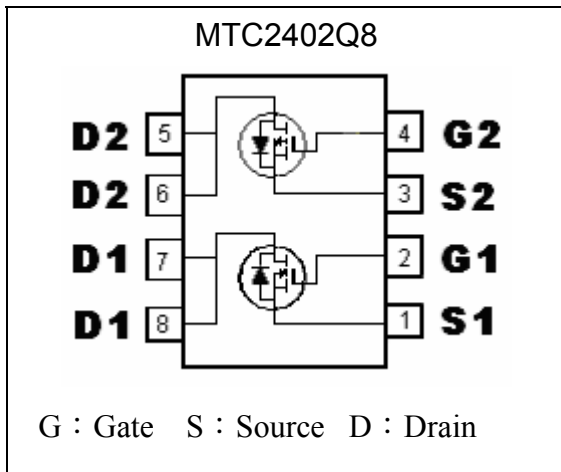
The MTC2402Q8 consists of a N-channel and a P-channel enhancement-mode MOSFET in a single SOP-8 package, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The SOP-8 package is universally preferred for all commercial-industrial surface mount applications.

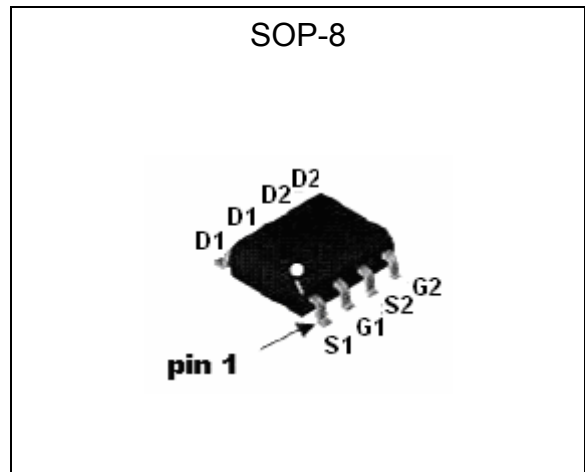
Features

- Simple drive requirement
- Low on-resistance
- Fast switching speed
- Pb-free lead plating and halogen-free package

Equivalent Circuit

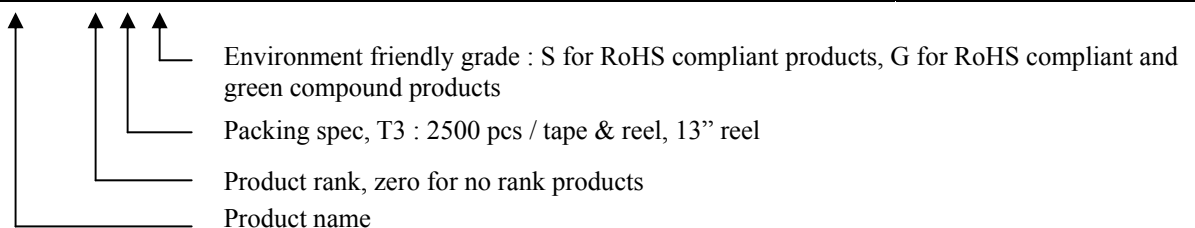


Outline



Ordering Information

Device	Package	Shipping
MTC2402Q8-0-T3-G	SOP-8 (Pb-free lead plating and halogen-free package)	2500 pcs / tape & reel





Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter	Symbol	Limits		Unit	
		N-channel	P-channel		
Drain-Source Breakdown Voltage	BV _{DSS}	20	-20	V	
Gate-Source Voltage	V _{GS}	±12	±12		
Continuous Drain Current (Note 2)	T _A =25 °C, V _{GS} =4.5V (-4.5V) T _A =70 °C, V _{GS} =4.5V (-4.5V)	I _D	8.5	-7.9	A
			6.8	-6.3	
Pulsed Drain Current (Note 1)	IDM	40	-30		
Power Dissipation (Note 2)	T _A =25°C	2.5		W	
	T _A =70°C	1.6			
Operating Junction and Storage Temperature Range	T _j ; T _{stg}	-55~+150		°C	

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{θJC}	25	°C/W
Thermal Resistance, Junction-to-ambient, max	R _{θJA}	50 (Note 2)	°C/W
Lead Temperature(1/16" from case, for 10 seconds)	T _L	275	°C

Note : 1.Pulse width limited by maximum junction temperature.
 2.Surface mounted on 1 in² copper pad of FR-4 board, pulse width≤10s.

N-Channel Electrical Characteristics (T_C=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	20	-	-	V	V _{GS} =0, I _D =250μA
V _{GS(th)}	0.5	0.7	1.2		V _{DS} =V _{GS} , I _D =250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±12V, V _{DS} =0
I _{DSS}	-	-	1	μA	V _{DS} =20V, V _{GS} =0
	-	-	25		V _{DS} =20V, V _{GS} =0, T _j =125°C
*R _{Ds(ON)}	-	20	24	mΩ	V _{GS} =4.5V, I _D =8A
	-	28	36		V _{GS} =2.5V, I _D =5.2A
*G _{FS}	-	8	-	S	V _{DS} =5V, I _D =3A
Dynamic					
C _{iss}	-	762	995	pF	V _{DS} =10V, V _{GS} =0, f=1MHz
C _{oss}	-	74	96		
C _{rss}	-	87	115		
*t _{d(ON)}	-	7.8	16	ns	V _{DS} =10V, I _D =1A, V _{GS} =4.5V, R _G =10Ω
*t _r	-	9.5	19		
*t _{d(OFF)}	-	37	74		
*t _f	-	23	46		
*Q _g	-	8	11	nC	V _{DS} =10V, I _D =5A, V _{GS} =4.5V
*Q _{gs}	-	0.9	1.2		
*Q _{gd}	-	2.6	3.4		



Body Diode					
*I _S	-	-	1.9	A	
*I _{SM}	-	-	7.6		
*V _{SD}	-	0.76	1.2	V	V _{GS} =0V, I _S =1A

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

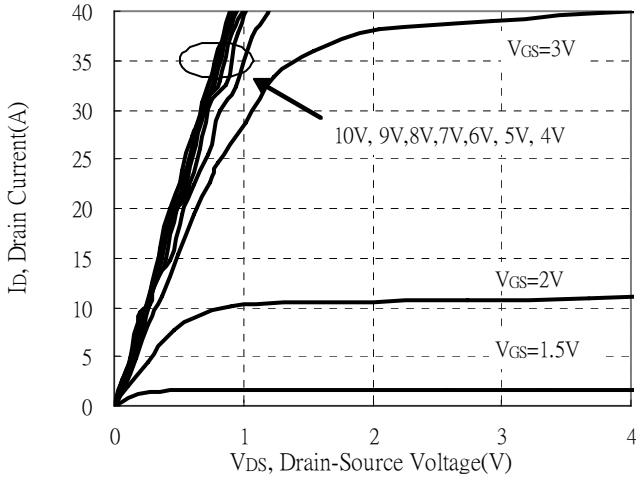
P-Channel Electrical Characteristics (T_c=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-20	-	-	V	V _{GS} =0, I _D =-250μA
V _{GS(th)}	-0.5	-0.7	-1.2		V _{DS} =V _{GS} , I _D =-250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±12V, V _{DS} =0
I _{DSS}	-	-	-1	μA	V _{DS} =-20V, V _{GS} =0
	-	-	-25		V _{DS} =-20V, V _{GS} =0, T _j =125°C
*R _{DS(ON)}	-	25	42	mΩ	V _{GS} =-4.5V, I _D =-5A
	-	35	52		V _{GS} =-2.5V, I _D =-4A
*G _{FS}	-	10	-	S	V _{DS} =-5V, I _D =-3A
Dynamic					
C _{iss}	-	1916	2490	pF	V _{DS} =-10V, V _{GS} =0, f=1MHz
C _{oss}	-	159	210		
C _{rss}	-	132	175		
*t _{d(ON)}	-	20	40	ns	V _{DS} =-10V, I _D =-1A, V _{GS} =-4.5V, R _G =10Ω
*t _r	-	29	58		
*t _{d(OFF)}	-	63	126		
*t _f	-	36	72		
*Q _g	-	14	19	nC	V _{DS} =-10V, I _D =-5A, V _{GS} =-4.5V
*Q _{gs}	-	2.2	2.9		
*Q _{gd}	-	3.9	5.1		
Body Diode					
*I _S	-	-	-1.9	A	
*I _{SM}	-	-	-7.6		
*V _{SD}	-	-0.73	-1.2	V	V _{GS} =0V, I _S =-6.8A

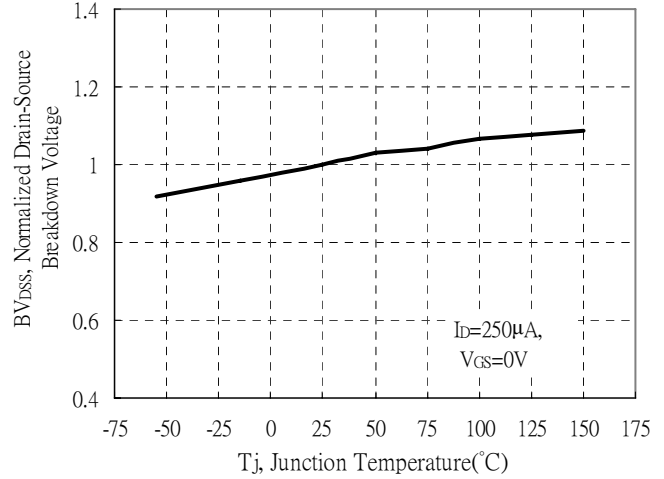
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Typical Characteristics : Q1(N-channel)

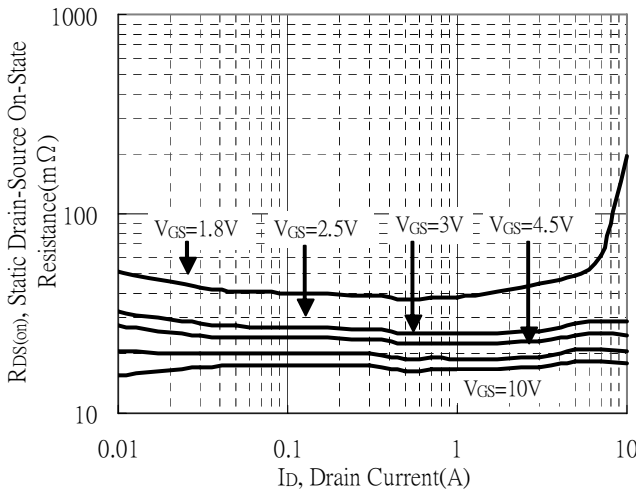
Typical Output Characteristics



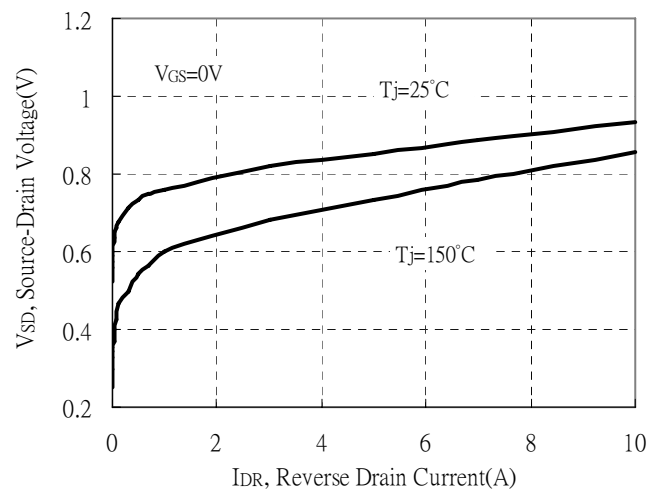
Brekdown Voltage vs Ambient Temperature



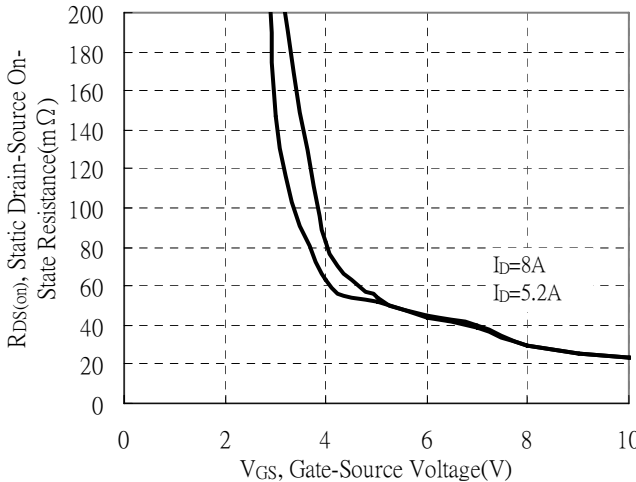
Static Drain-Source On-State resistance vs Drain Current



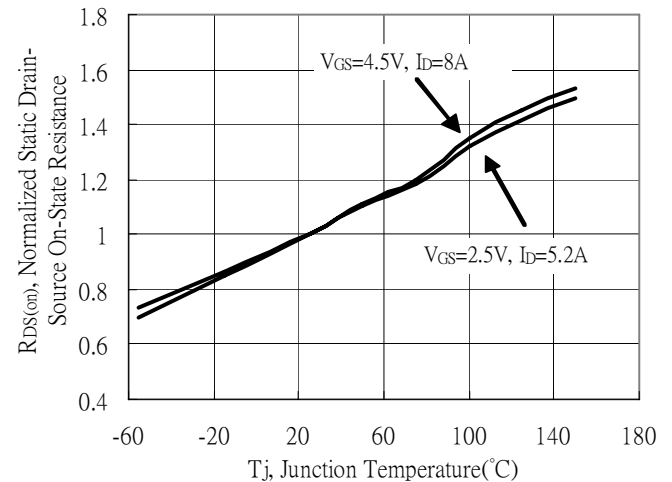
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

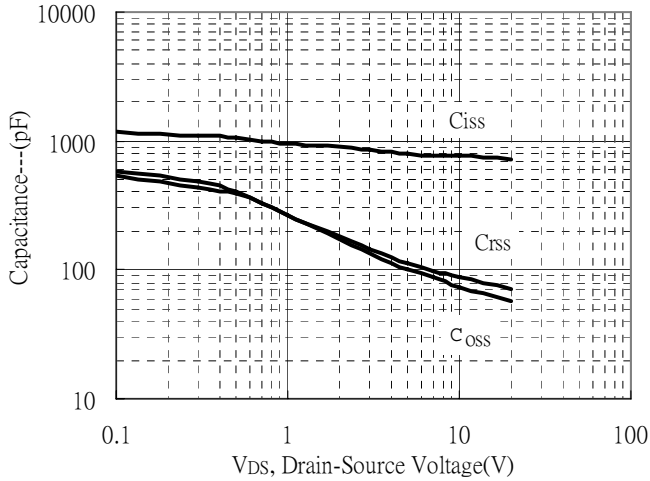


Drain-Source On-State Resistance vs Junction Temperature

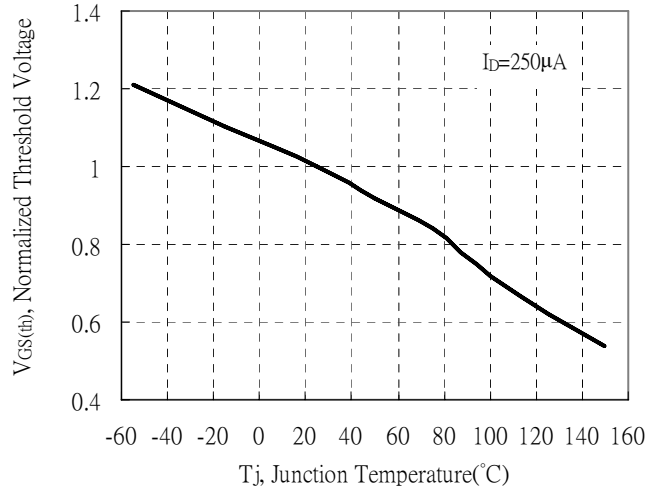


Typical Characteristics(Cont.) : Q1(N-channel)

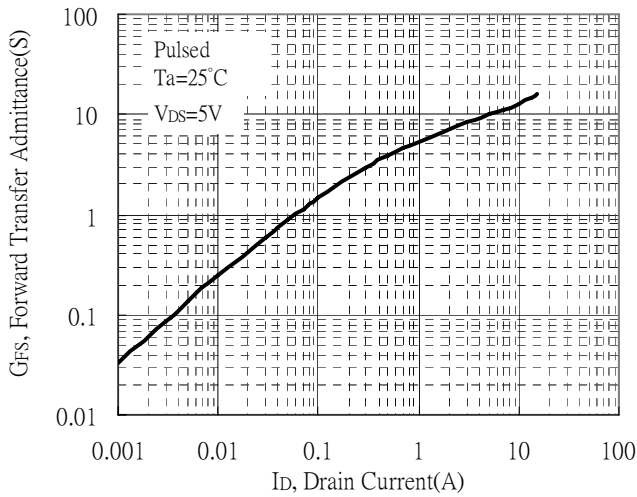
Capacitance vs Drain-to-Source Voltage



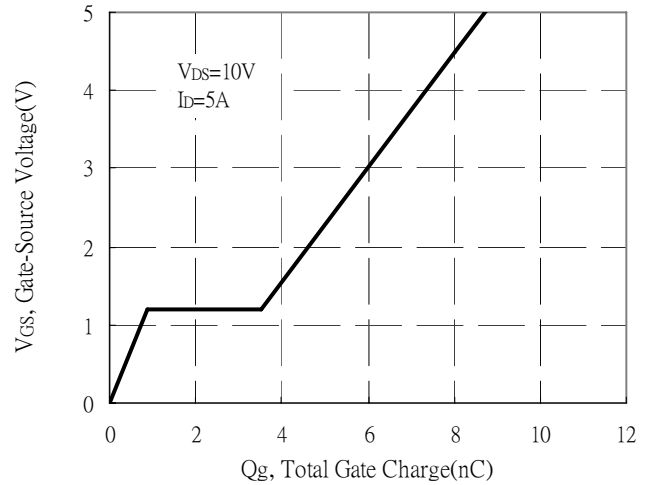
Threshold Voltage vs Junction Temperature



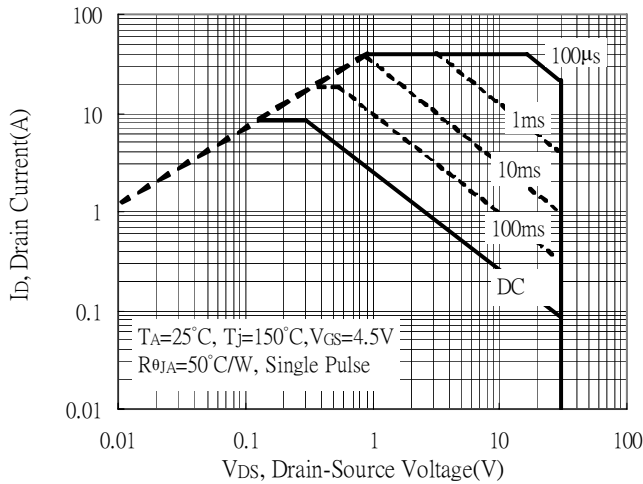
Forward Transfer Admittance vs Drain Current



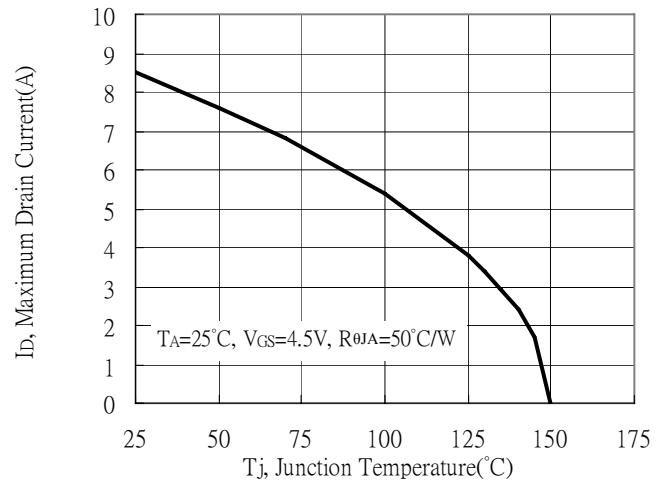
Gate Charge Characteristics



Maximum Safe Operating Area



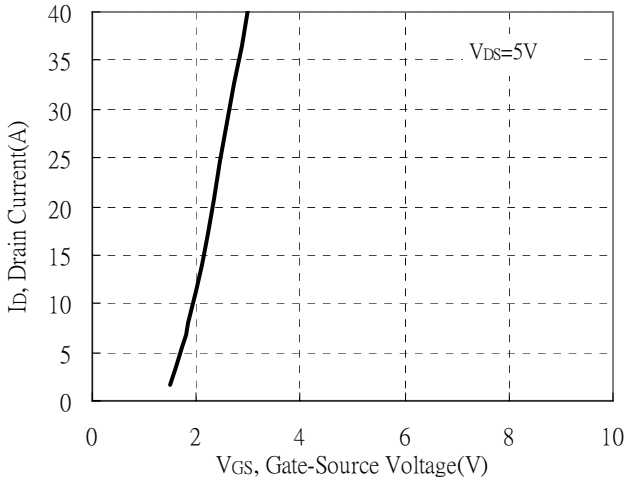
Maximum Drain Current vs Junction Temperature



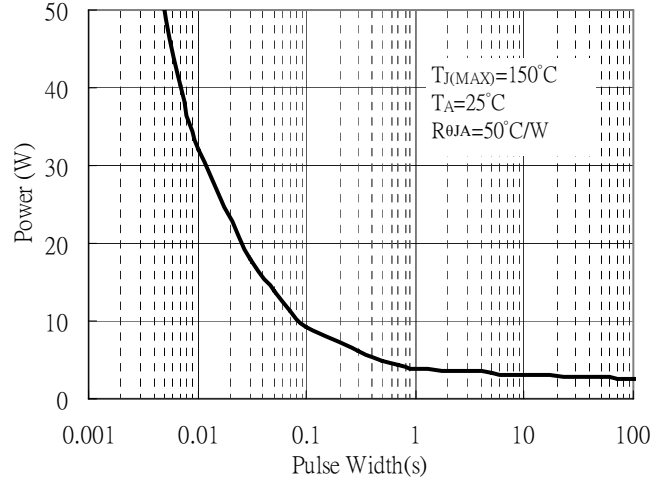


Typical Characteristics(Cont.) : Q1(N-channel)

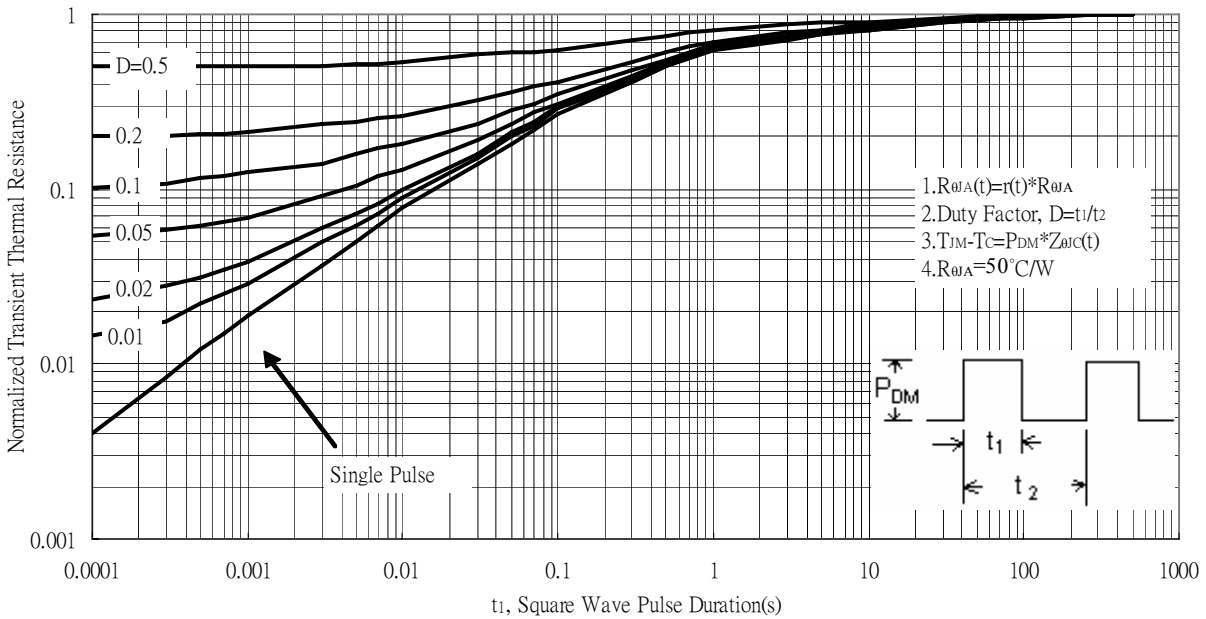
Typical Transfer Characteristics



Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)

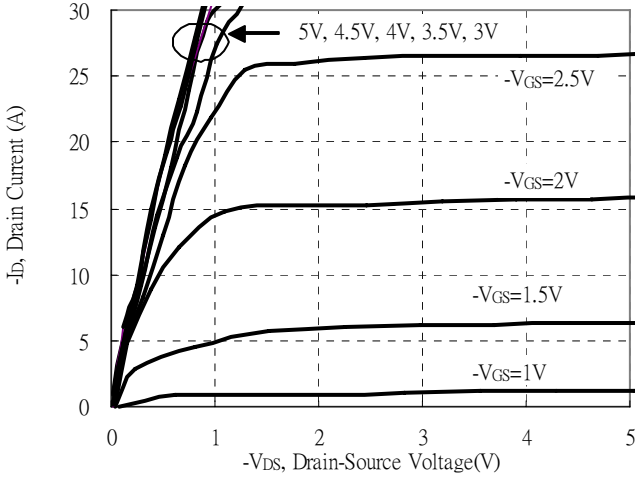


Transient Thermal Response Curves

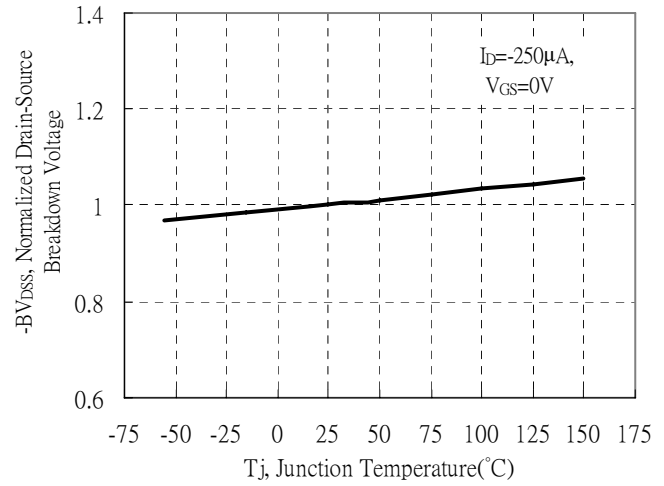


Typical Characteristics : Q2(P-channel)

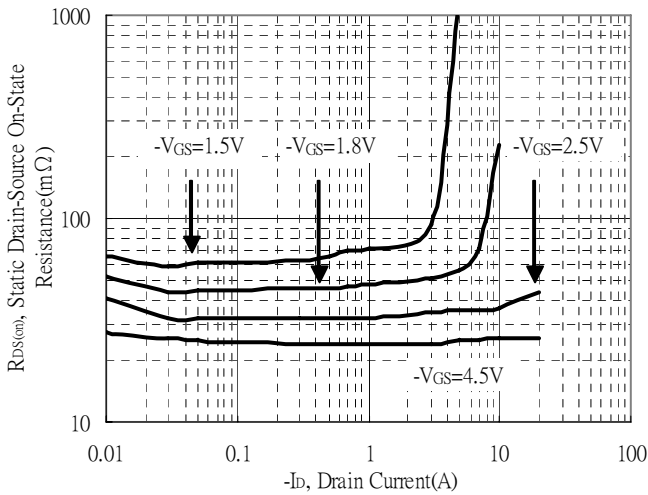
Typical Output Characteristics



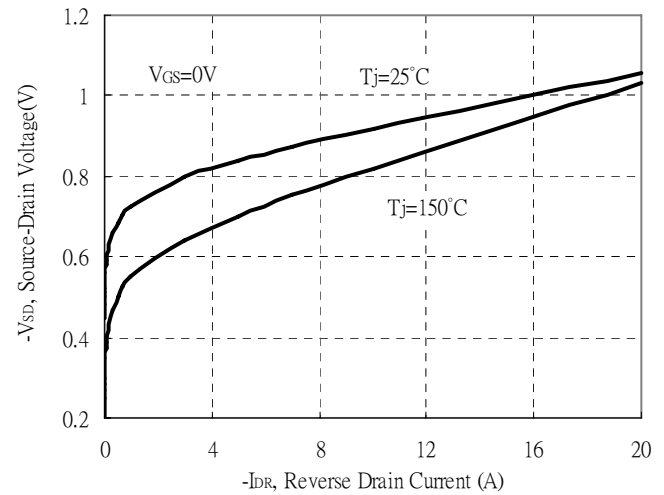
Breakdown Voltage vs Ambient Temperature



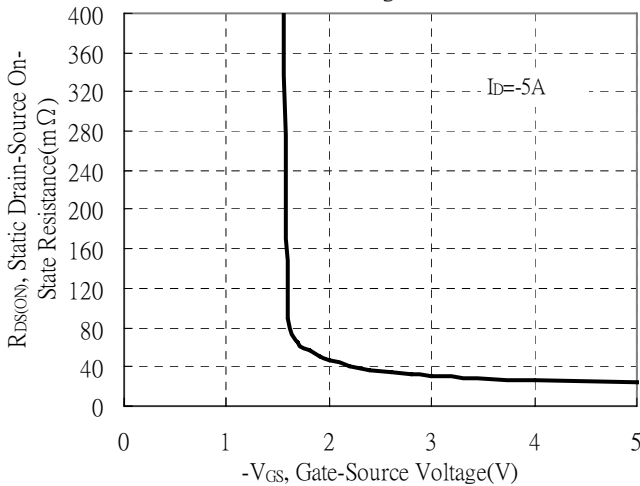
Static Drain-Source On-State resistance vs Drain Current



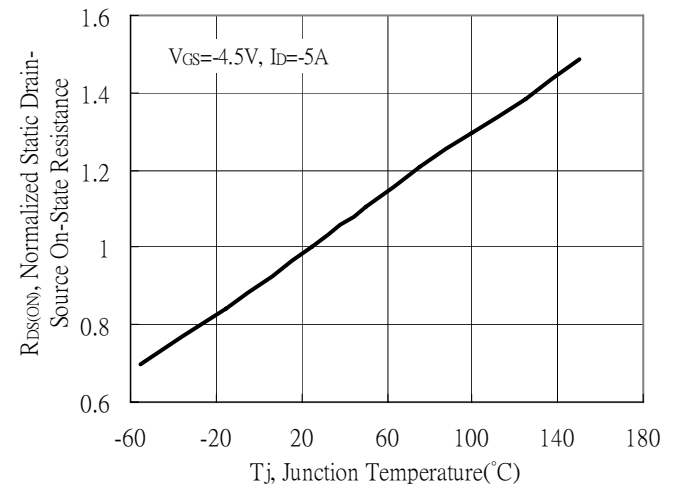
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

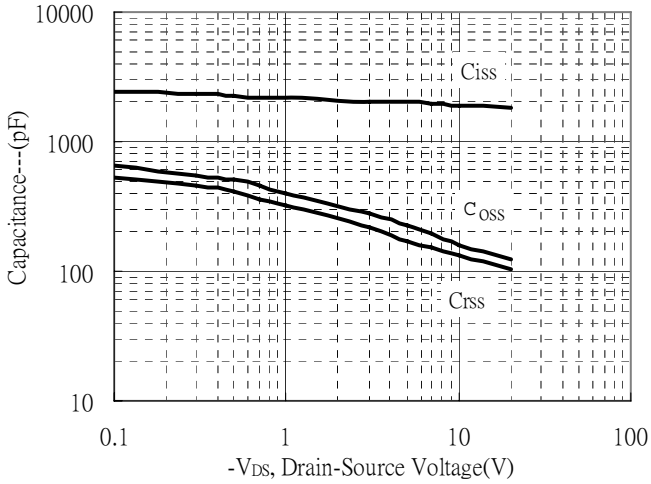


Drain-Source On-State Resistance vs Junction Temperature

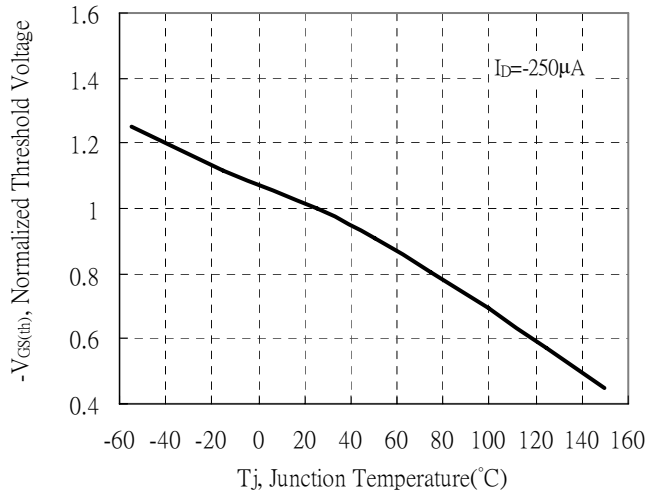


Typical Characteristics(Cont.) : Q2(P-channel)

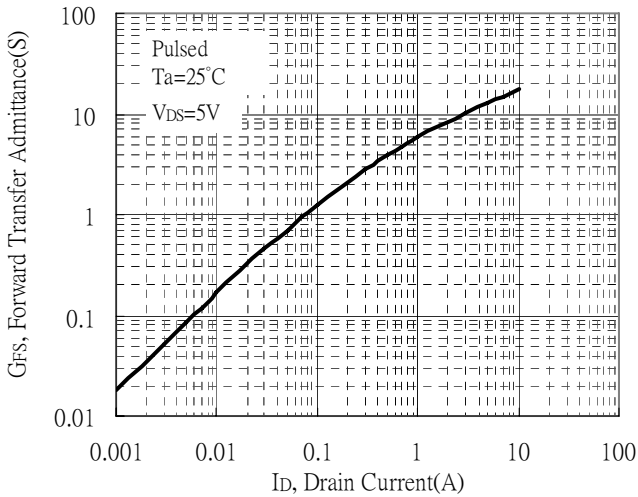
Capacitance vs Drain-to-Source Voltage



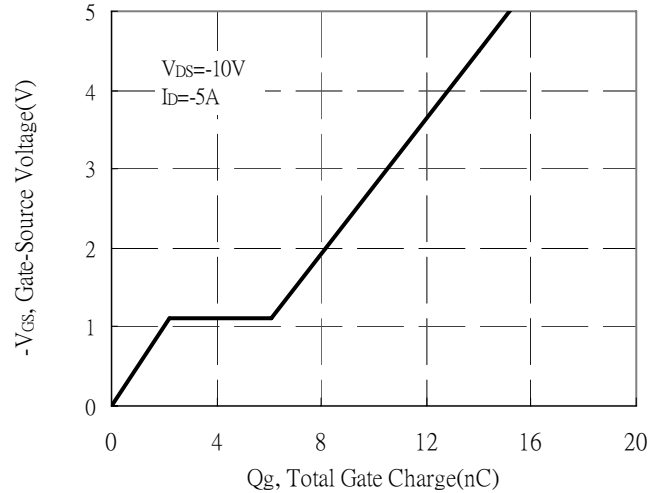
Threshold Voltage vs Junction Temperature



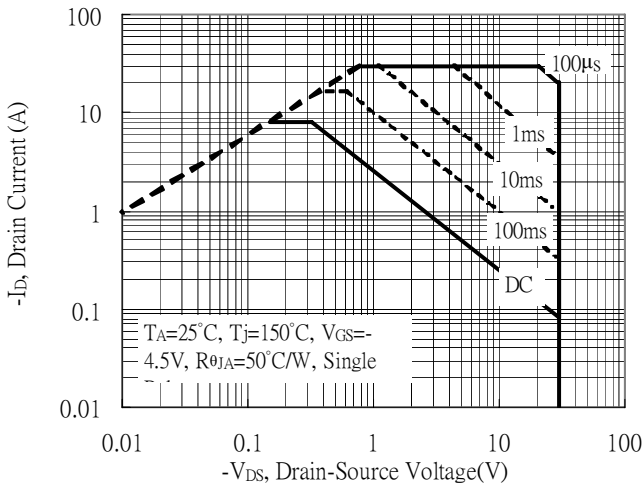
Forward Transfer Admittance vs Drain Current



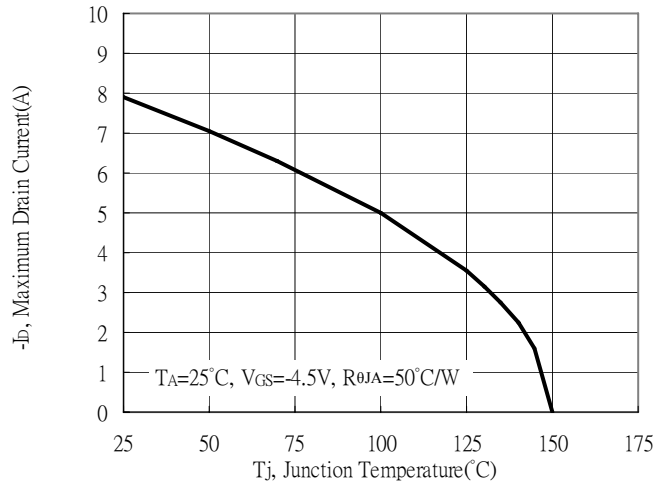
Gate Charge Characteristics



Maximum Safe Operating Area

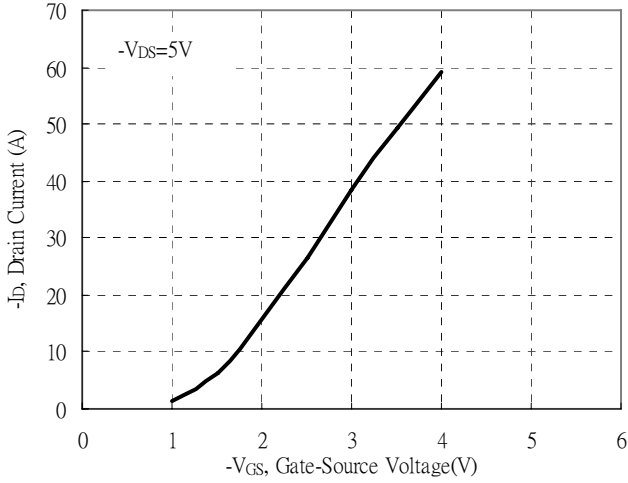


Maximum Drain Current vs Junction Temperature

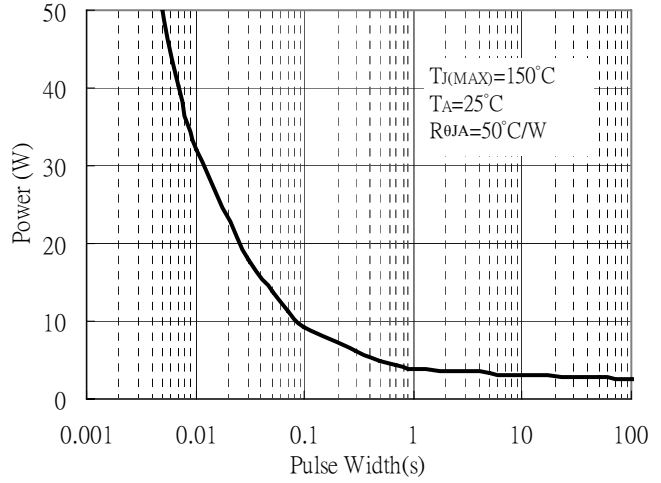


Typical Characteristics(Cont.) : Q2(P-channel)

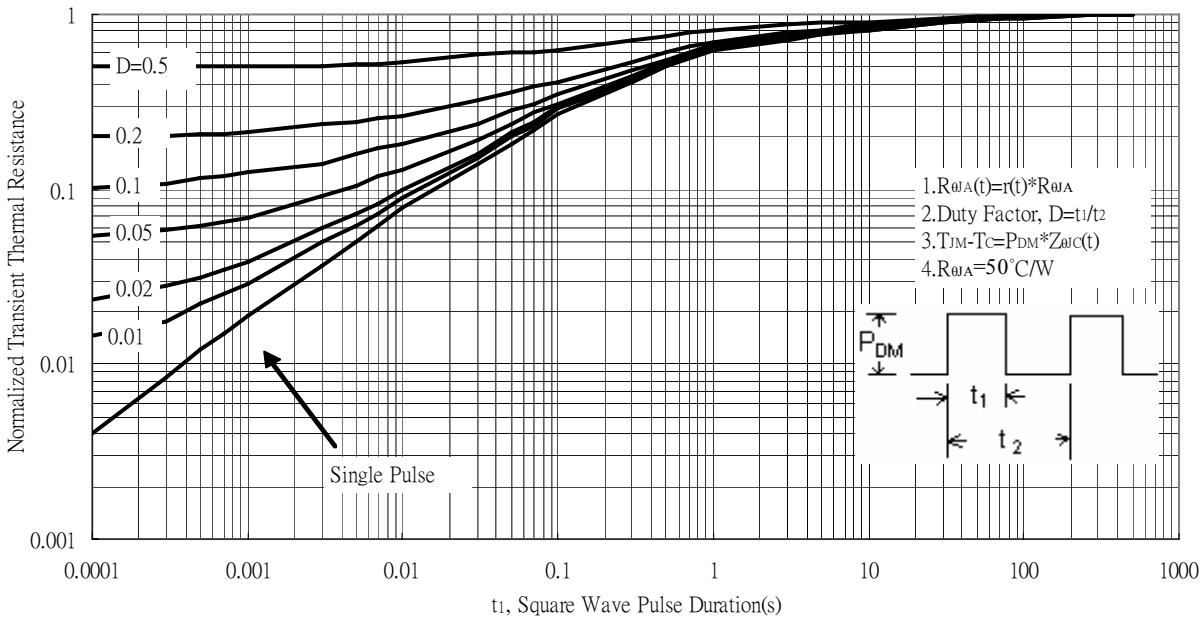
Typical Transfer Characteristics



Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)

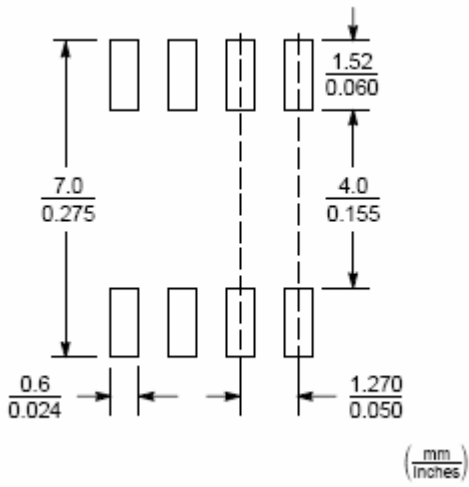


Transient Thermal Response Curves

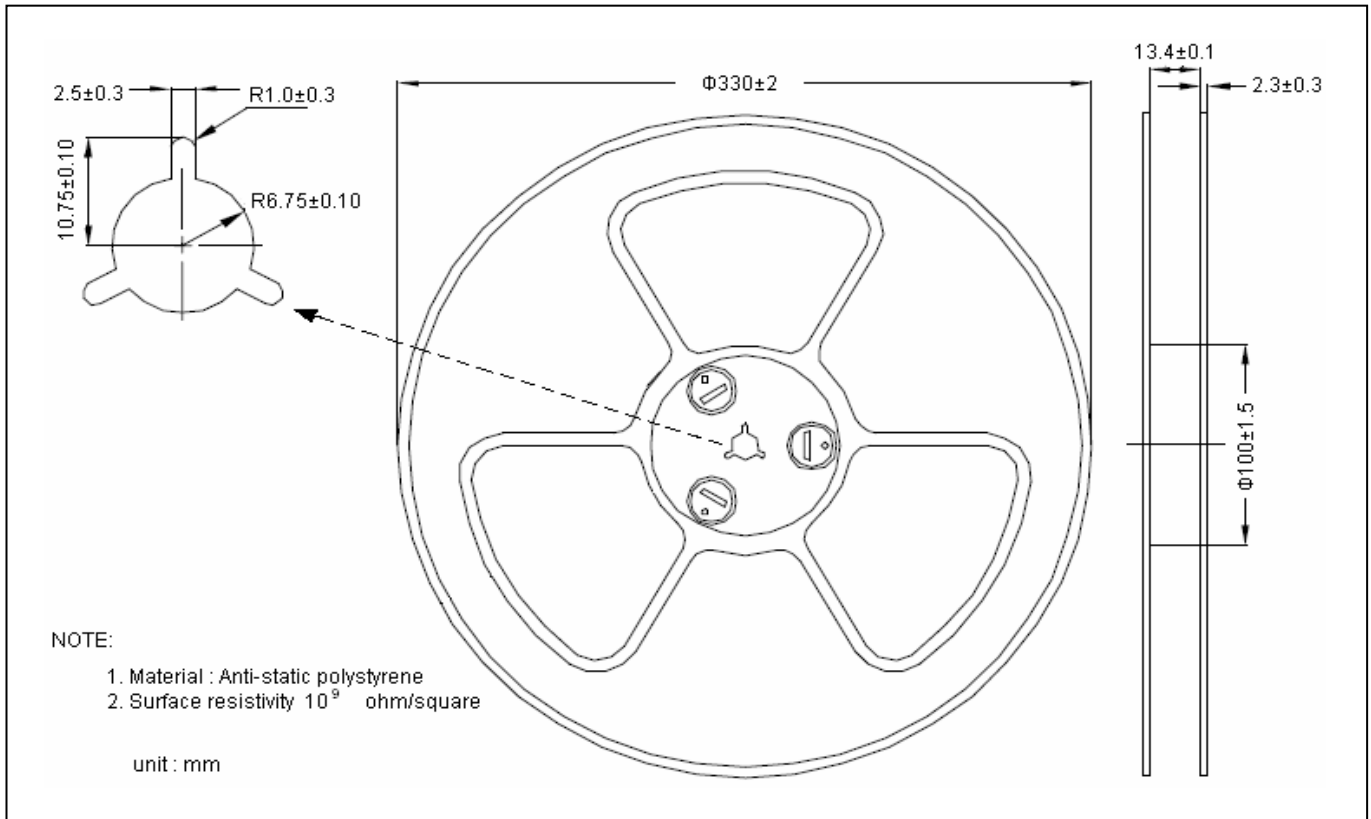




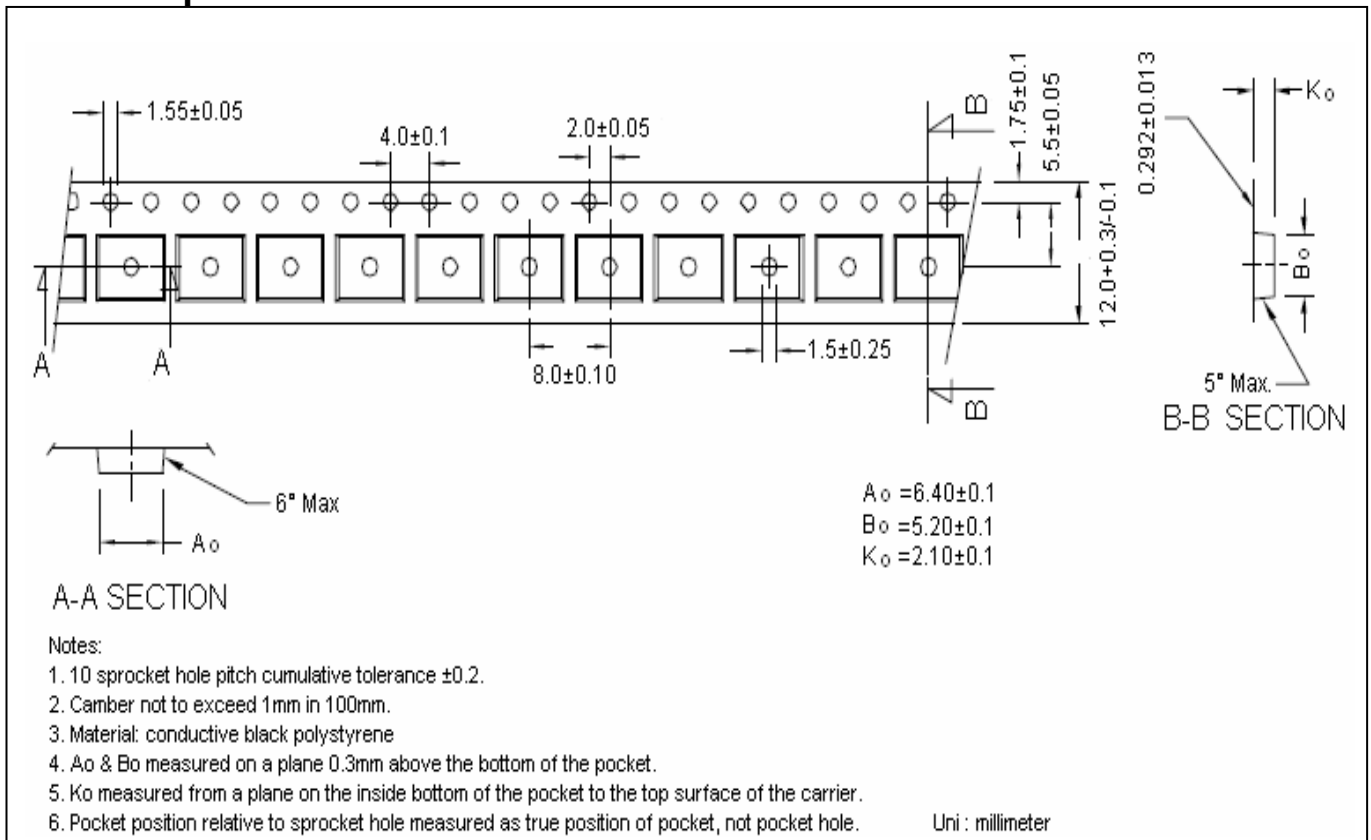
Recommended Soldering Footprint



Reel Dimension



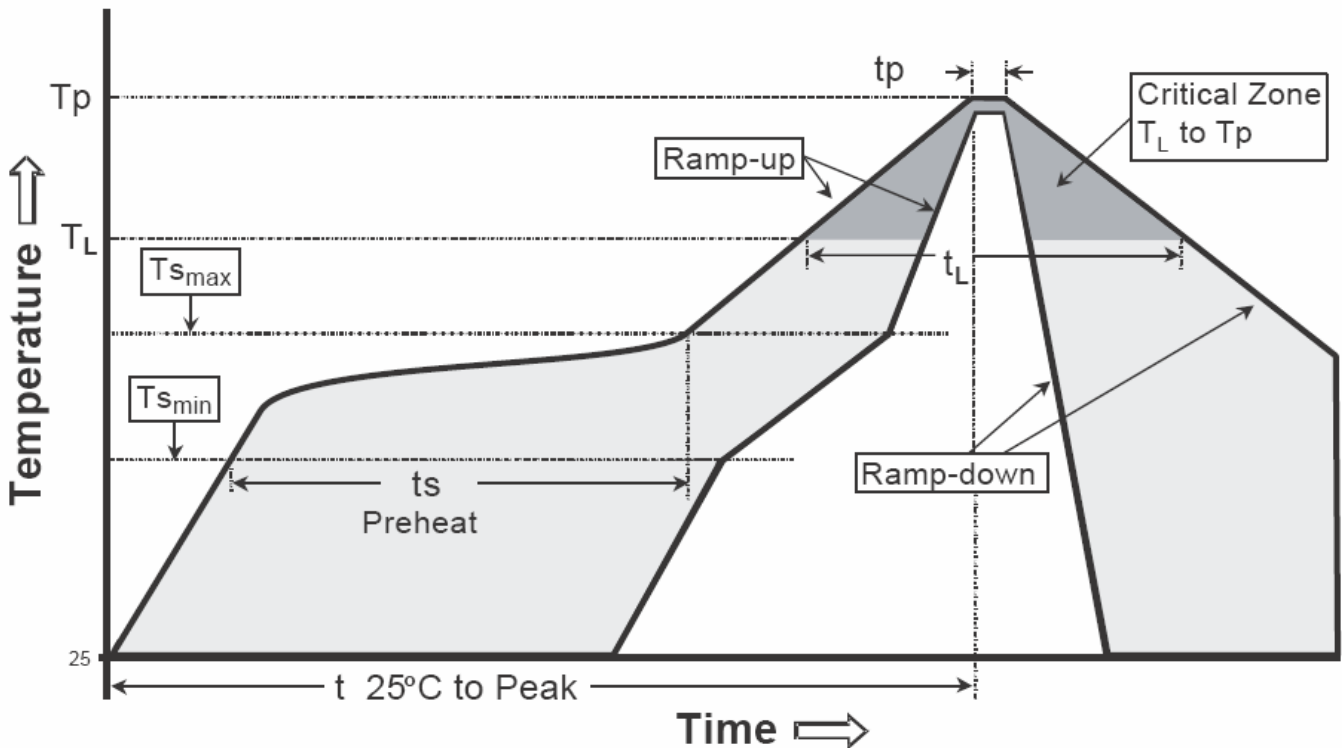
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

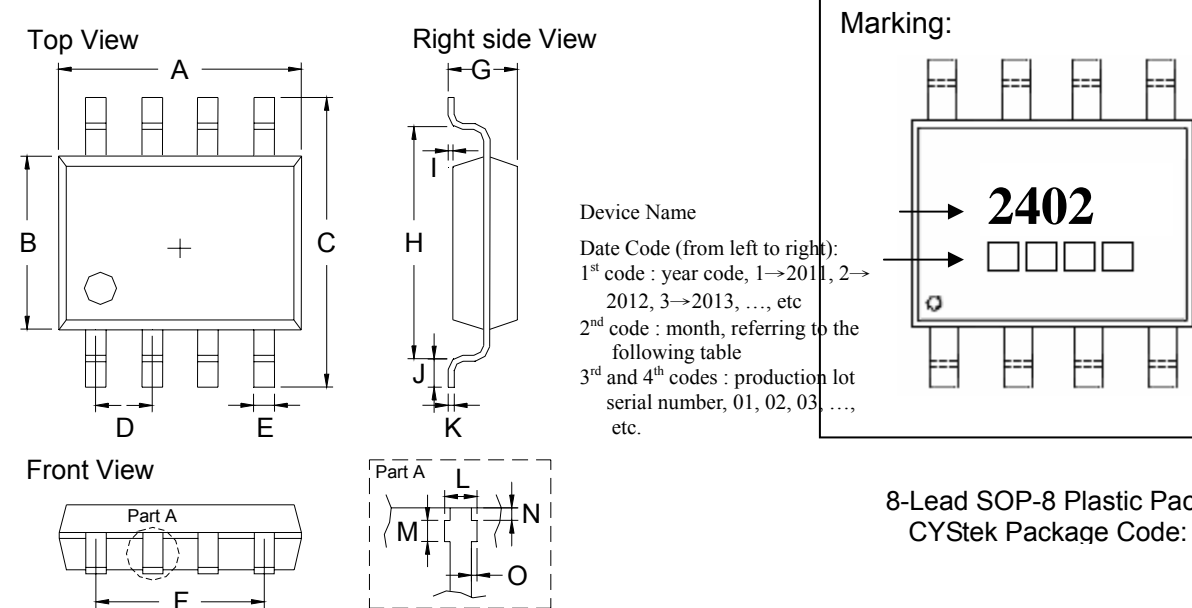
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _p)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOP-8 Dimension



Top View
 Dimensions: A, B, C, D, E

Right side View
 Dimensions: G, H, I, J, K

Front View
 Dimensions: F, Part A

Part A Detail
 Dimensions: L, M, N, O

Marking:
 Device Name: 2402
 Date Code (from left to right):
 1st code : year code, 1→2011, 2→2012, 3→2013, ..., etc
 2nd code : month, referring to the following table
 3rd and 4th codes : production lot serial number, 01, 02, 03, ..., etc.

8-Lead SOP-8 Plastic Package
 CYStek Package Code: Q8

Month code :

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	A	B	C	D	E	F	G	H	J	K	L	M

*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1909	0.2007	4.85	5.10	I	0.0019	0.0078	0.05	0.20
B	0.1515	0.1555	3.85	3.95	J	0.0118	0.0275	0.30	0.70
C	0.2283	0.2441	5.80	6.20	K	0.0074	0.0098	0.19	0.25
D	0.0480	0.0519	1.22	1.32	L	0.0145	0.0204	0.37	0.52
E	0.0145	0.0185	0.37	0.47	M	0.0118	0.0197	0.30	0.50
F	0.1472	0.1527	3.74	3.88	N	0.0031	0.0051	0.08	0.13
G	0.0570	0.0649	1.45	1.65	O	0.0000	0.0059	0.00	0.15
H	0.1889	0.2007	4.80	5.10					

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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