

**N-Channel Enhancement Mode Power MOSFET**

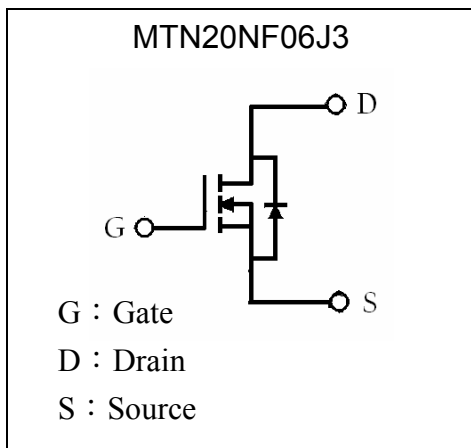
# MTN20NF06J3

$BV_{DSS}$	60V
$I_D$	60A
$R_{DSON}$	10.6mΩ (typ)

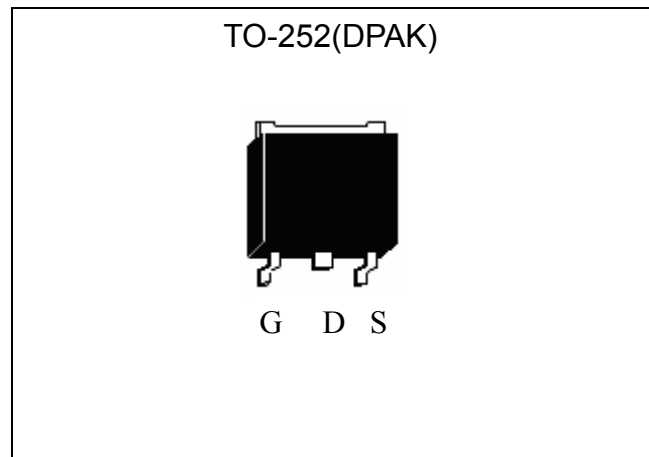
**Features**

- Low Gate Charge
- Simple Drive Requirement
- RoHS compliant package
- Repetitive Avalanche Rated
- Fast Switching Characteristic

**Symbol**

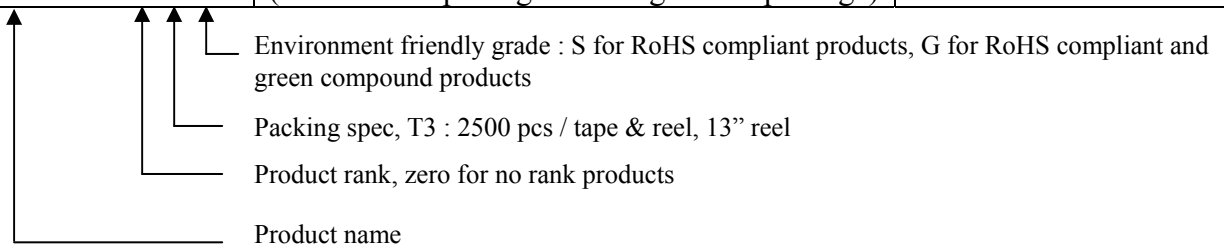


**Outline**



**Ordering Information**

Device	Package	Shipping
MTN20NF06J3-0-T3-G	TO-252 (Pb-free lead plating and halogen-free package)	2500 pcs / Tape & Reel





## Absolute Maximum Ratings (T<sub>C</sub>=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current @ T <sub>C</sub> =25°C	I <sub>D</sub>	60	A
Continuous Drain Current @ T <sub>C</sub> =100°C		42	
Pulsed Drain Current (Note 1)	I <sub>DM</sub>	240	
Avalanche Current (Note 1)	I <sub>AR</sub>	30	
Avalanche Energy (Note 2)	E <sub>AS</sub>	386	mJ
Repetitive Avalanche Energy (Note 1)	E <sub>AR</sub>	9	
Power Dissipation (T <sub>C</sub> =25°C)	P <sub>D</sub>	110	W
Derates above 25°C		0.73	W/°C
Operating Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55~+175	°C

Note : \*1. Repetitive Rating : Pulse width limited by maximum junction temperature

\*2. L=500 μH, I<sub>AS</sub>=30A, V<sub>DD</sub>=25V, starting T<sub>J</sub>=+25°C

## Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R <sub>th,j-c</sub>	1.36	°C/W
Thermal Resistance, Junction-to-ambient, max	R <sub>th,j-a</sub>	110	°C/W
Maximum Lead Temperature for Soldering purpose (Note)	T <sub>J</sub>	275	°C

Note : 1.6mm from case, for 10 seconds

## Characteristics (T<sub>C</sub>=25°C, unless otherwise specified)

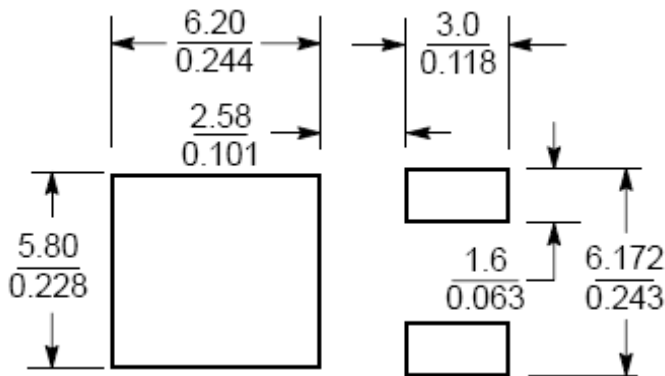
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	60	-	-	V	V <sub>GS</sub> =0, I <sub>D</sub> =250μA
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	-	0.06	-	V/°C	Reference to 25°C, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	2	3.2	4	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA
G <sub>FS</sub>	-	13	-	S	V <sub>DS</sub> = 10V, I <sub>D</sub> =25A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0
	-	-	25		V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0, T <sub>C</sub> =125°C
*R <sub>DS(ON)</sub>	-	10.6	14	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> =30A
<b>Dynamic</b>					
*Q <sub>g</sub>	-	67	-	nC	I <sub>D</sub> =60A, V <sub>DS</sub> =48V, V <sub>GS</sub> =10V
*Q <sub>gs</sub>	-	18	-		
*Q <sub>gd</sub>	-	30	-		
*t <sub>d(ON)</sub>	-	20	-	ns	V <sub>DS</sub> =30V, I <sub>D</sub> =30A, V <sub>GS</sub> =10V, R <sub>GS</sub> =4.7Ω
*t <sub>r</sub>	-	24	-		
*t <sub>d(OFF)</sub>	-	50	-		
*t <sub>f</sub>	-	36	-		



Ciss	-	3768	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz
Coss	-	146	-		
Crss	-	129	-		
<b>Source-Drain Diode</b>					
*I <sub>S</sub>	-	-	50	A	
*I <sub>SM</sub>	-	-	200		
*V <sub>SD</sub>	-	-	1.5	V	I <sub>S</sub> =50A, V <sub>GS</sub> =0
*trr	-	52	-	ns	I <sub>S</sub> =50A, dI <sub>F</sub> /dt=100A/μs, V <sub>GS</sub> =0
*Q <sub>rr</sub>	-	75	-	nC	

\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

**Recommended soldering footprint**

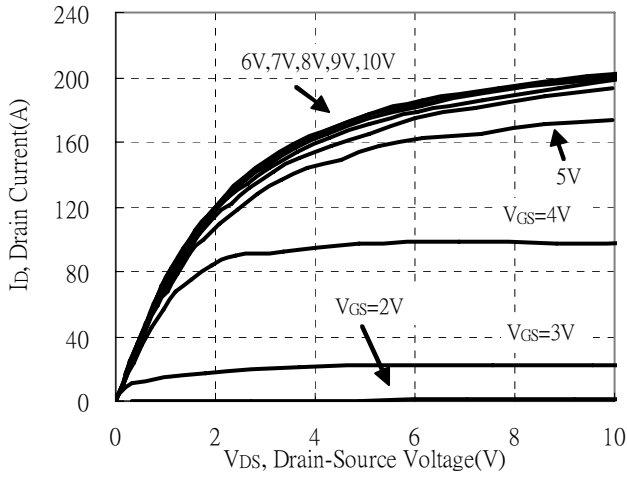


Unit (  $\frac{\text{mm}}{\text{inch}}$  )

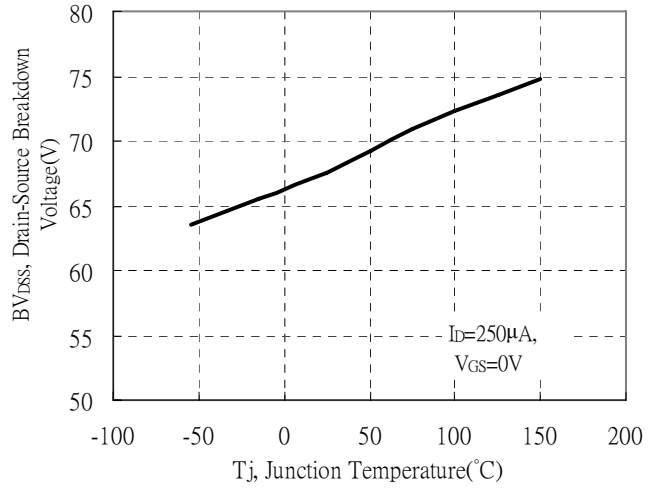


**Typical Characteristics**

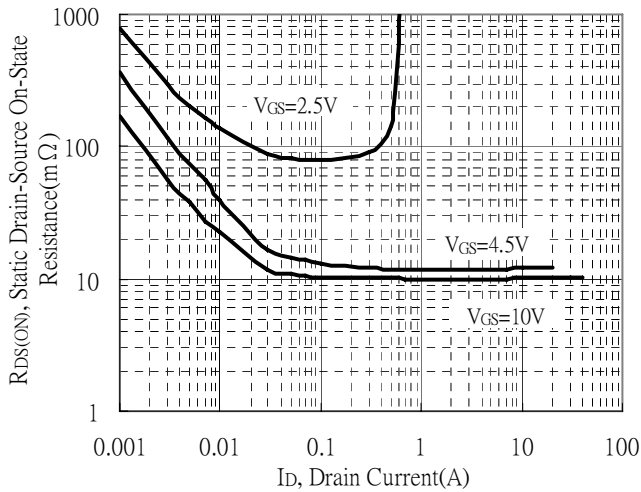
Typical Output Characteristics



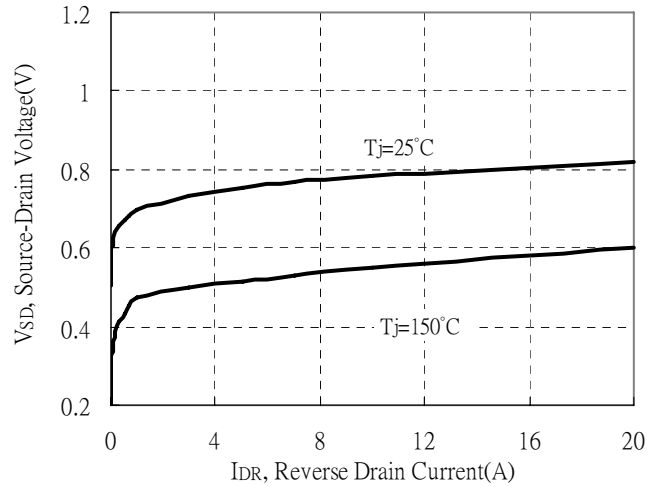
Brekdown Voltage vs Ambient Temperature



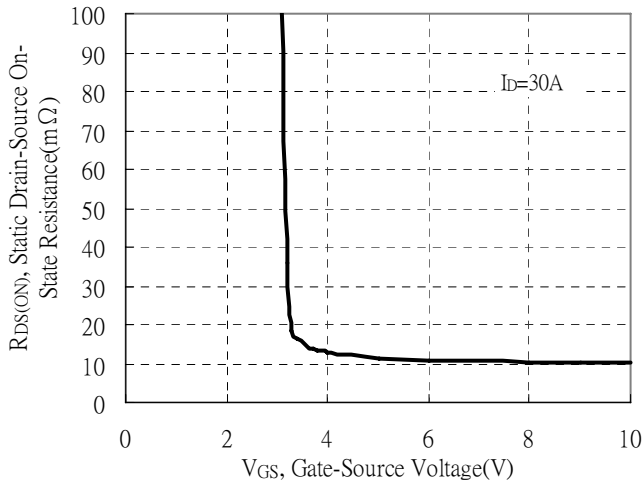
Static Drain-Source On-State resistance vs Drain Current



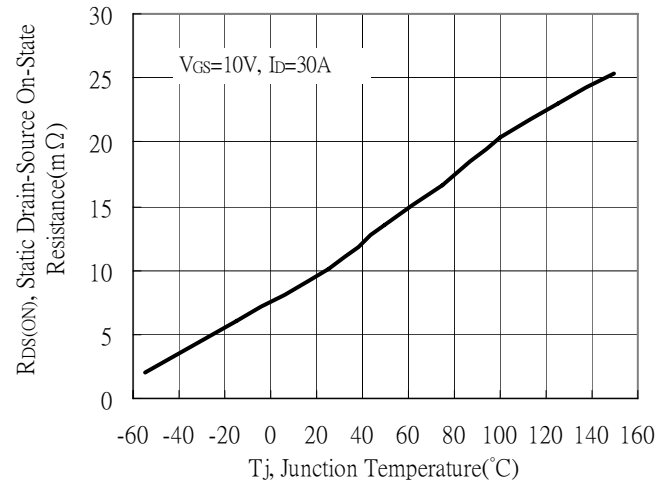
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

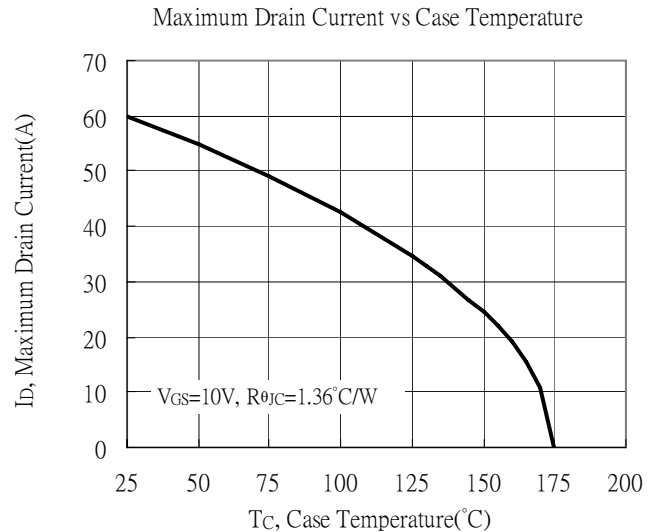
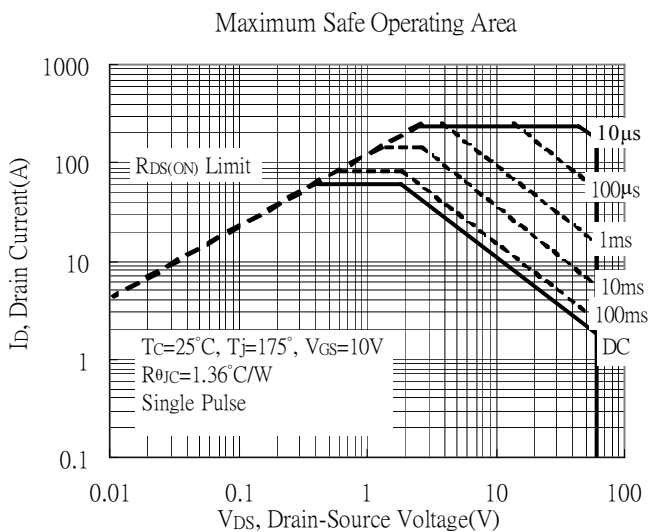
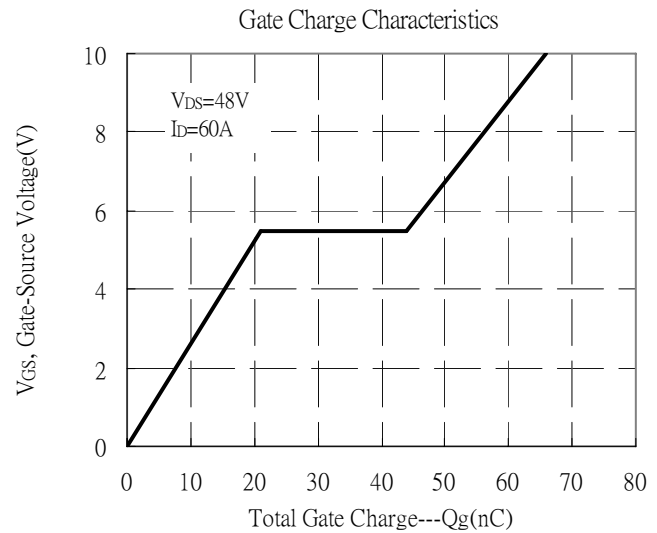
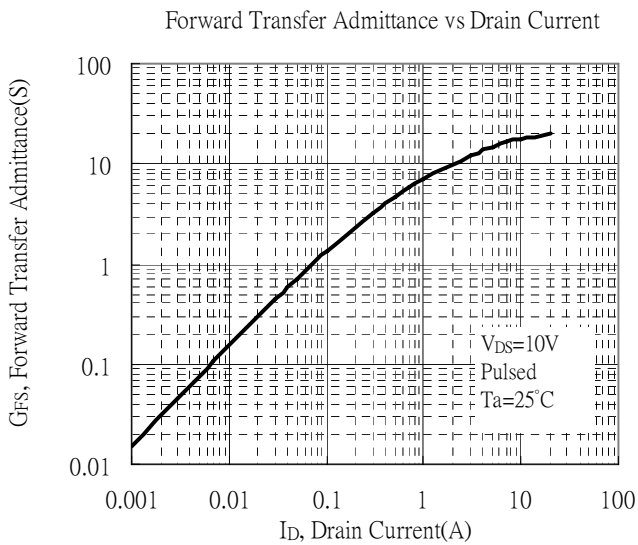
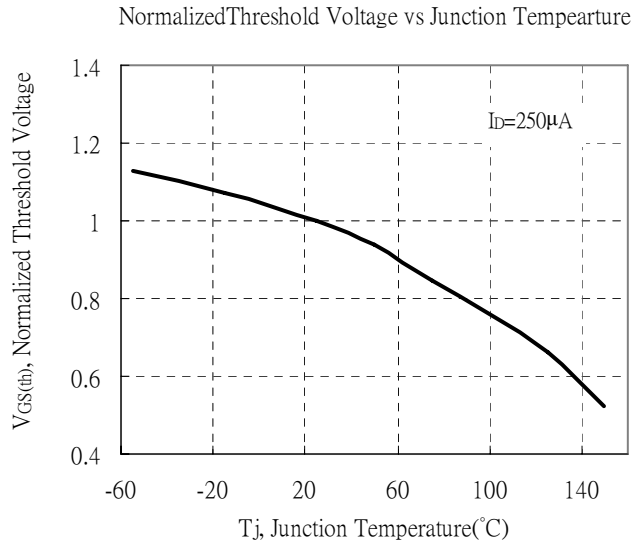
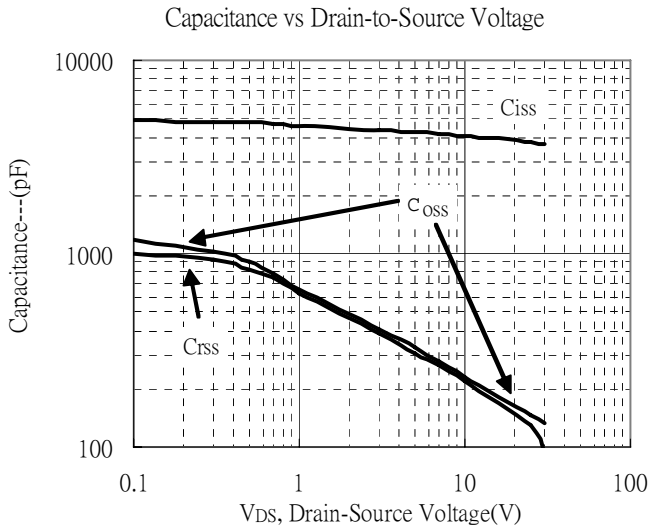


Drain-Source On-State Resistance vs Junction Temperature





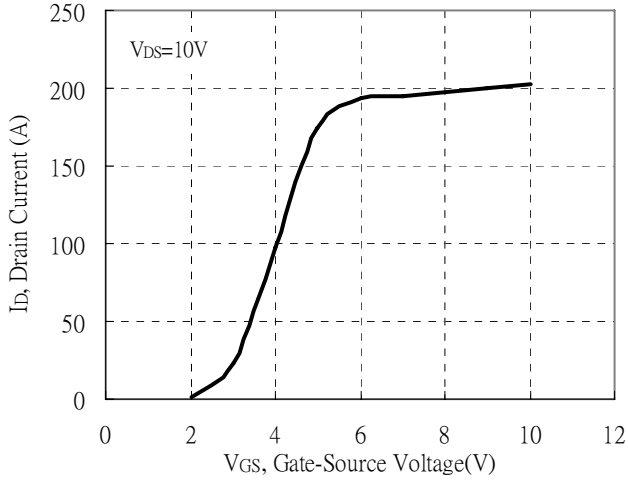
**Typical Characteristics(Cont.)**



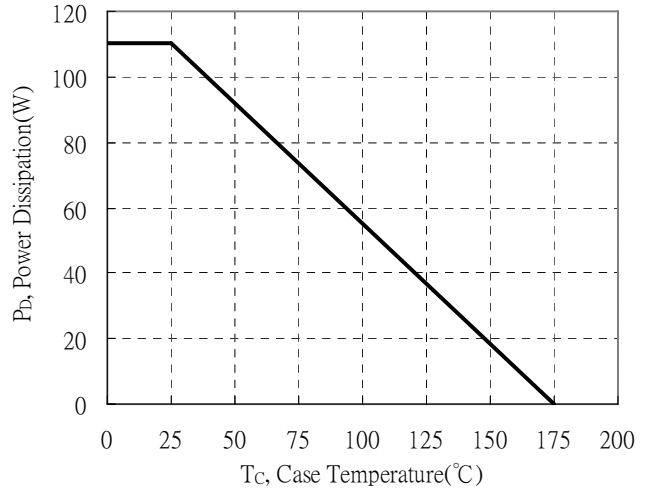


### Typical Characteristics(Cont.)

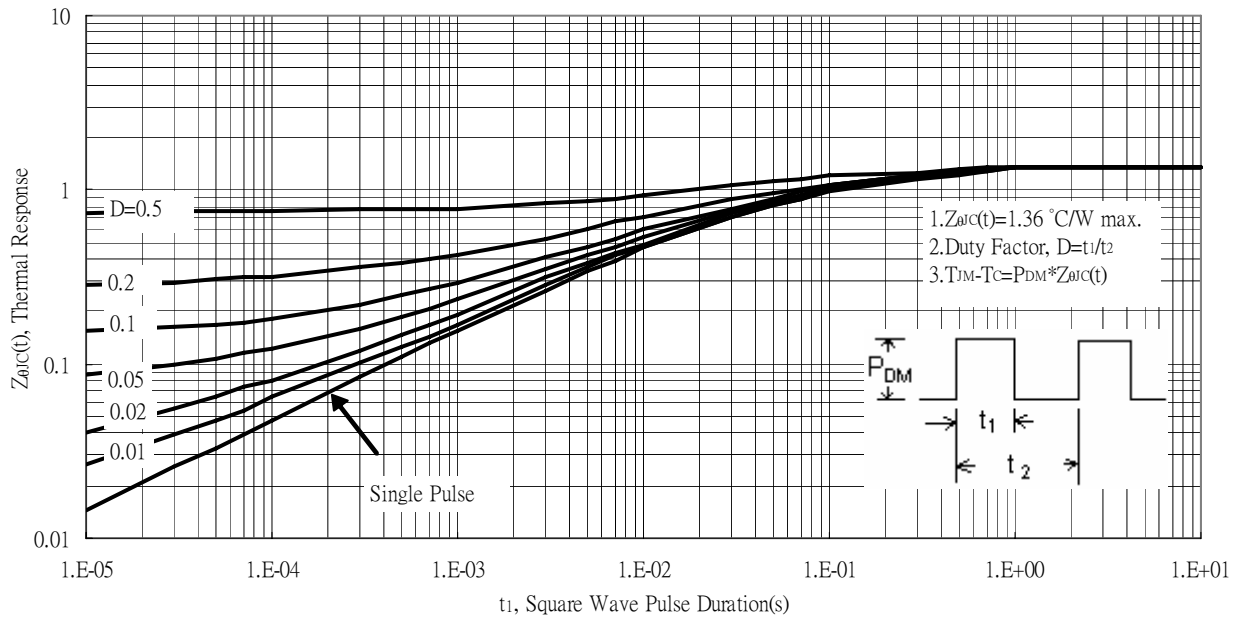
Typical Transfer Characteristics



Power Derating Curve

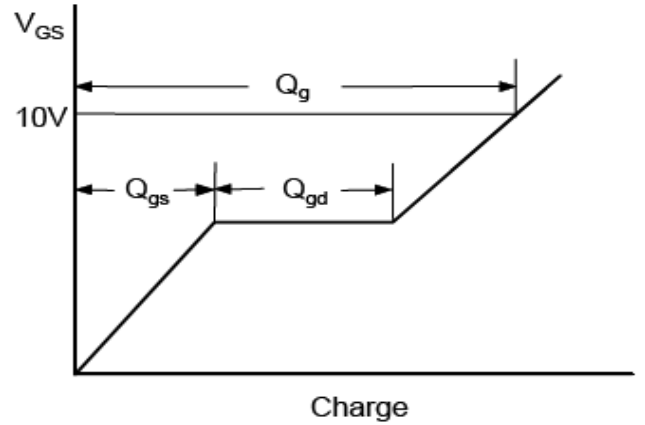
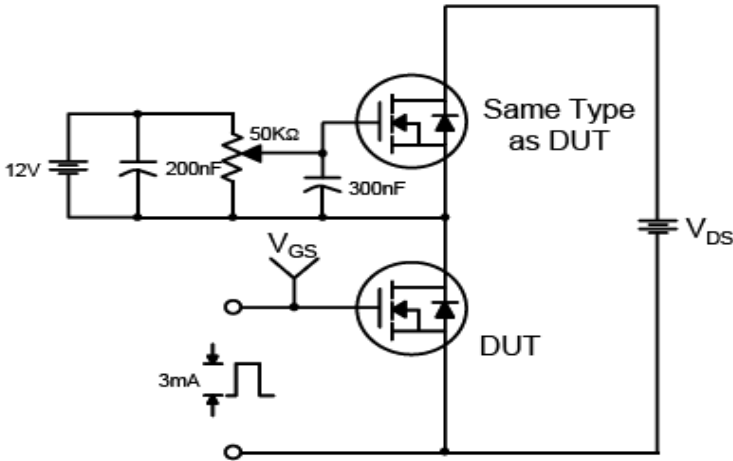


Transient Thermal Response Curves

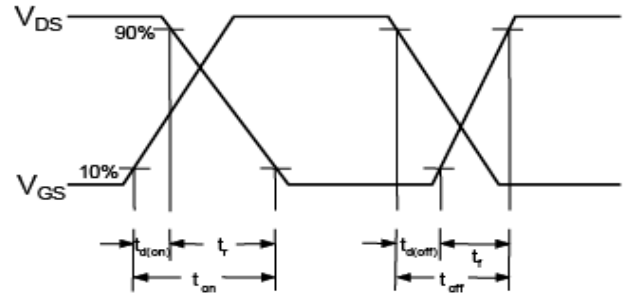
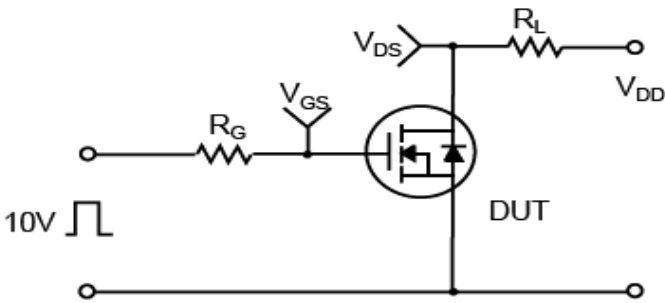


## Test Circuit and Waveforms

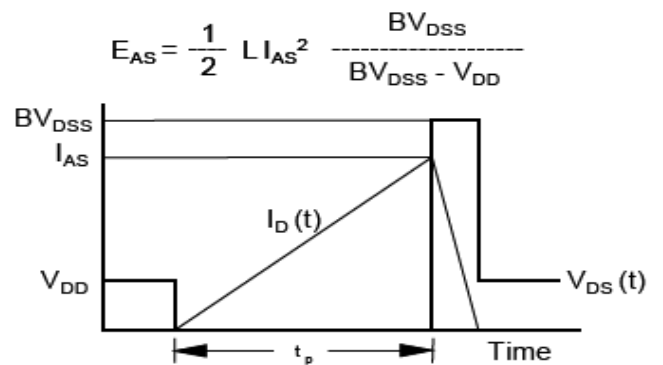
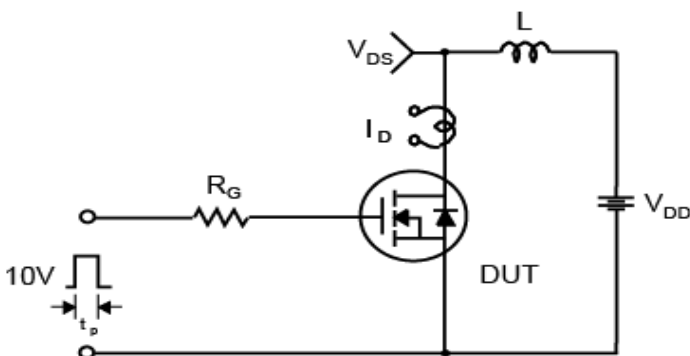
### Gate Charge Test Circuit & Waveform



### Resistive Switching Test Circuit & Waveforms

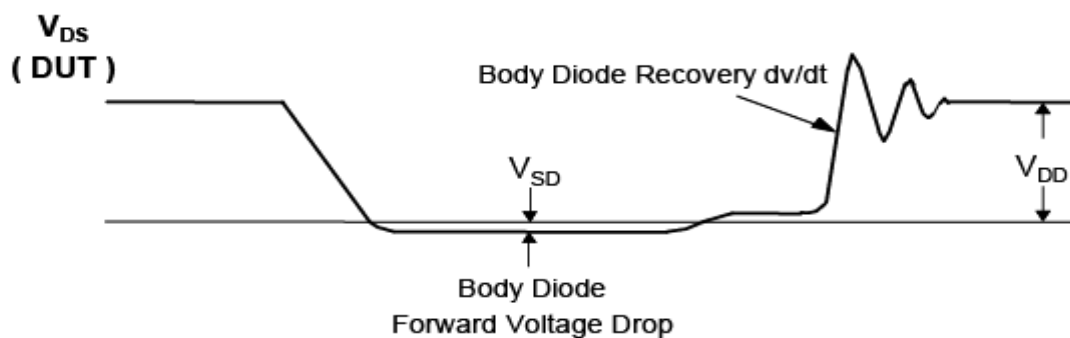
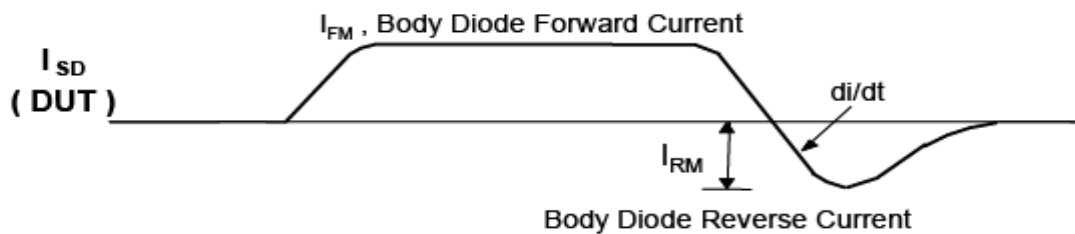
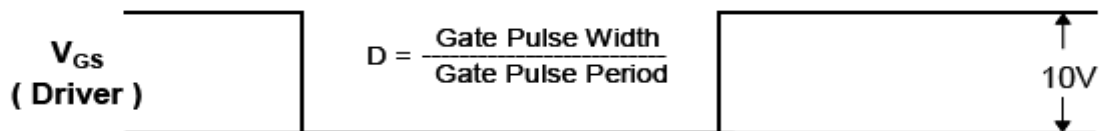
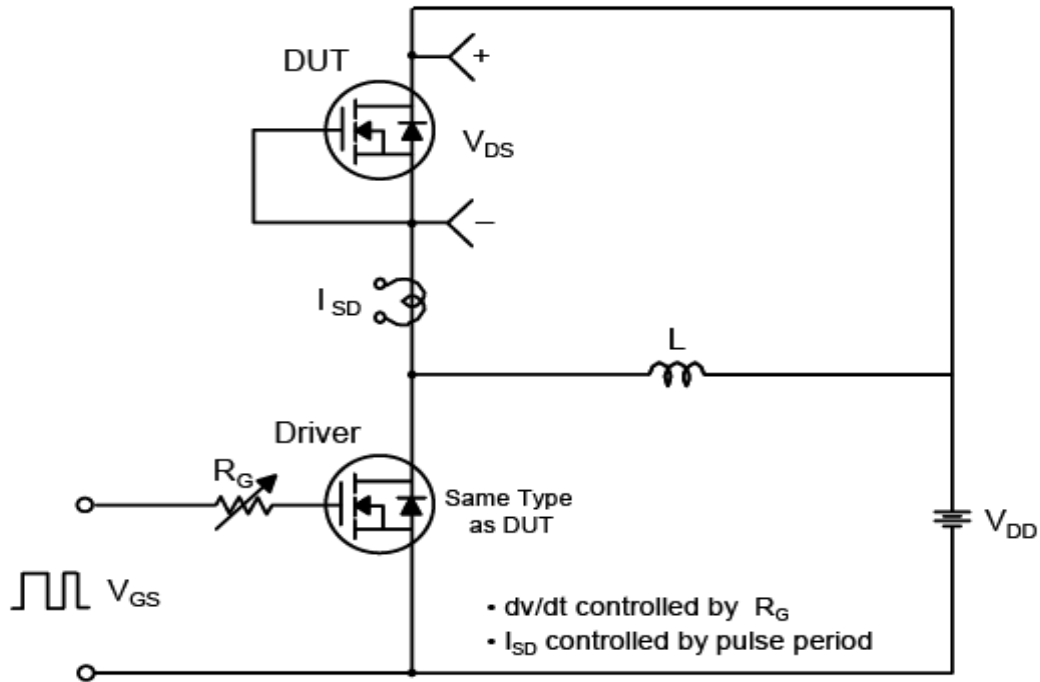


### Unclamped Inductive Switching Test Circuit & Waveforms



**Test Circuit and Waveforms(Cont.)**

**Peak Diode Recovery dv/dt Test Circuit & Waveforms**



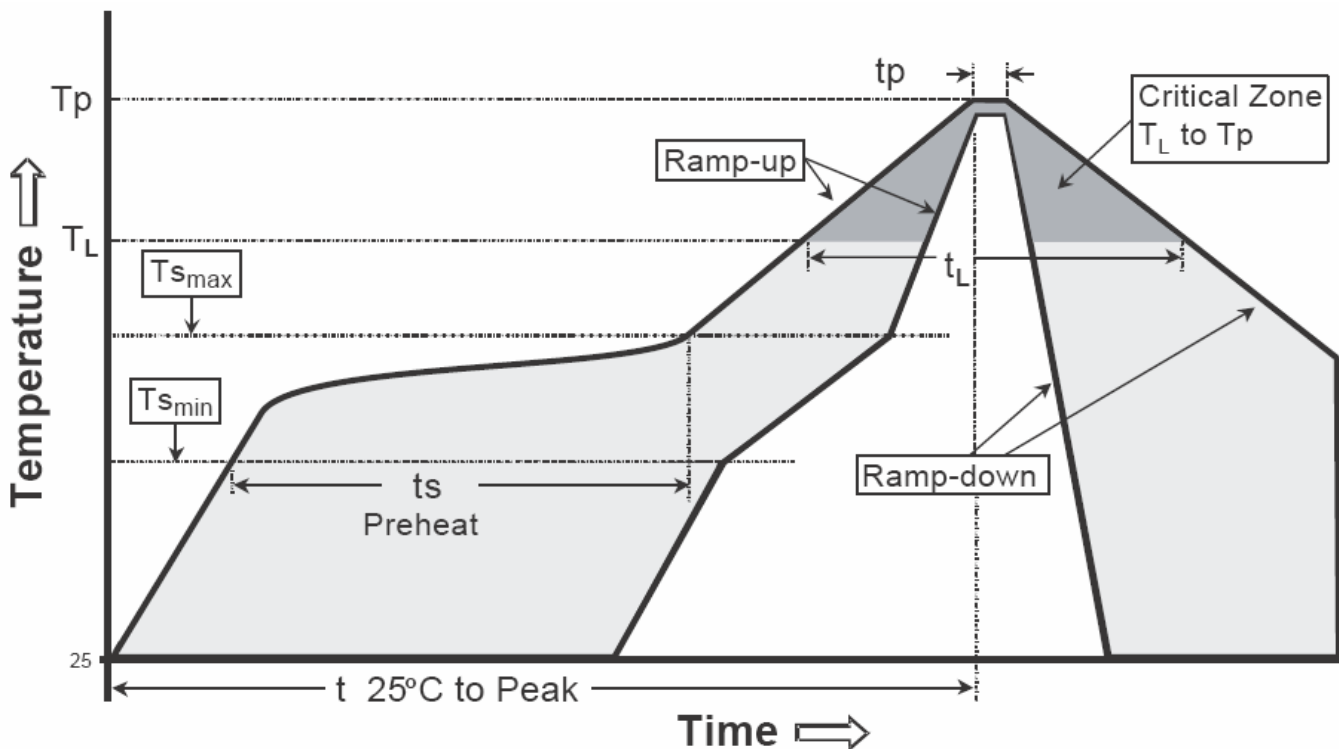




**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

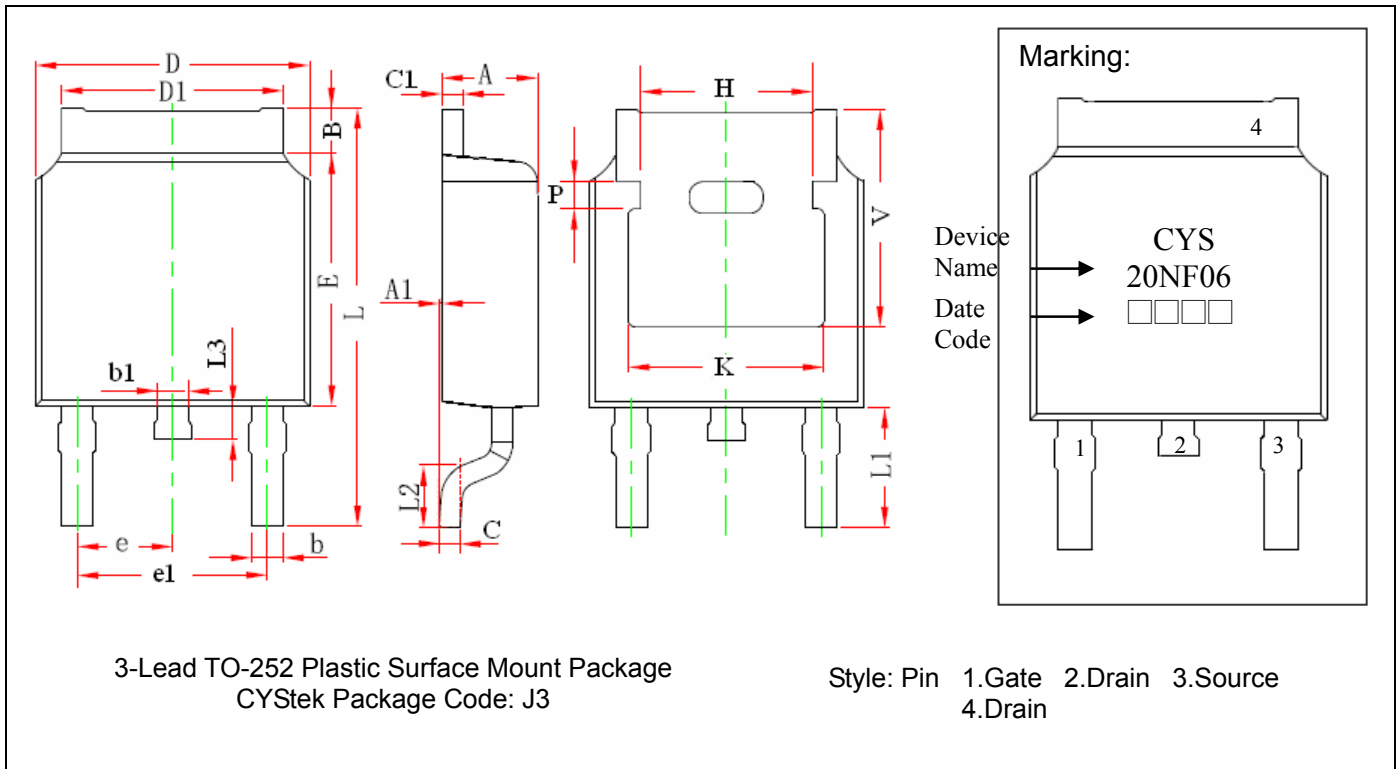
**Recommended temperature profile for IR reflow**



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T <sub>s min</sub> )	100°C	150°C
-Temperature Max(T <sub>s max</sub> )	150°C	200°C
-Time(t <sub>s min</sub> to t <sub>s max</sub> )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60-150 seconds	60-150 seconds
Peak Temperature(T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

## TO-252 Dimension



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.087	0.094	2.200	2.400	e	0.086	0.094	2.186	2.386
A1	0.000	0.005	0.000	0.127	e1	0.172	0.188	4.372	4.772
B	0.039	0.048	0.990	1.210	H	0.163	REF	4.140	REF
b	0.026	0.034	0.660	0.860	K	0.190	REF	4.830	REF
b1	0.026	0.034	0.660	0.860	L	0.386	0.409	9.800	10.400
C	0.018	0.023	0.460	0.580	L1	0.114	REF	2.900	REF
C1	0.018	0.023	0.460	0.580	L2	0.055	0.067	1.400	1.700
D	0.256	0.264	6.500	6.700	L3	0.024	0.039	0.600	1.000
D1	0.201	0.215	5.100	5.460	P	0.026	REF	0.650	REF
E	0.236	0.244	6.000	6.200	V	0.211	REF	5.350	REF

- Notes:**
- 1.Controlling dimension: millimeters.
  - 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
  - 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead : Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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