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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Not recommended
for new design

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HSS104

Silicon Epitaxial Planar Diode for High Speed Switching

REJ03G0571-0300
 (Previous: ADE-208-178B)
 Rev.3.00
 Mar 25, 2005

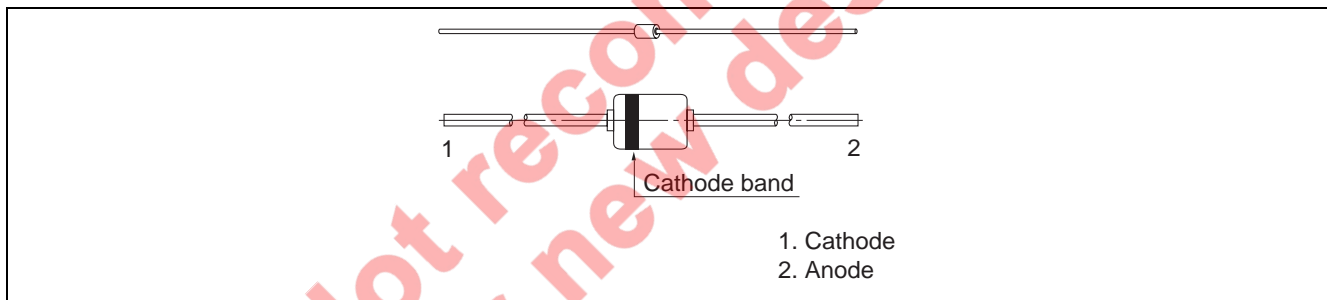
Features

- Short reverse recovery time and forward recovery time.
- Suitable for 5 mm pitch high speed automatic insertion.
- Small glass package (MHD) enables easy mounting and high reliability.

Ordering Information

Type No.	Cathode band	Package Name	Package Code (Previous Code)
HSS104	Blue	MHD	GRZZ0002ZC-A (MHD)

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}	40	V
Reverse voltage	V_R	35	V
Average rectified current	I_o	110	mA
Peak forward current	I_{FM}	300	mA
Non-Repetitive peak forward surge current	I_{FSM}^*	400	mA
Power dissipation	P_d	300	mW
Junction temperature	T_j	175	°C
Storage temperature	T_{stg}	-65 to +175	°C

Note: * Within 1s forward surge current.

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_F	—	—	1.2	V	$I_F = 100$ mA
Reverse current	I_R	—	—	0.5	μ A	$V_R = 35$ V
Capacitance	C	—	—	3.0	pF	$V_R = 0.5$ V, $f = 1$ MHz
Reverse recovery time	t_{rr}	—	—	3.0	ns	$I_F = 10$ mA, $V_R = 6$ V, $R_L = 50$ Ω

Not recommended
for new designs

Main Characteristic

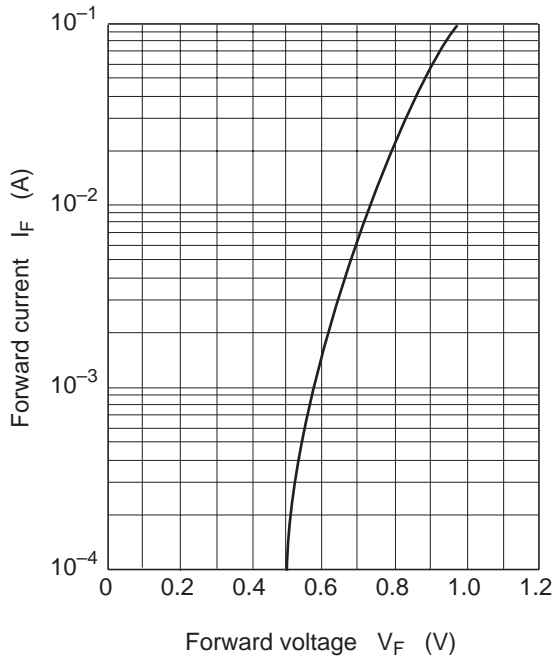


Fig.1 Forward current vs. Forward voltage

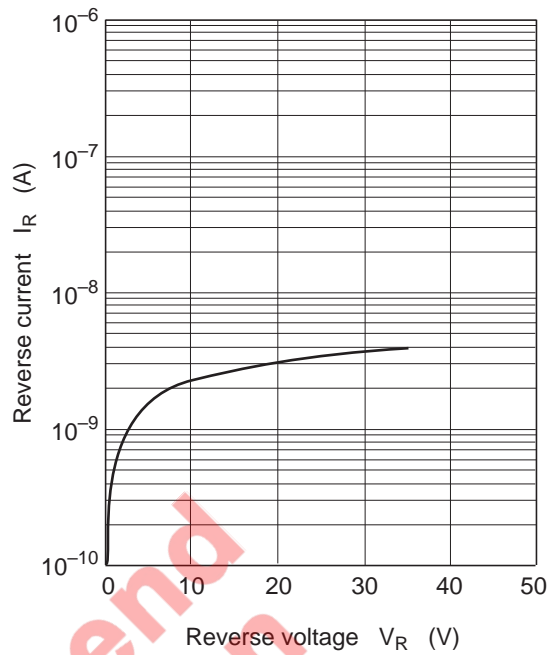


Fig.2 Reverse current vs. Reverse voltage

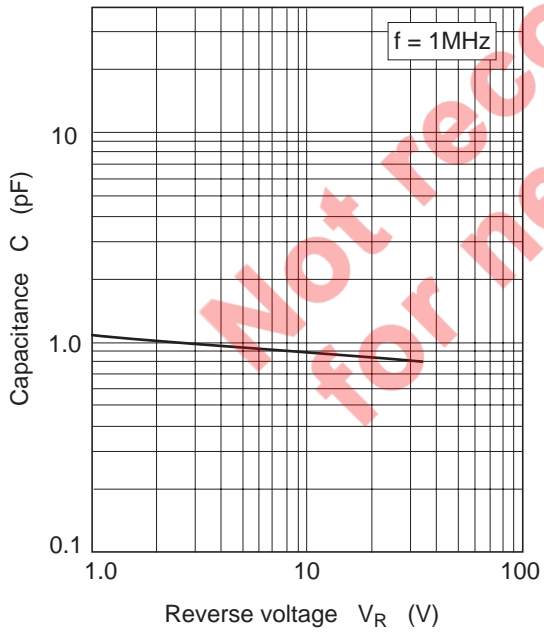
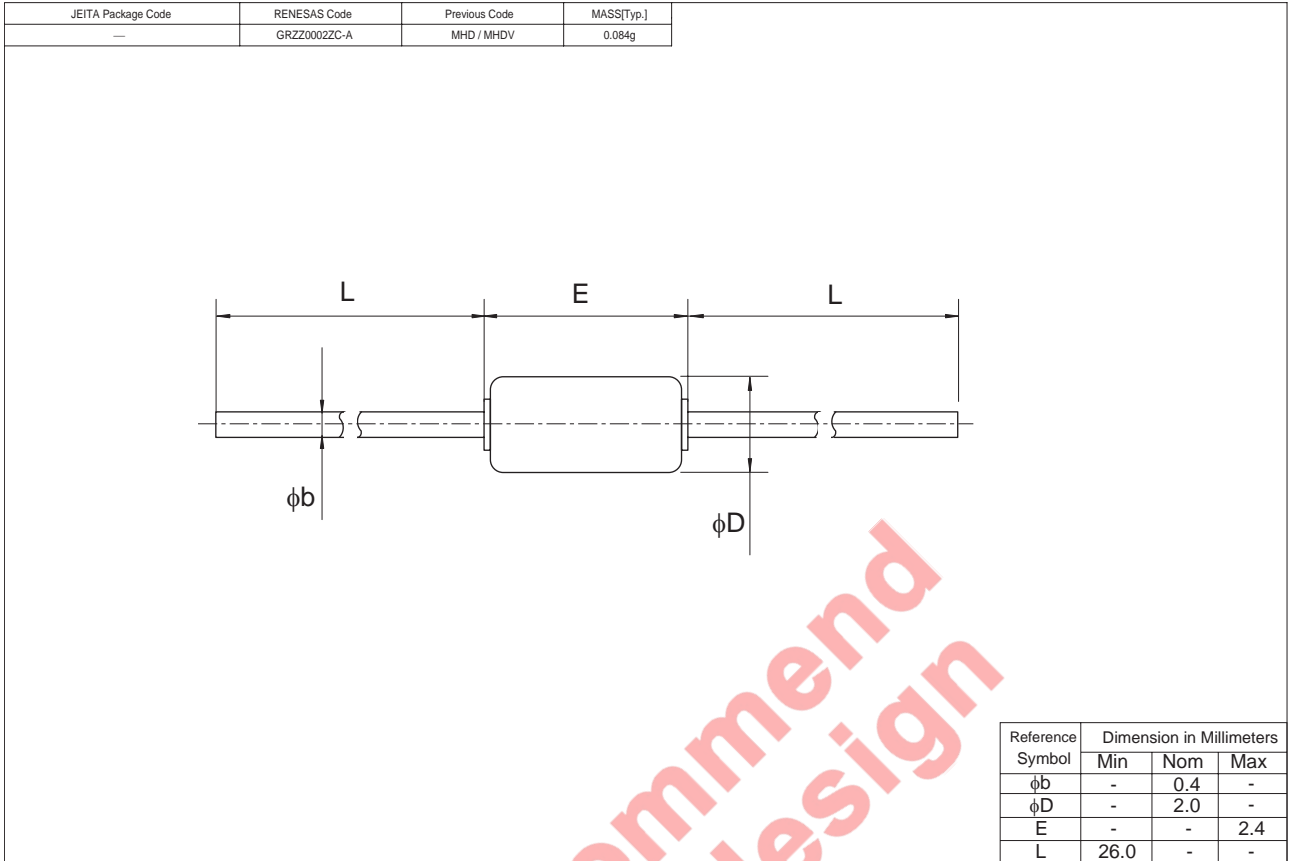


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions



Not recommend for new design

Keep safety first in your circuit designs!

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Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

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