

P-Channel Enhancement Mode Power MOSFET

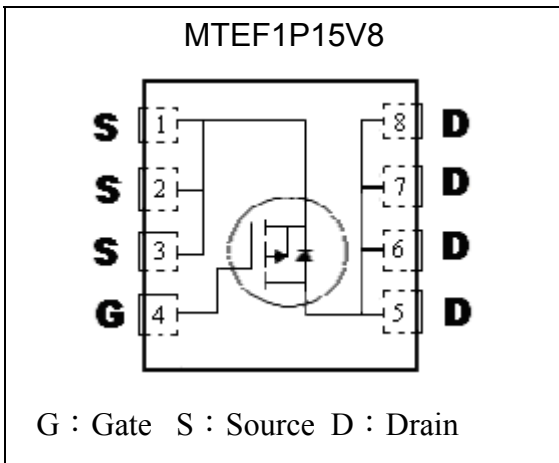
MTEF1P15V8

BV _{DSS}	-150V
I _D	-4A @ V _{GS} =-10V, T _C =25°C
R _{DS(on)(Typ)}	0.64Ω @ V _{GS} =-10V, I _D =-1.4A
	0.7Ω @ V _{GS} =-6V, I _D =-1A

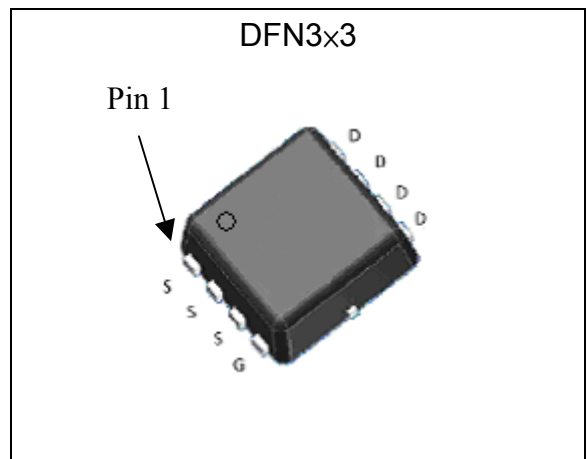
Features

- Simple drive requirement
- Low on-resistance
- Fast switching speed
- Pb-free lead plating and halogen-free package

Equivalent Circuit

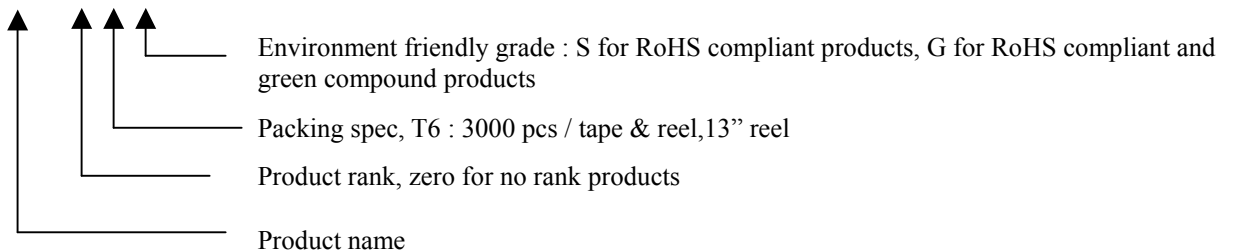


Outline



Ordering Information

Device	Package	Shipping
MTEF1P15V8-0-T6-G	DFN3x3 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel





Absolute Maximum Ratings (T_c=25°C, unless otherwise noted)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V _{DS}	-150	V
Gate-Source Voltage		V _{GS}	±30	
Continuous Drain Current @ T _c =25°C, V _{GS} =-10V		I _D	-4.0	A
Continuous Drain Current @ T _c =100°C, V _{GS} =-10V			-2.5	
Continuous Drain Current @ T _A =25°C, V _{GS} =-10V *3			-1.1	
Continuous Drain Current @ T _A =70°C, V _{GS} =-10V *3			-0.88	
Pulsed Drain Current *1, 2			I _{DM}	
Continuous Source-Drain Diode Current	T _c =25°C	I _S	-4	
Avalanche Current		I _{AS}	-1.4	
Avalanche Energy @ L=10mH, I _D =-1.4A, R _G =25Ω		E _{AS}	9.8	mJ
Maximum Power Dissipation	T _c =25°C	P _D	42	W
	T _c =70°C		27	
	T _A =25°C *3		2.1	
	T _A =70°C *3		1.3	
Operating Junction and Storage Temperature Range		T _j , T _{stg}	-55~+150	°C

Thermal Data

Parameter	Symbol	Typ	Maximum	Unit
Thermal Resistance, Junction-to-ambient *3	R _{th,j-a}	50	60	°C/W
Thermal Resistance, Junction-to-case	R _{th,j-c}	2.5	3	

- Note : 1. Pulse width limited by maximum junction temperature.
 2. Duty cycle ≤ 1%.
 3. Surface mounted on 1 in² copper pad of FR-4 board; 135°C/W when mounted on minimum pad of 2 oz. copper.

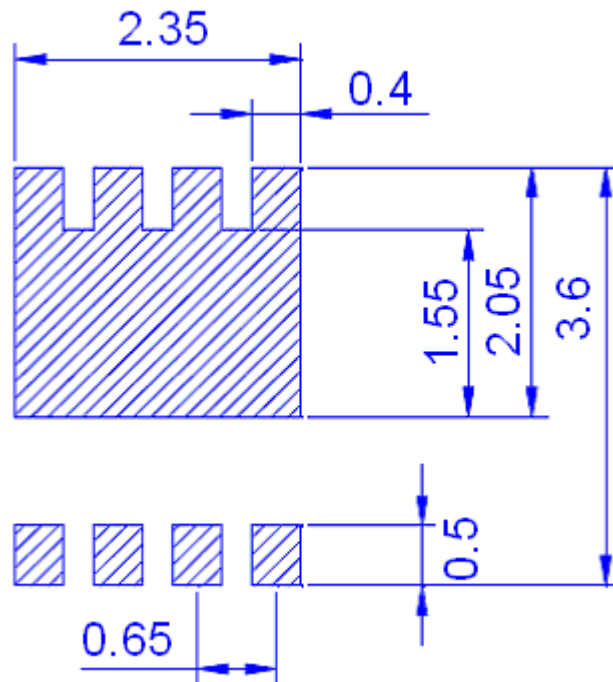
Characteristics (T_c=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-150	-	-	V	V _{GS} =0V, I _D =-250μA
ΔBV _{DSS} /ΔT _J	-	-100	-	mV/°C	I _D =-250μA
ΔV _{GS(th)} /ΔT _J	-	6	-		
V _{GS(th)}	-2	-3	-4	V	V _{DS} = V _{GS} , I _D =-250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±30V, V _{DS} =0V
I _{DSS}	-	-	-1	μA	V _{DS} = -120V, V _{GS} = 0V
	-	-	-10		V _{DS} = -120V, V _{GS} = 0V, T _J =55°C
R _{DS(ON)} *1	-	0.64	0.85	Ω	V _{GS} = -10V, I _D =-1.4A
	-	0.7	0.9		V _{GS} = -6V, I _D =-1A
G _{FS} *1	-	2.5	-	S	V _{DS} = -10V, I _D =-1.4A

Dynamic					
Qg *1, 2	-	8.3	-	nC	V _{DS} =-75V, I _D =-4A, V _{GS} =-10V
Qgs *1, 2	-	2.4	-		
Qgd *1, 2	-	2.2	-		
t _{d(ON)} *1, 2	-	8	-	ns	V _{DS} =-75V, I _D =-1A, V _{GS} =-10V, R _G =6Ω
t _r *1, 2	-	6	-		
t _{d(OFF)} *1, 2	-	20	-		
t _f *1, 2	-	4	-		
C _{iss}	-	475	-	pF	V _{DS} =-25V, V _{GS} =0V, f=1MHz
C _{oss}	-	31	-		
C _{rss}	-	13	-		
Source-Drain Diode					
I _S *1	-	-	-4	A	T _C =25°C
I _{SM} *3	-	-	-16		
V _{SD} *1	-	-0.78	-1.2	V	I _F =-1A, V _{GS} =0V
t _{rr}	-	60	-	ns	I _F =-1A, dI _F /dt=100A/μs
Q _{rr}	-	120	-	nC	

Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%
 *2.Independent of operating temperature
 *3.Pulse width limited by maximum junction temperature.

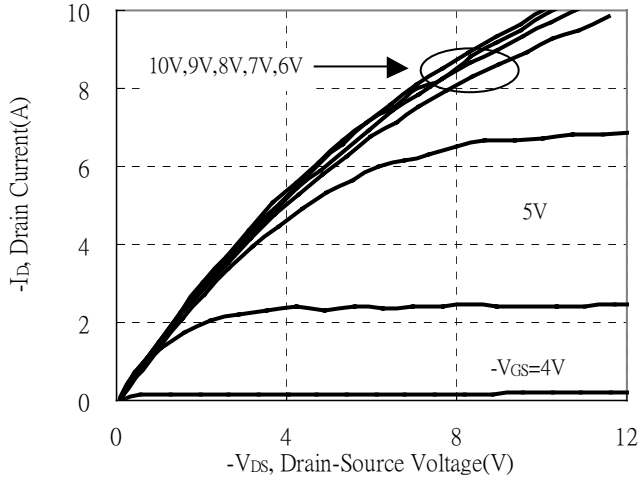
Recommended Soldering Footprint



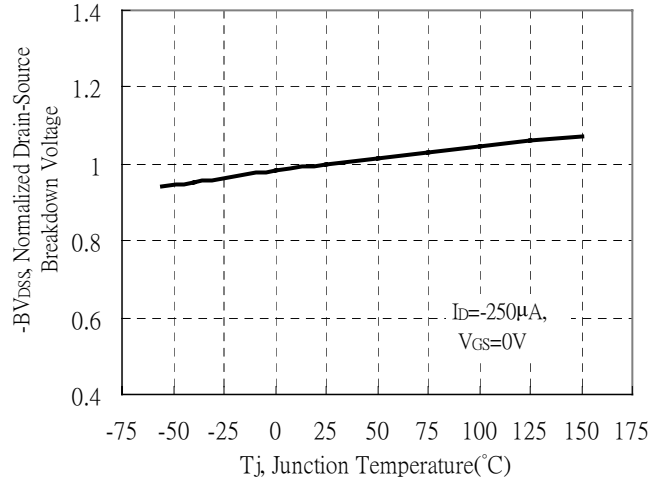
unit : mm

Typical Characteristics

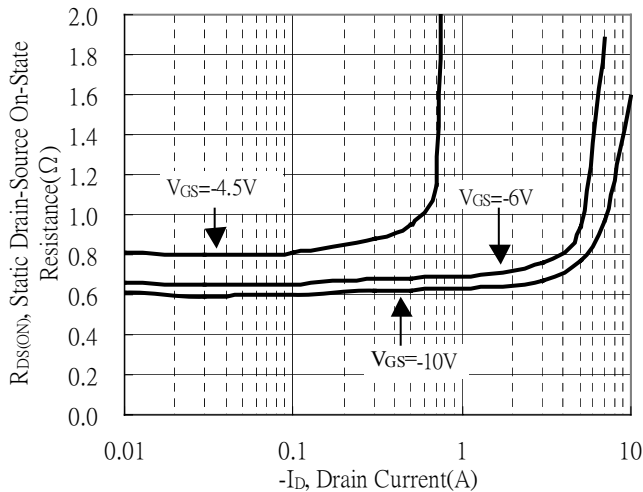
Typical Output Characteristics



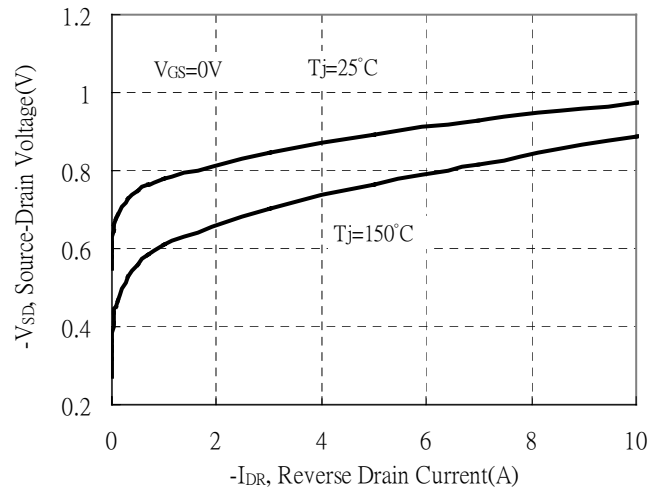
Breakdown Voltage vs Ambient Temperature



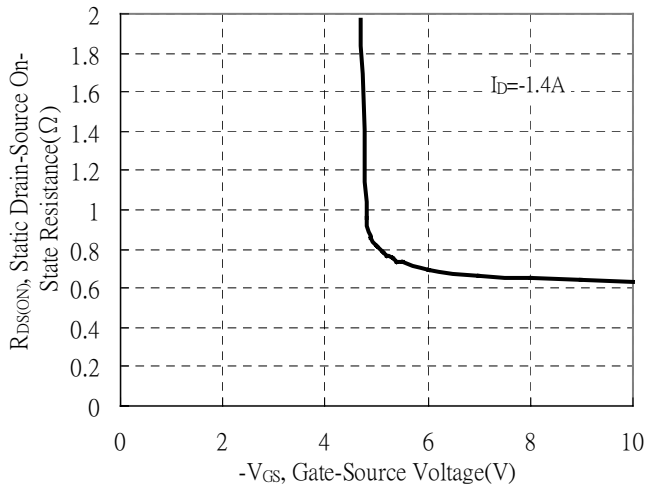
Static Drain-Source On-State resistance vs Drain Current



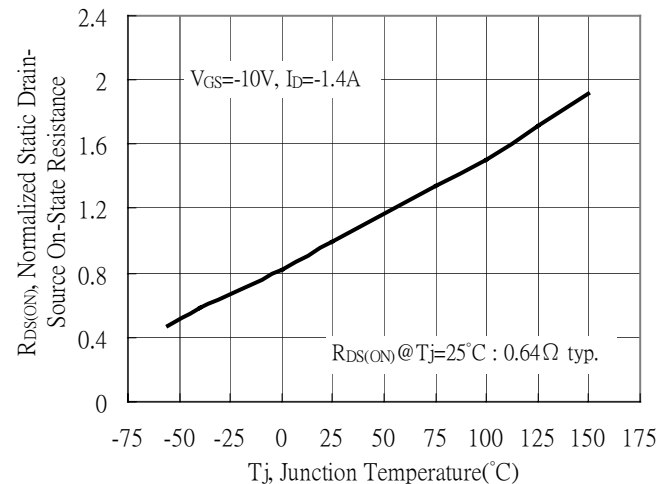
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

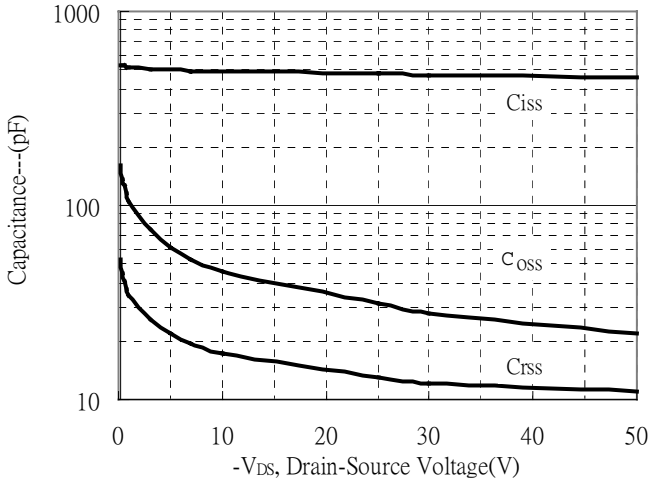


Drain-Source On-State Resistance vs Junction Temperature

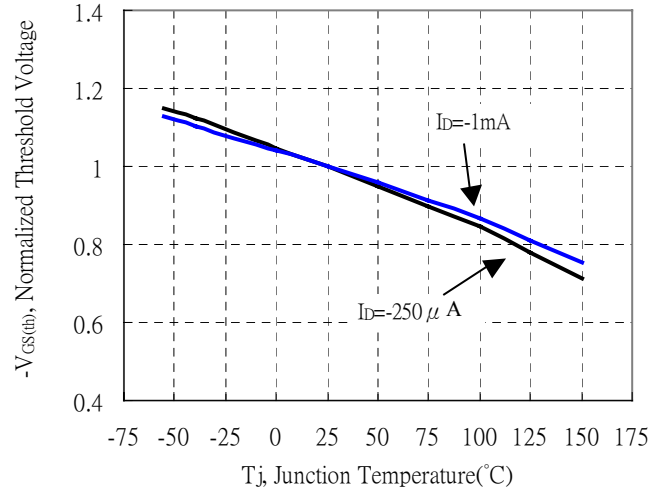


Typical Characteristics(Cont.)

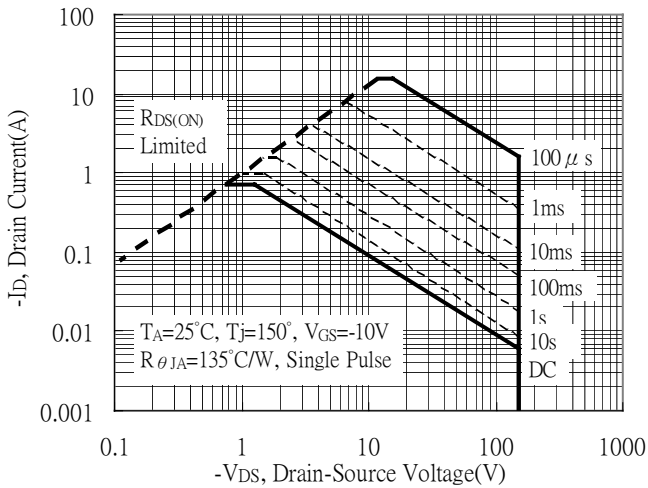
Capacitance vs Drain-to-Source Voltage



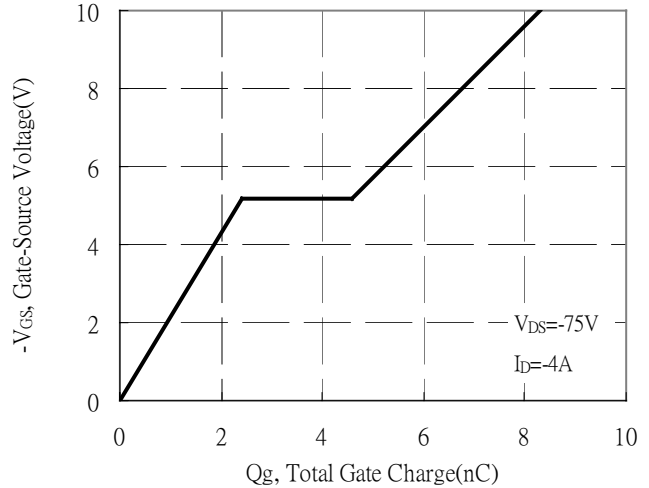
Threshold Voltage vs Junction Temperature



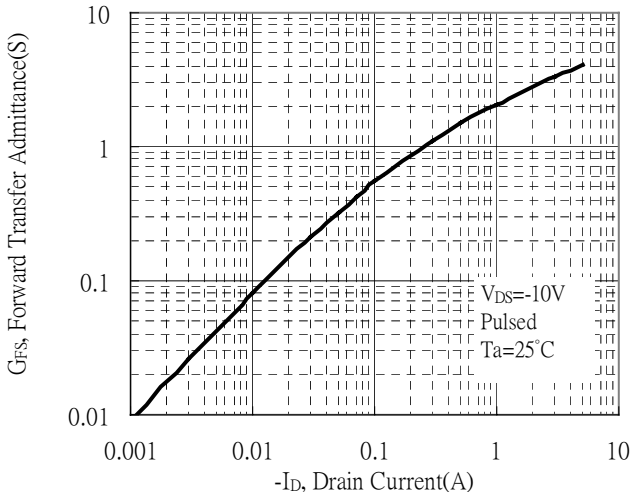
Maximum Safe Operating Area



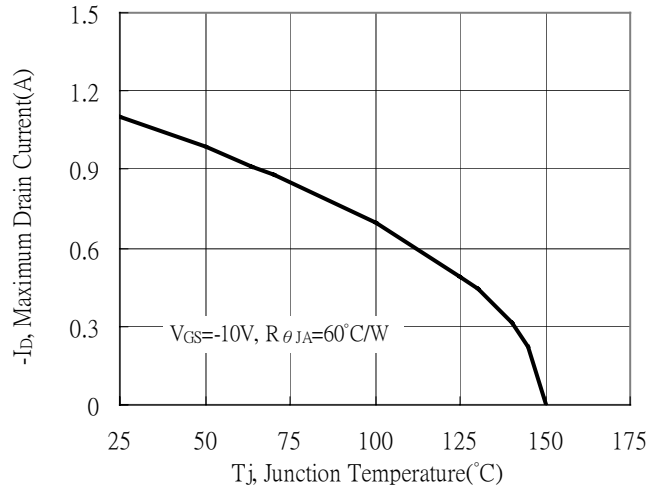
Gate Charge Characteristics



Forward Transfer Admittance vs Drain Current

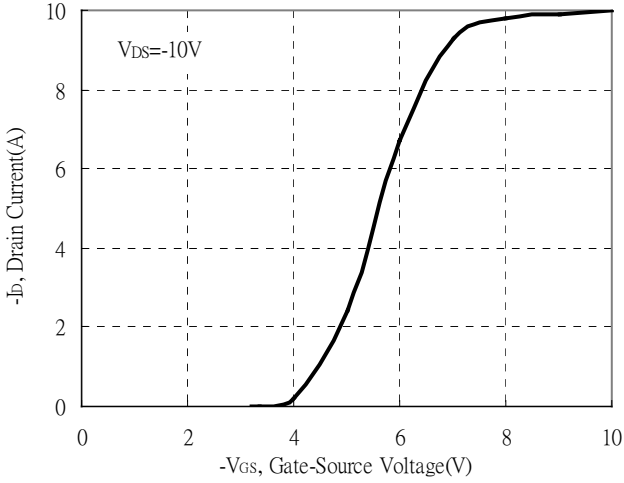


Maximum Drain Current vs Junction Temperature

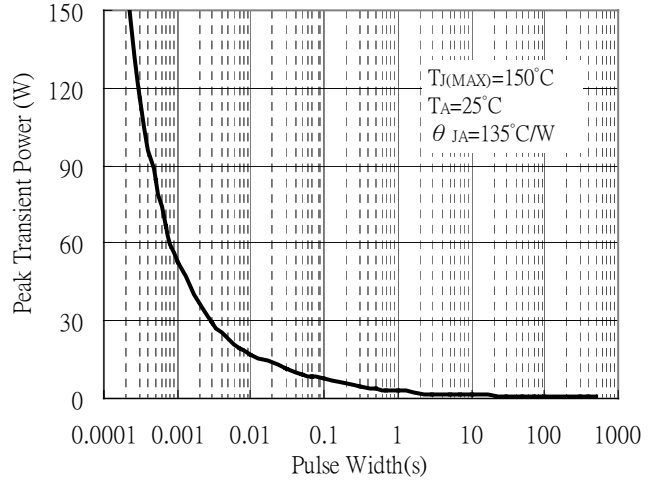


Typical Characteristics(Cont.)

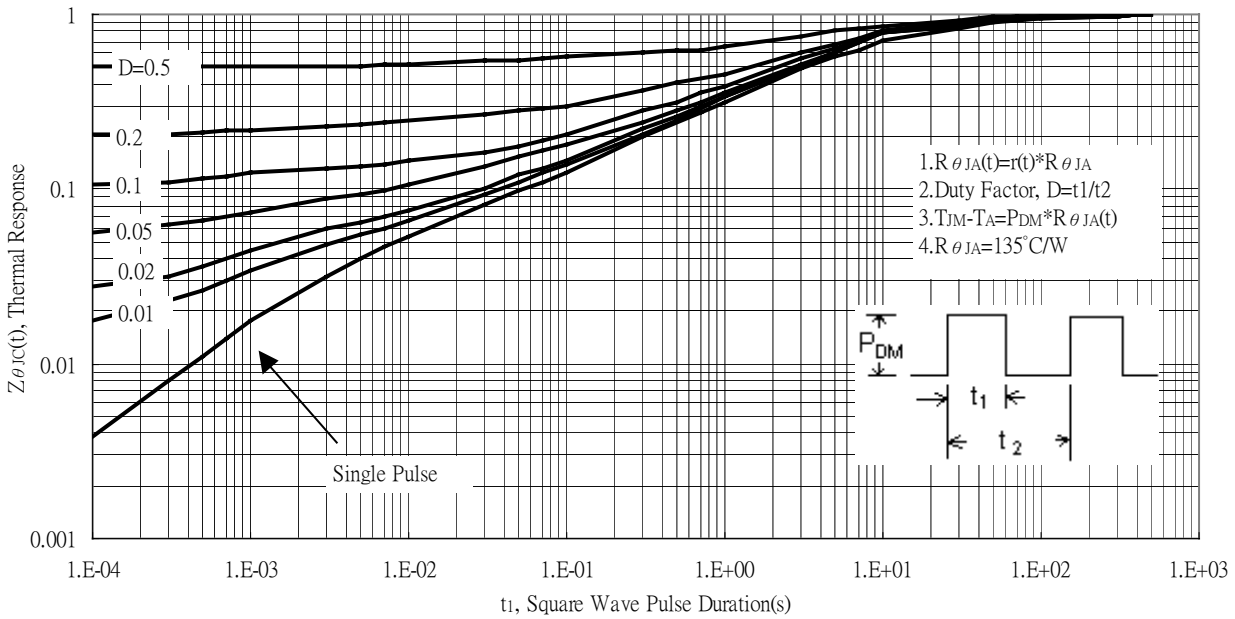
Typical Transfer Characteristics



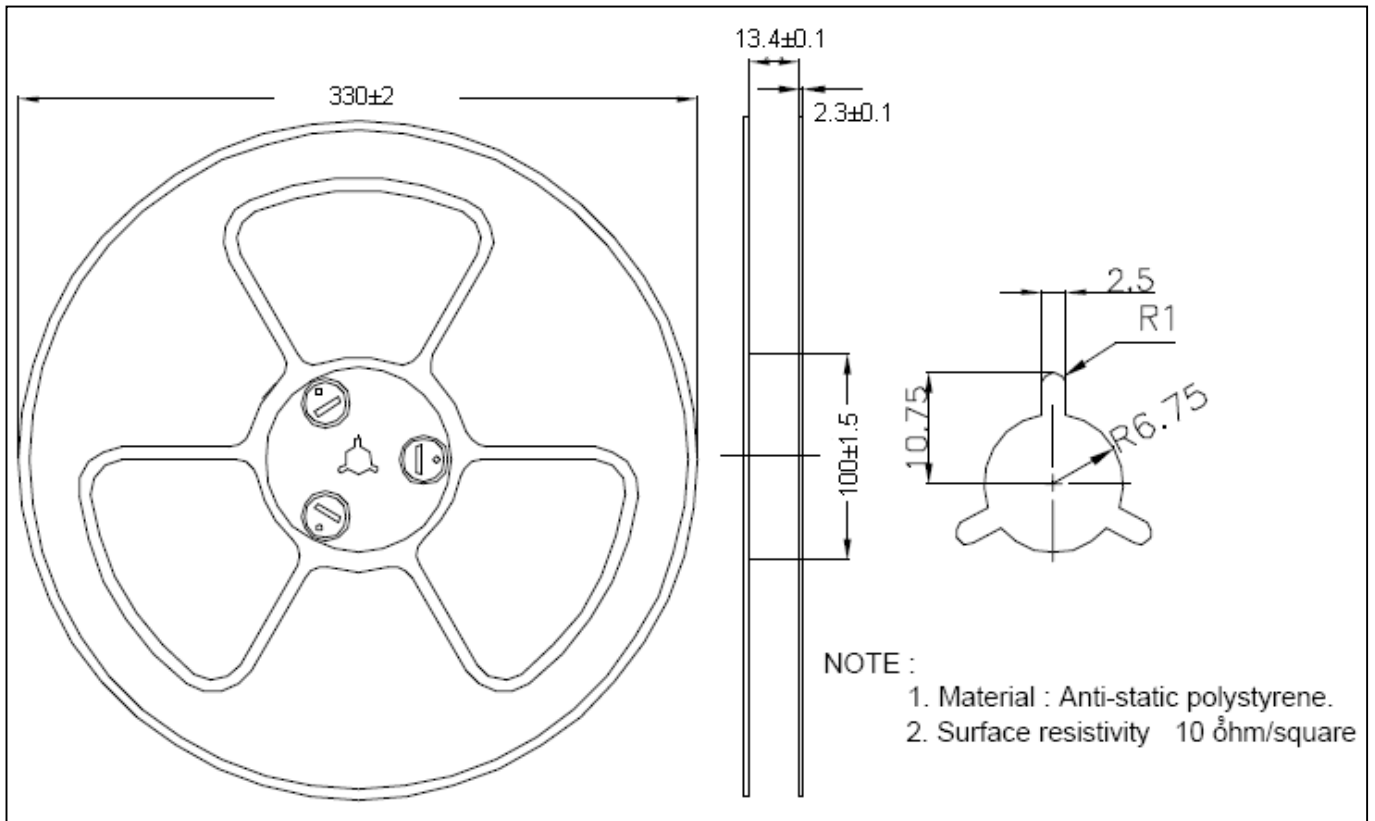
Single Pulse Maximum Power Dissipation



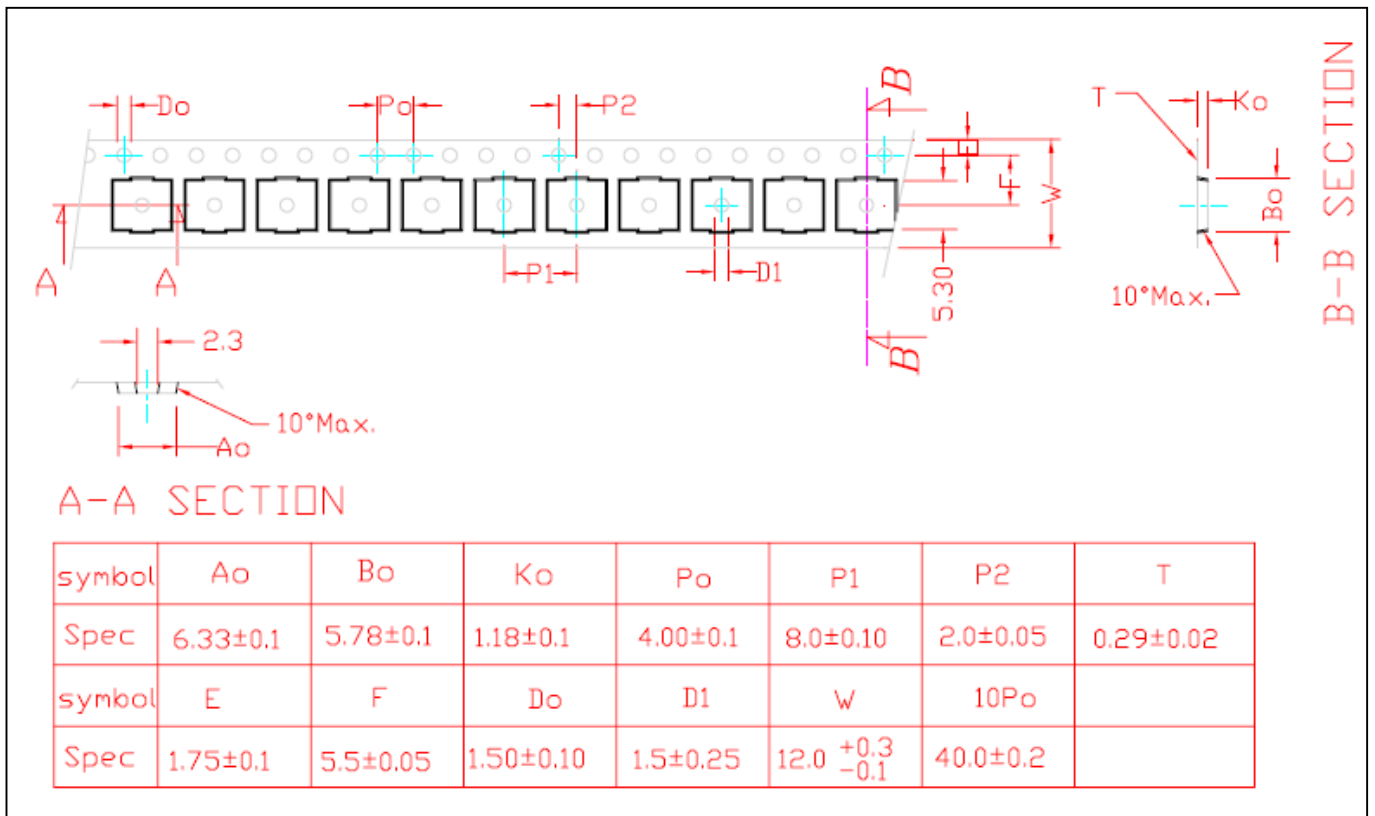
Transient Thermal Response Curves



Reel Dimension



Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

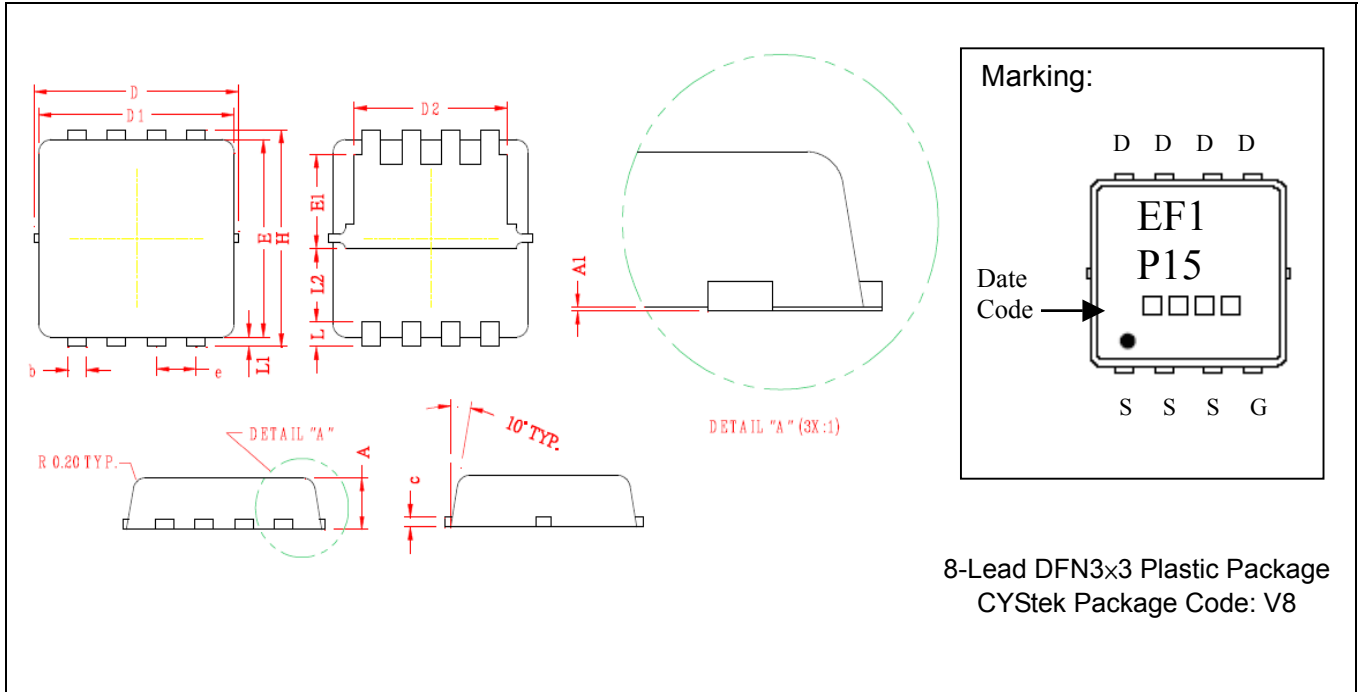
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

DFN3x3 Dimension



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.0276	0.0354	0.70	0.90	E	0.1181	0.1260	3.00	3.20
A1	0.0000	0.0197	0.00	0.50	E1	0.0531	0.0610	1.35	1.55
b	0.0094	0.0138	0.24	0.35	e	0.0256	BSC	0.65	BSC
c	0.0039	0.0079	0.10	0.20	H	0.1260	0.1339	3.20	3.40
D	0.1280	0.1339	3.25	3.40	L	0.0118	0.0197	0.30	0.50
D1	0.1201	0.1280	3.05	3.25	L1	0.0039	0.0079	0.10	0.20
D2	0.0945	0.1024	2.40	2.60	L2	0.0445	REF	1.13	REF

- Notes:** 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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