

P-Channel Enhancement Mode MOSFET

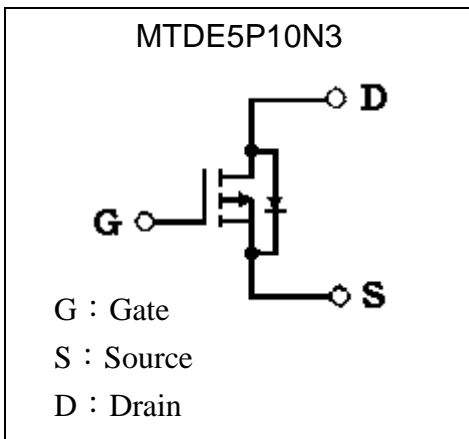
MTDE5P10N3

BV_{DSS}	-100V
I_D	-1.1A
R_{DS(ON)}@ V_{GS}=-10V, I_D=-1A	450mΩ (typ)
R_{DS(ON)}@ V_{GS}=-6V, I_D=-1A	472mΩ (typ)

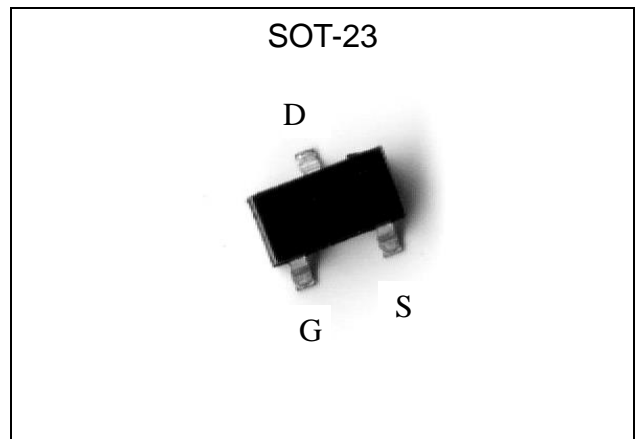
Features

- Advanced trench process technology
- High density cell design for ultra low on resistance
- Low gate charge
- Compact and low profile SOT-23 package
- Pb-free & halogen-free package

Equivalent Circuit

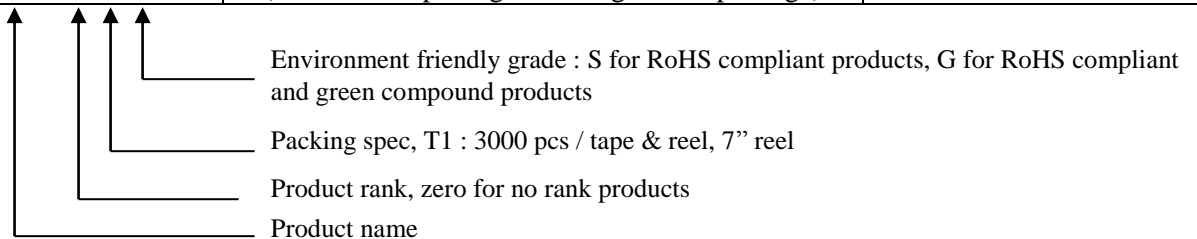


Outline



Ordering Information

Device	Package	Shipping
MTDE5P10N3-0-T1-G	SOT-23 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel





Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V _{DS}	-100	V
Gate-Source Voltage	V _{GS}	±20	
Continuous Drain Current @T _A =25°C (Note 1)	I _D	-1.1	A
Continuous Drain Current @T _A =70°C (Note 1)	I _D	-0.9	
Pulsed Drain Current (Note 2)	I _{DM}	-8	
Maximum Power Dissipation	P _D	1.38	W
Linear Derating Factor		0.01	W/°C
Operating Junction and Storage Temperature	T _j , T _{stg}	-55~+150	°C

Note : 1. Surface mounted on 1 in²copper pad of FR-4 board, 270°C/W when mounted on minimum copper pad.
 2. Pulse width limited by maximum junction temperature.

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient (Note)	R _{th,ja}	90	°C/W
Thermal Resistance, Junction-to-Case	R _{th,jc}	75	

Note : Surface mounted on 1 in²copper pad of FR-4 board, 270°C/W when mounted on minimum copper pad.

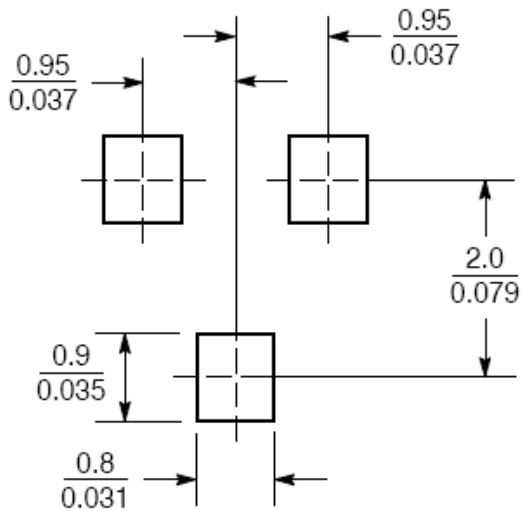
Electrical Characteristics (Tj=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-100	-	-	V	V _{GS} =0V, I _D =-250μA
V _{GS(th)}	-1.0	-	-3.0		V _{DS} =V _{GS} , I _D =-250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	-1	μA	V _{DS} =-80V, V _{GS} =0V
	-	-	-25		V _{DS} =-80V, V _{GS} =0V, T _j =55°C
*R _{DS(ON)}	-	450	700	mΩ	I _D =-1A, V _{GS} =-10V
	-	472	750		I _D =-1A, V _{GS} =-6V
*G _{FS}	-	1.9	-	S	V _{DS} =-15V, I _D =-1A
Dynamic					
C _{iss}	-	479	-	pF	V _{DS} =-30V, V _{GS} =0, f=1MHz
C _{oss}	-	16	-		
C _{rss}	-	12	-		
*t _{d(ON)}	-	5	-	ns	V _{DS} =-50V, V _{GS} =-10V, I _D =-1A, R _G =6Ω
*t _r	-	8	-		
*t _{d(OFF)}	-	18	-		
*t _f	-	4	-		

*Qg	-	7.7	-	nC	V _{DS} =-80V, I _D =-1A, V _{GS} =-10V
*Qgs	-	1.6	-		
*Qgd	-	1.9	-		
Source-Drain Diode					
*I _S	-	-	-1.1	A	
*V _{SD}	-	-0.81	-1.2	V	V _{GS} =0V, I _{SD} =-1.0A
*trr	-	35	-	ns	I _F =-1A, dI _F /dt=100A/μs
*Qrr	-	50	-	nC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

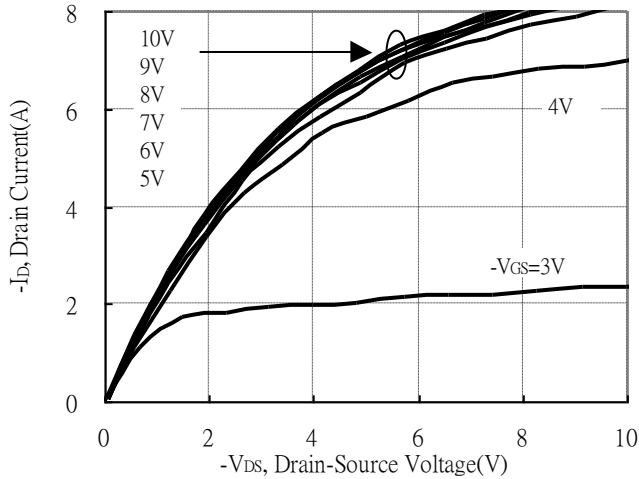
Recommended Soldering Footprint



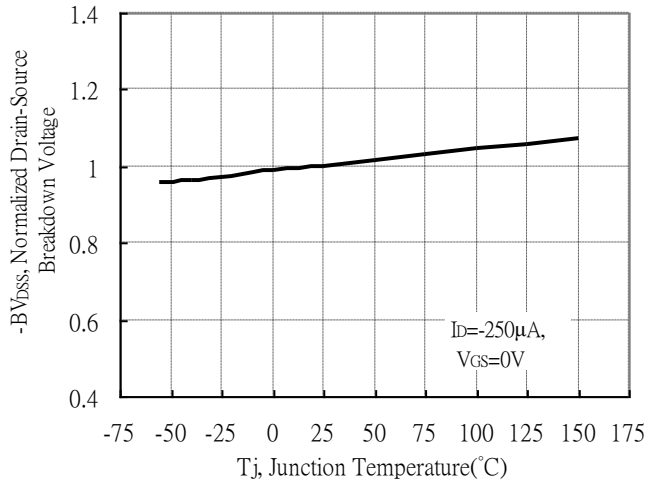
Unit : $\frac{\text{mm}}{\text{inches}}$

Typical Characteristics

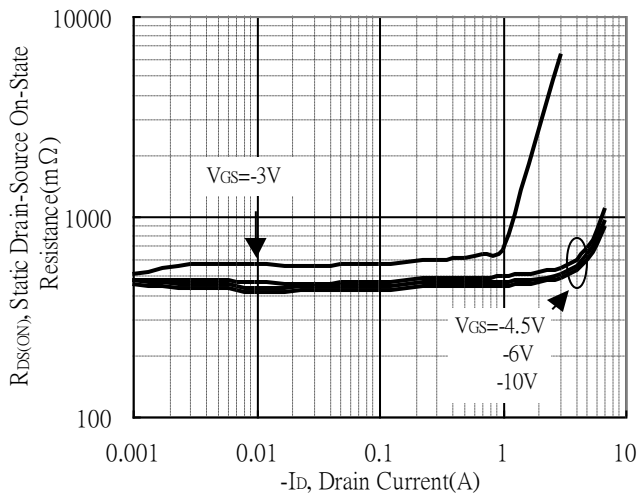
Typical Output Characteristics



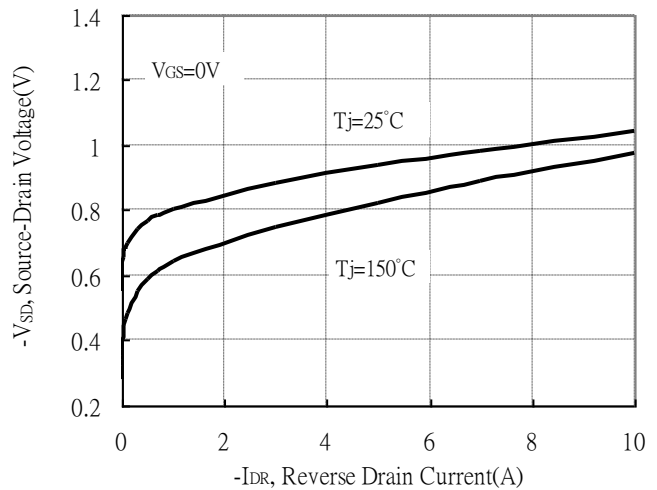
Breakdown Voltage vs Ambient Temperature



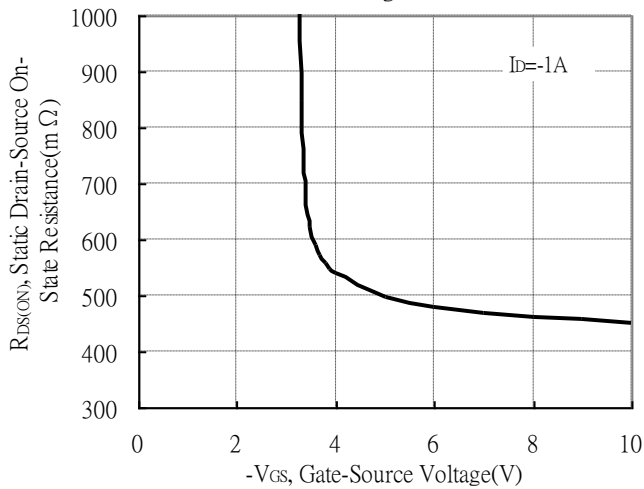
Static Drain-Source On-State resistance vs Drain Current



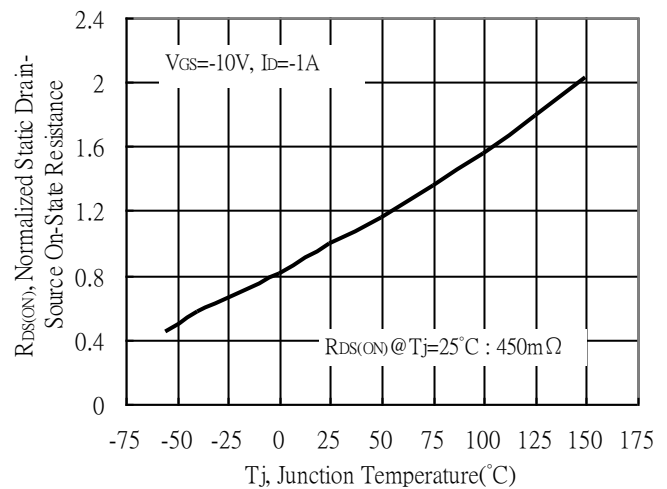
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

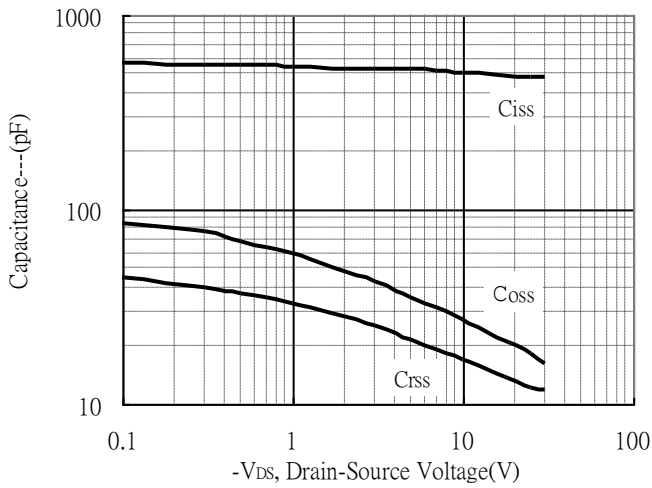


Drain-Source On-State Resistance vs Junction Temperature

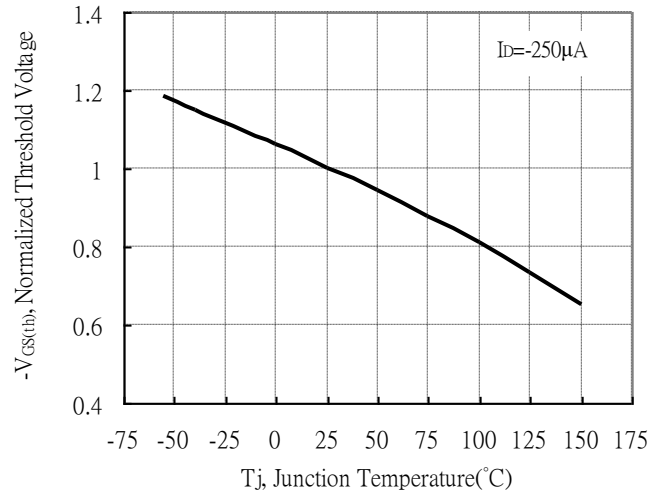


Typical Characteristics(Cont.)

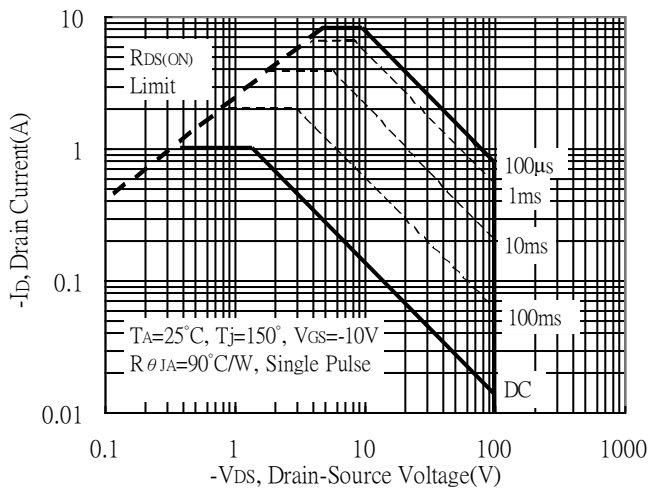
Capacitance vs Drain-to-Source Voltage



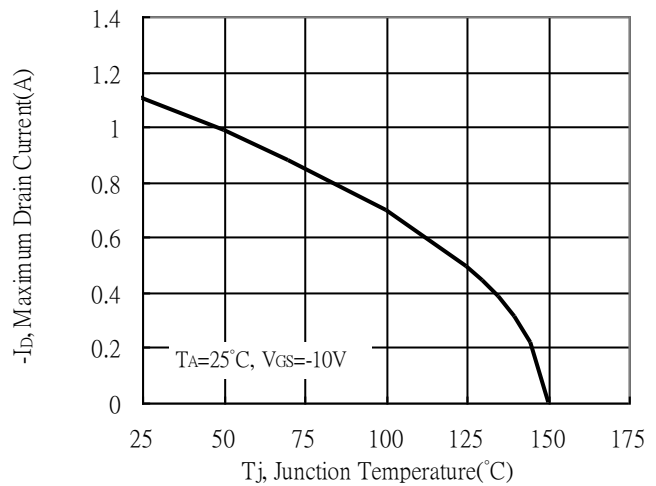
Threshold Voltage vs Junction Temperature



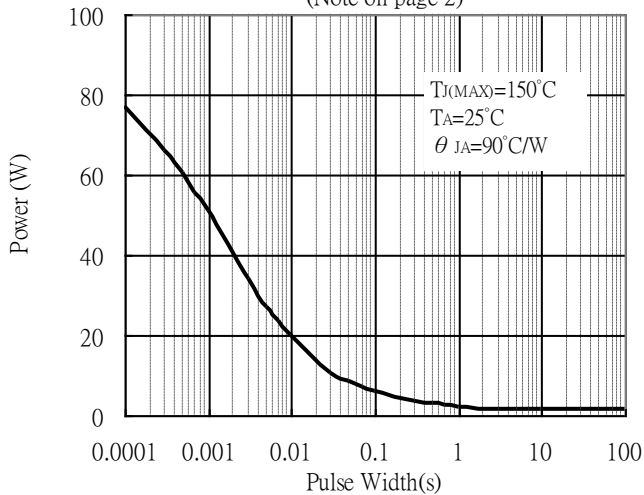
Maximum Safe Operating Area



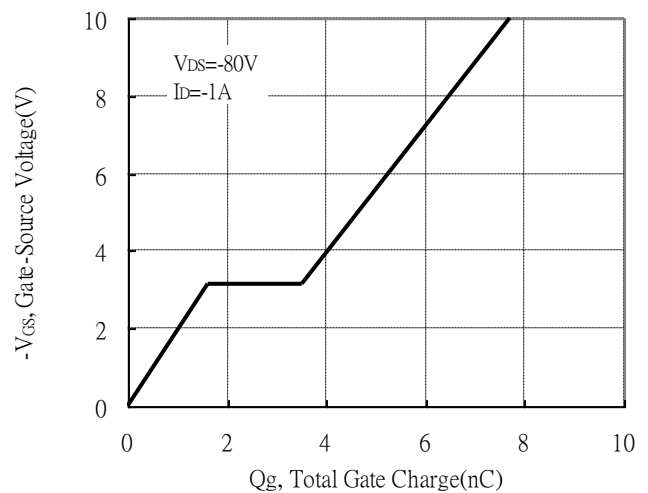
Maximum Drain Current vs Junction Temperature



Single Pulse Power Rating, Junction to Ambient
(Note on page 2)

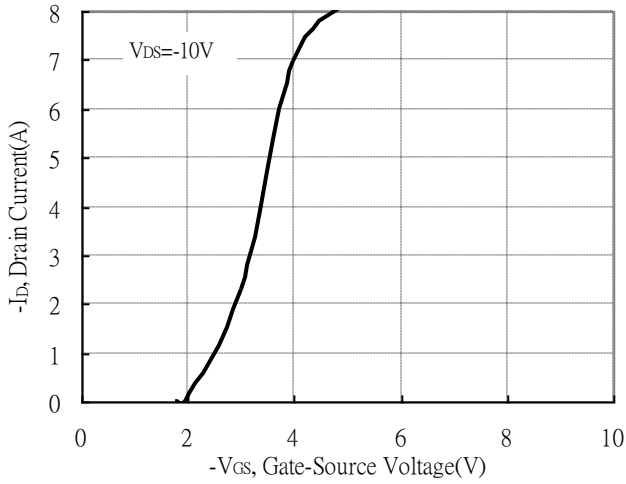


Gate Charge Characteristics

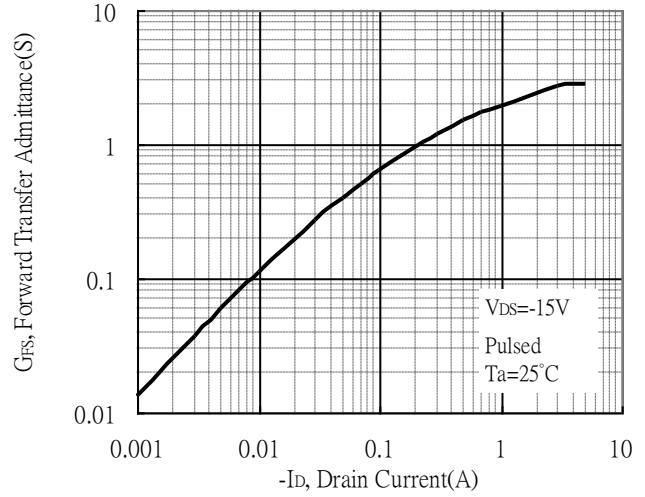


Typical Characteristics(Cont.)

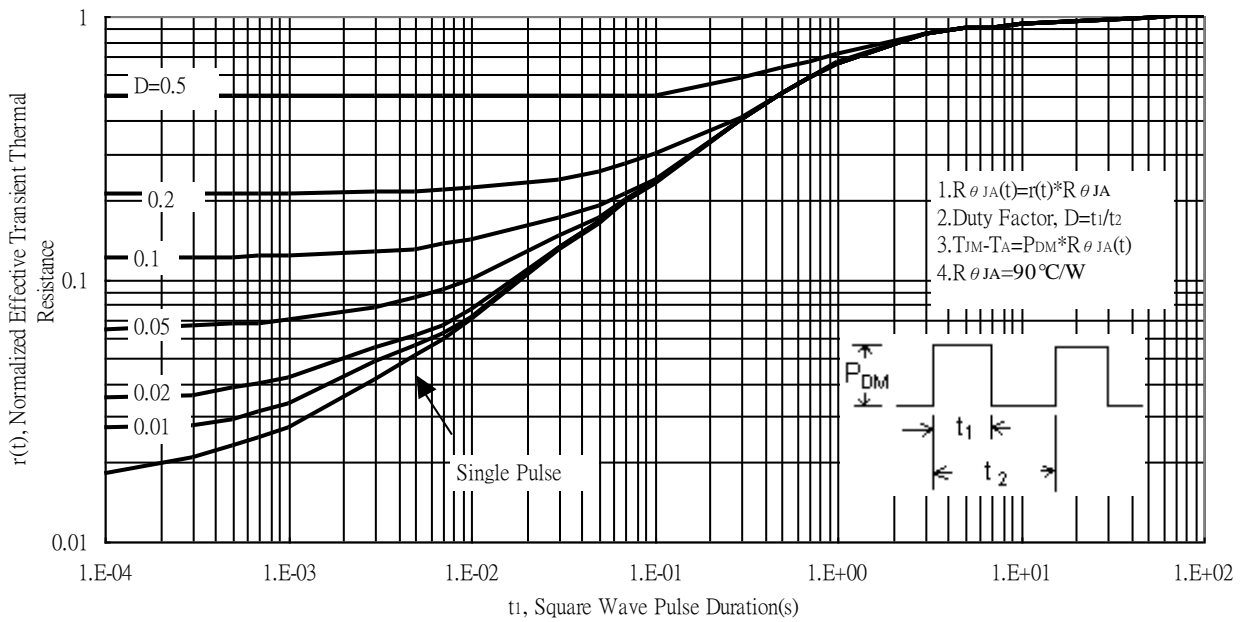
Typical Transfer Characteristics



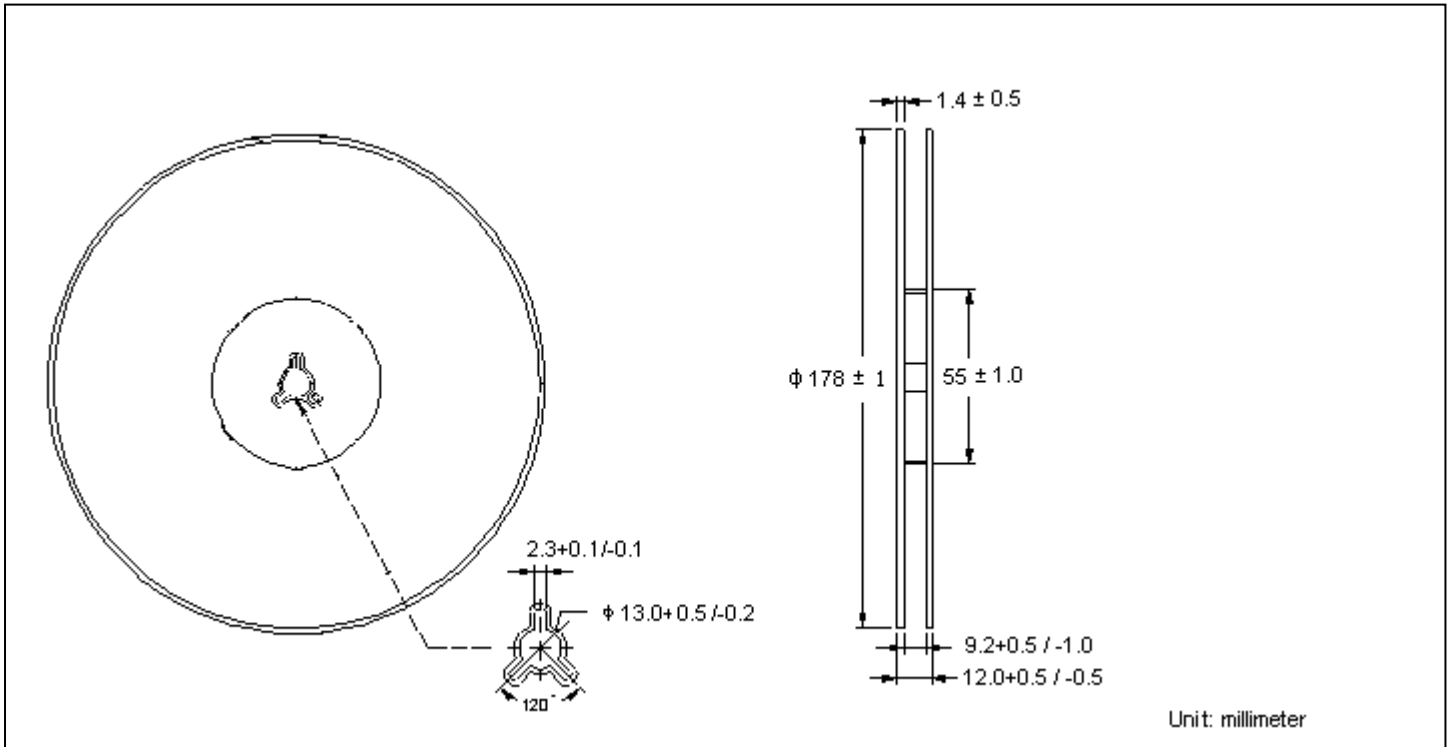
Forward Transfer Admittance vs Drain Current



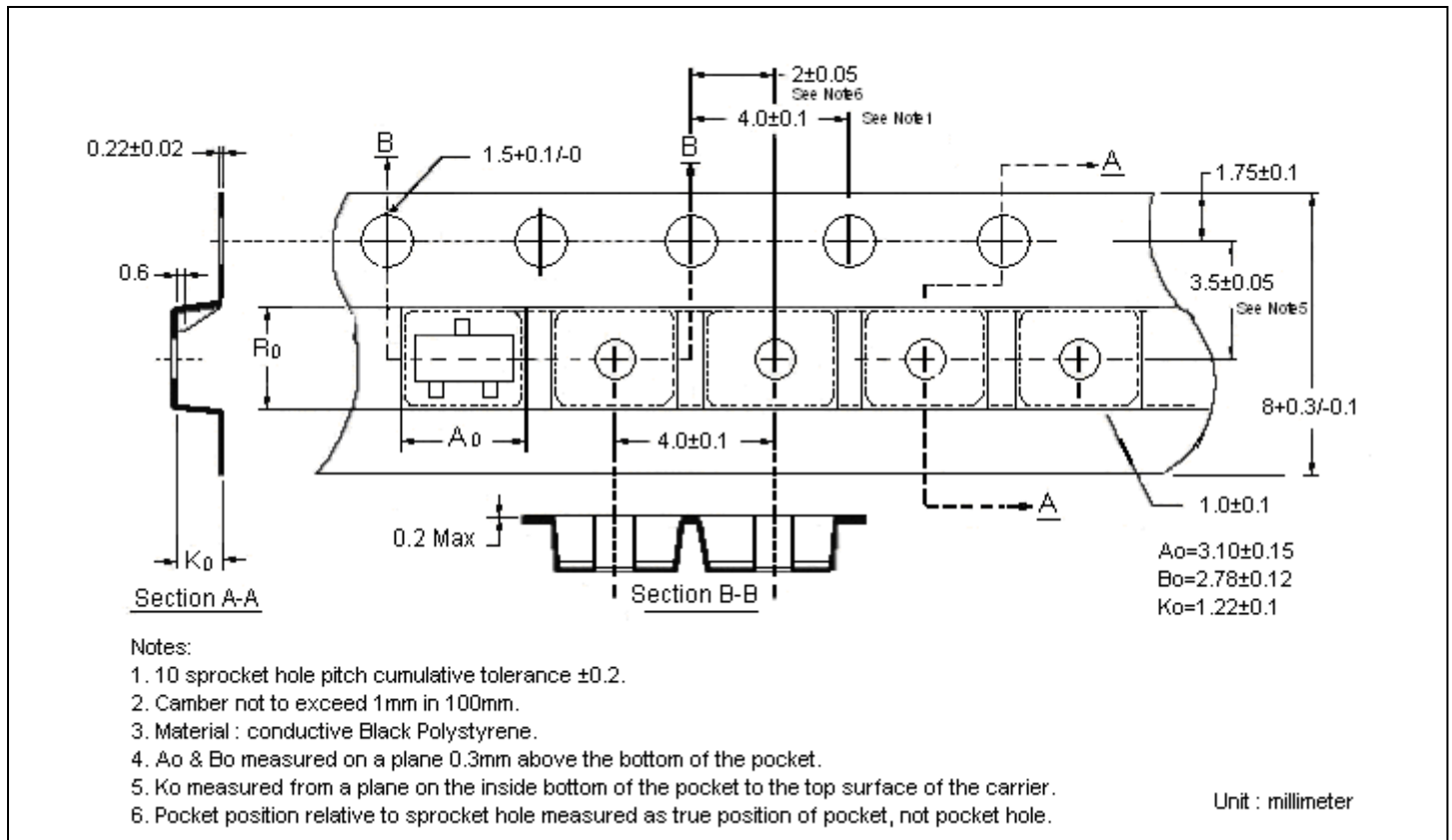
Transient Thermal Response Curves



Reel Dimension

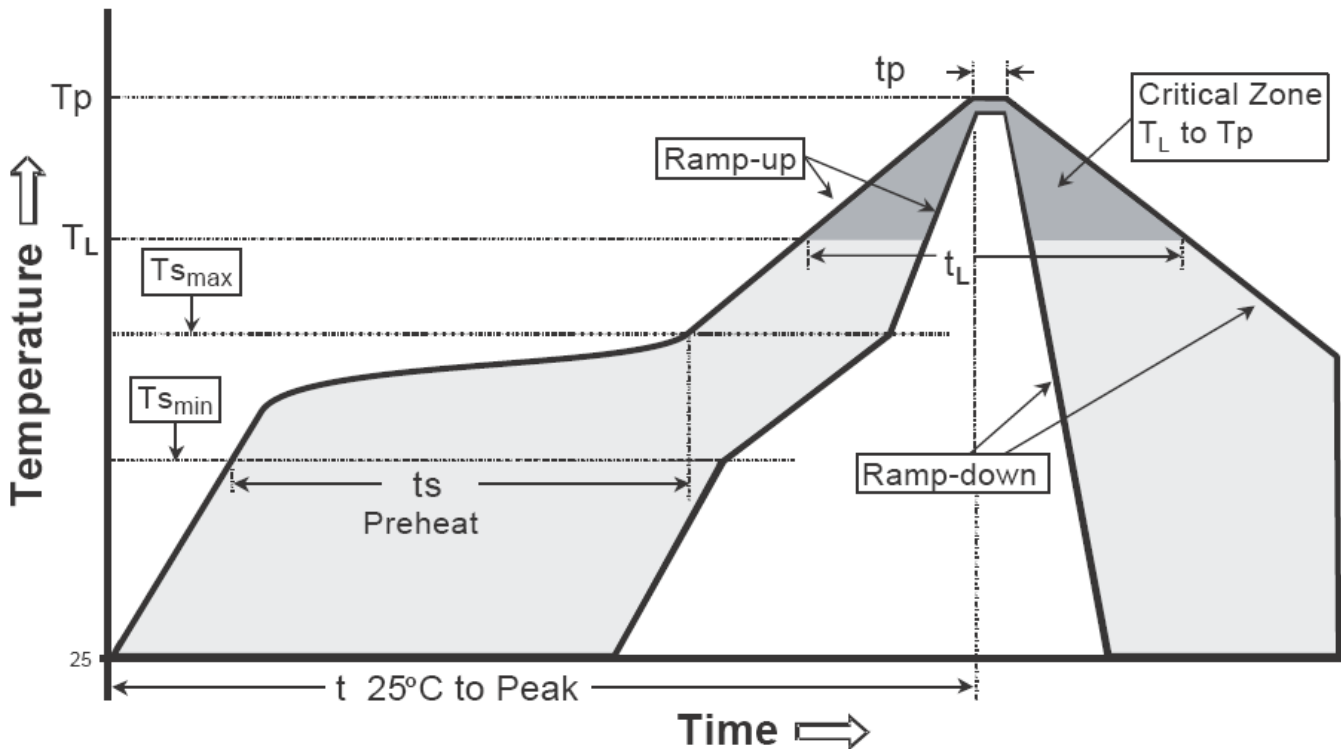


Carrier Tape Dimension



Recommended wave soldering condition

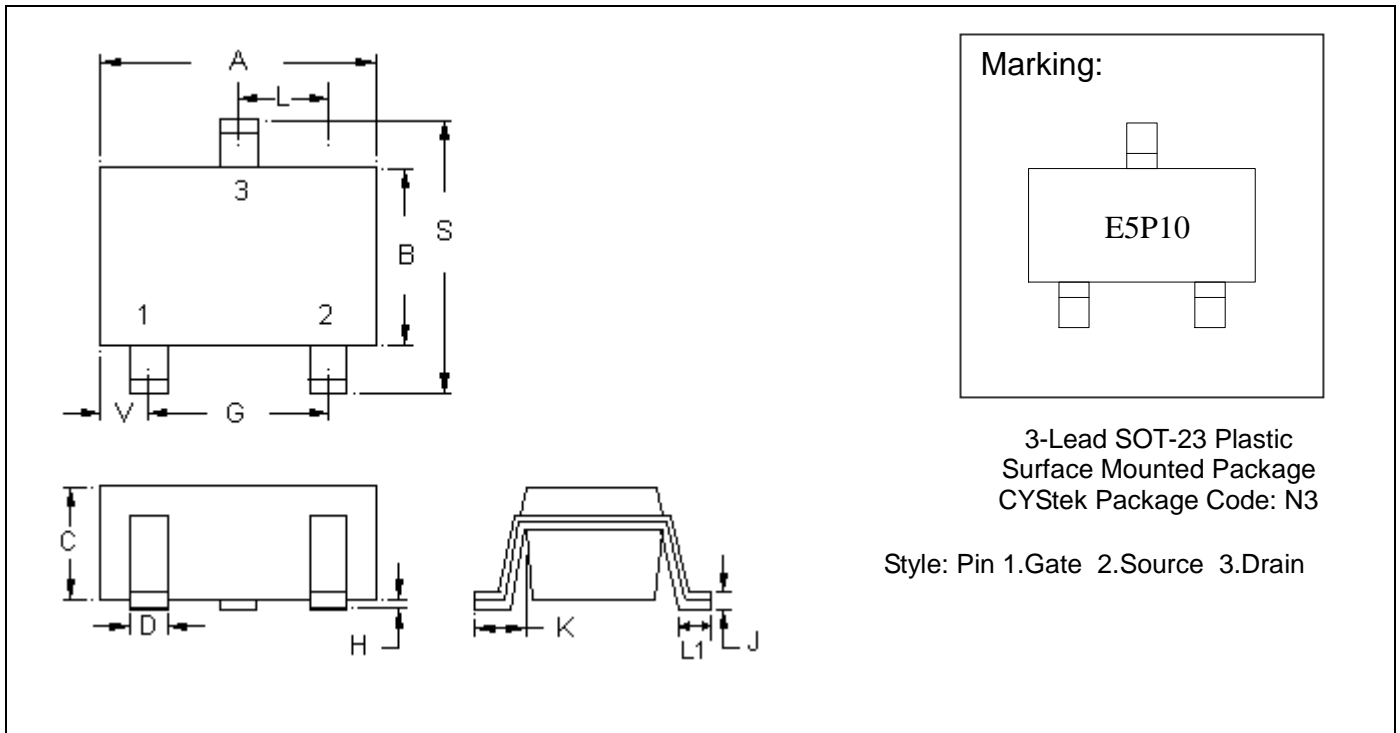
Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _p)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOT-23 Dimension



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1102	0.1204	2.80	3.04	J	0.0032	0.0079	0.08	0.20
B	0.0472	0.0669	1.20	1.70	K	0.0118	0.0266	0.30	0.67
C	0.0335	0.0512	0.89	1.30	L	0.0335	0.0453	0.85	1.15
D	0.0118	0.0197	0.30	0.50	S	0.0830	0.1161	2.10	2.95
G	0.0669	0.0910	1.70	2.30	V	0.0098	0.0256	0.25	0.65
H	0.0000	0.0040	0.00	0.10	L1	0.0118	0.0197	0.30	0.50

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.