

**40V N-Channel Enhancement Mode MOSFET**

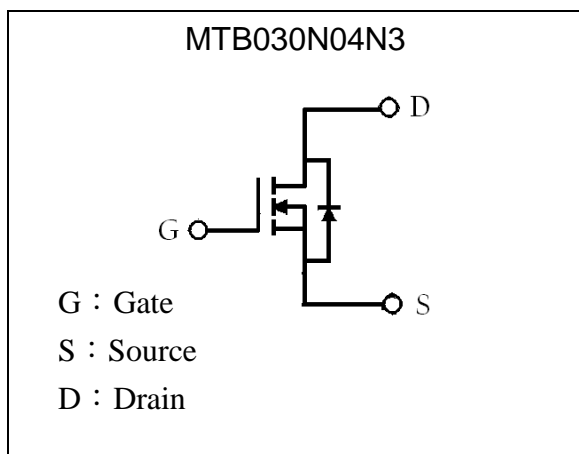
# MTB030N04N3

$BV_{DSS}$		40V
$I_D @ V_{GS}=10V$		8A
$R_{DS(on)(TYP)}$	$V_{GS}=10V, I_D=7.9A$	25.3m $\Omega$
	$V_{GS}=4.5V, I_D=7.3A$	34.2m $\Omega$

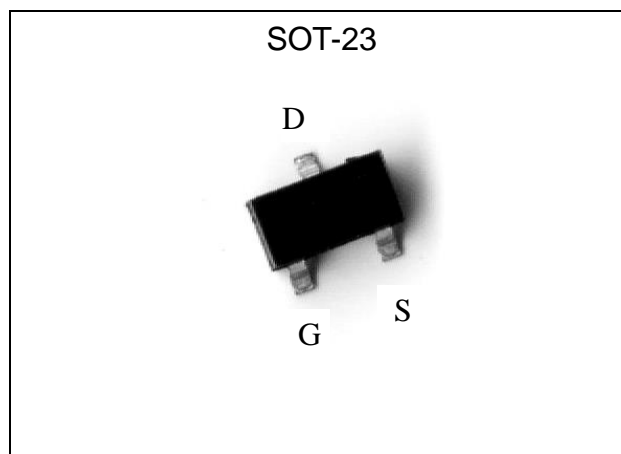
**Features**

- Low on-resistance
- Low voltage gate drive
- Excellent thermal and electrical capabilities
- Pb-free lead plating and halogen-free package

**Equivalent Circuit**

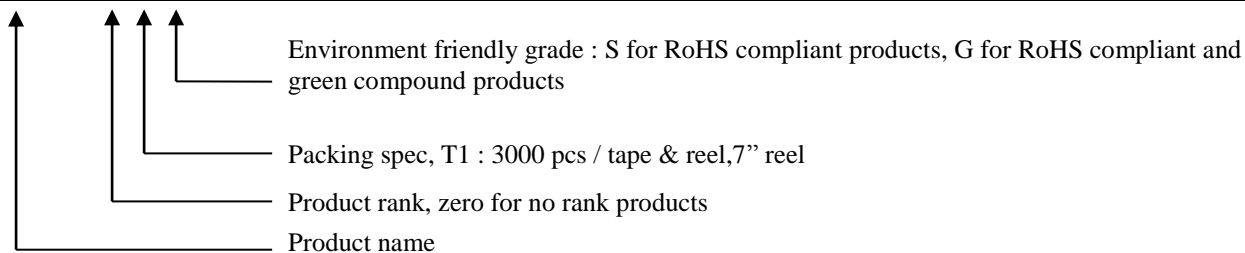


**Outline**



**Ordering Information**

Device	Package	Shipping
MTB030N04N3-0-T1-G	SOT-23 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel





**Absolute Maximum Ratings** (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V <sub>DS</sub>	40	V
Gate-Source Voltage		V <sub>GS</sub>	±20	
Continuous Drain Current @ V <sub>GS</sub> =10V, T <sub>C</sub> =25°C		I <sub>D</sub>	8	A
Continuous Drain Current @ V <sub>GS</sub> =10V, T <sub>C</sub> =125°C			4.6	
Continuous Drain Current @ V <sub>GS</sub> =10V, T <sub>A</sub> =25°C (Note 3)		I <sub>DSM</sub>	4.7	
Continuous Drain Current @ V <sub>GS</sub> =10V, T <sub>A</sub> =70°C (Note 3)			3.8	
Pulsed Drain Current (Note 1, 2)		I <sub>DM</sub>	32	
Maximum Power Dissipation	T <sub>C</sub> =25°C	P <sub>D</sub>	3	W
	T <sub>C</sub> =125°C		1	
	T <sub>A</sub> =25°C	P <sub>DSM</sub>	1.25	
	T <sub>A</sub> =70°C		0.8	
Operating Junction and Storage Temperature		T <sub>j</sub> , T <sub>stg</sub>	-55~+175	°C

**Thermal Performance**

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient , max (Note 3)	R <sub>θJA</sub>	100	°C/W
Thermal Resistance, Junction-to-Case , max	R <sub>θJC</sub>	50	

- Note: 1. Pulse width limited by maximum junction temperature.  
 2. Pulse width ≤ 300µs, duty cycle ≤ 2%  
 3. Surface mounted on 1 in<sup>2</sup> copper pad of FR-4 board at steady state; 417°C/W when mounted on minimum copper pad.  
 4. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=175°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.  
 5. The power dissipation P<sub>DSM</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design, and the maximum temperature of 175°C may be used if the PCB allows it.

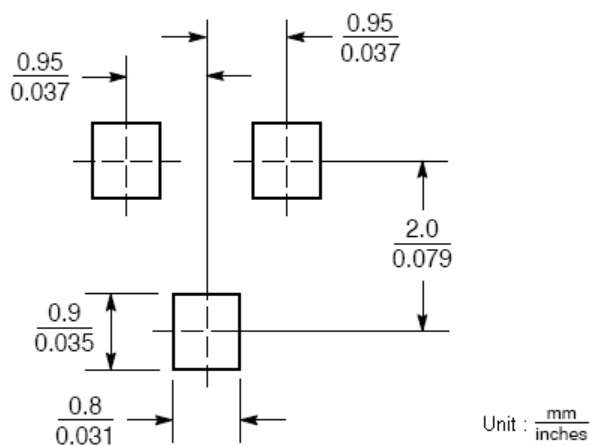
**Electrical Characteristics** (T<sub>j</sub>=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	40	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA
ΔBV <sub>DSS</sub> /ΔT <sub>j</sub>	-	32	-	mV/°C	Reference to 25°C, I <sub>D</sub> =250µA
V <sub>GS(th)</sub>	1.0	-	2.5	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA
G <sub>FS</sub>	-	9.1	-	S	V <sub>DS</sub> =5V, I <sub>D</sub> =7.9A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	1	µA	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V
	-	-	50		V <sub>DS</sub> =40V, V <sub>GS</sub> =0V, T <sub>j</sub> =125°C
	-	-	150		V <sub>DS</sub> =40V, V <sub>GS</sub> =0V, T <sub>j</sub> =175°C
*R <sub>Ds(ON)</sub>	-	25.3	34	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =7.9A
	-	-	47		V <sub>GS</sub> =10V, I <sub>D</sub> =7.9A, T <sub>j</sub> =125°C
	-	-	65		V <sub>GS</sub> =10V, I <sub>D</sub> =7.9A, T <sub>j</sub> =175°C
	-	34.2	46		V <sub>GS</sub> =4.5V, I <sub>D</sub> =7.3A

<b>Dynamic</b>					
C <sub>iss</sub>	-	694	-	pF	V <sub>DS</sub> =20V, V <sub>GS</sub> =0, f=1MHz
C <sub>oss</sub>	-	36	-		
C <sub>rss</sub>	-	40	-		
*t <sub>d(ON)</sub>	-	6.8	-	ns	V <sub>DS</sub> =20V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V, R <sub>G</sub> =1Ω
*t <sub>r</sub>	-	15.8	-		
*t <sub>d(OFF)</sub>	-	22	-		
*t <sub>f</sub>	-	5.4	-		
*Q <sub>g</sub>	-	11	-	nC	V <sub>DS</sub> =20V, I <sub>D</sub> =3.9A, V <sub>GS</sub> =10V
*Q <sub>gs</sub>	-	2.02	-		
*Q <sub>gd</sub>	-	2.05	-		
R <sub>g</sub>	-	1.9	-	Ω	f=1MHz
<b>Source-Drain Diode</b>					
*I <sub>s</sub>	-	-	3.8	A	
*I <sub>SM</sub>	-	-	32		
*V <sub>SD</sub>	-	0.82	1.2	V	V <sub>GS</sub> =0V, I <sub>S</sub> =5.4A
*t <sub>rr</sub>	-	6.7	-	ns	I <sub>F</sub> =3.9A, dI <sub>F</sub> /dt=100A/μs
*Q <sub>rr</sub>	-	2.9	-	nC	

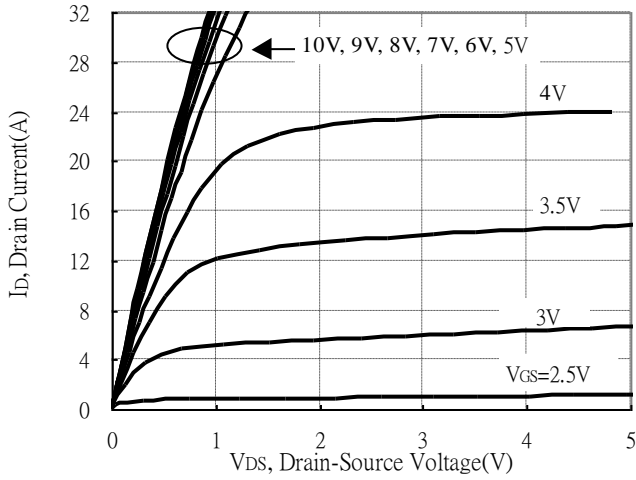
\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

## Recommended Soldering Footprint

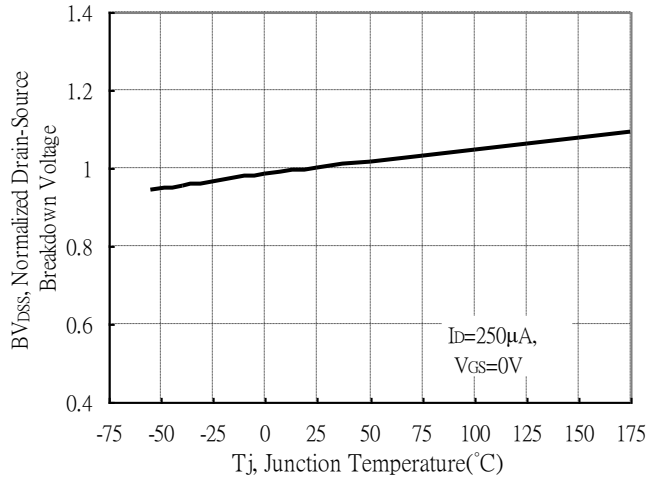


## Typical Characteristics

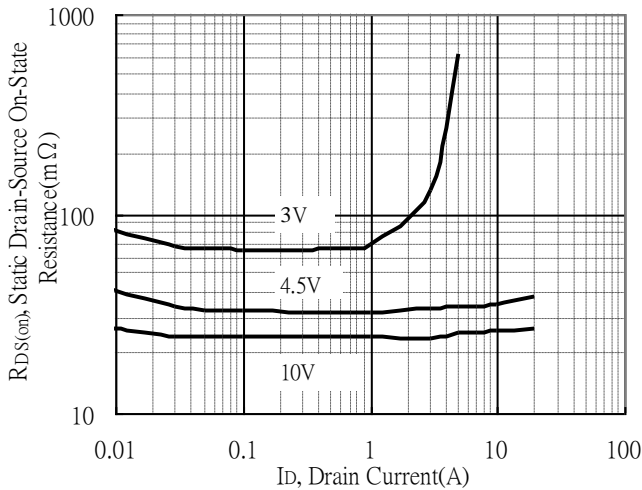
Typical Output Characteristics



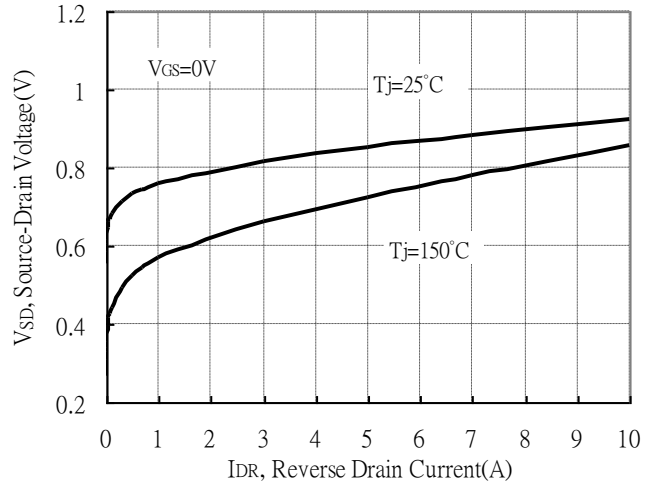
Brekdown Voltage vs Ambient Temperature



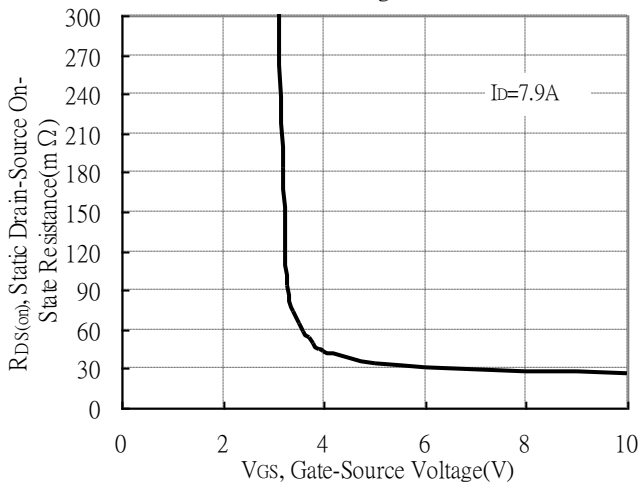
Static Drain-Source On-State resistance vs Drain Current



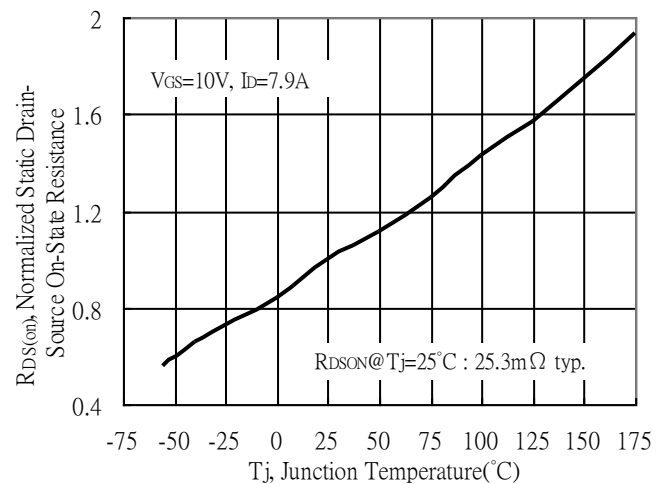
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

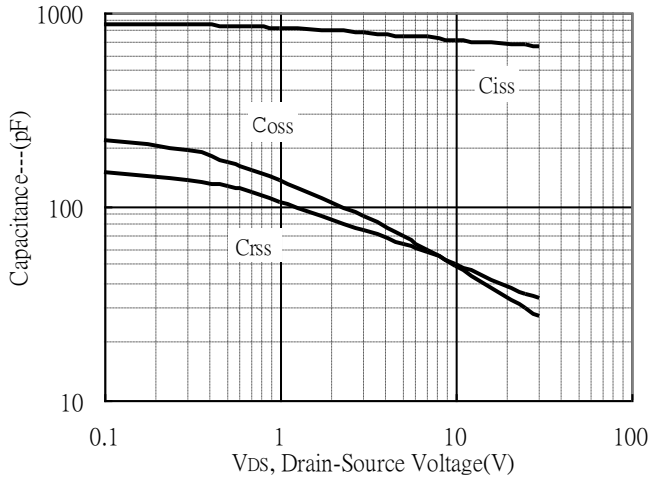


Drain-Source On-State Resistance vs Junction Temperature

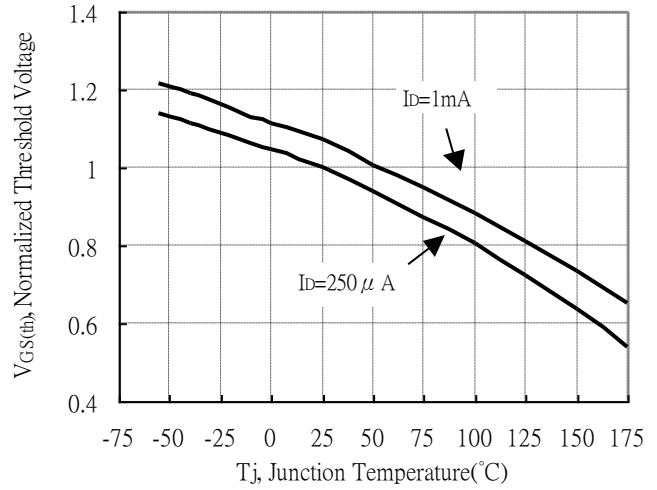


**Typical Characteristics(Cont.)**

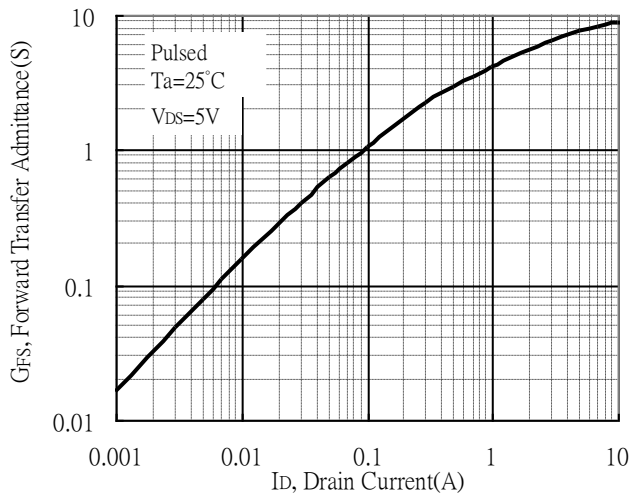
Capacitance vs Drain-to-Source Voltage



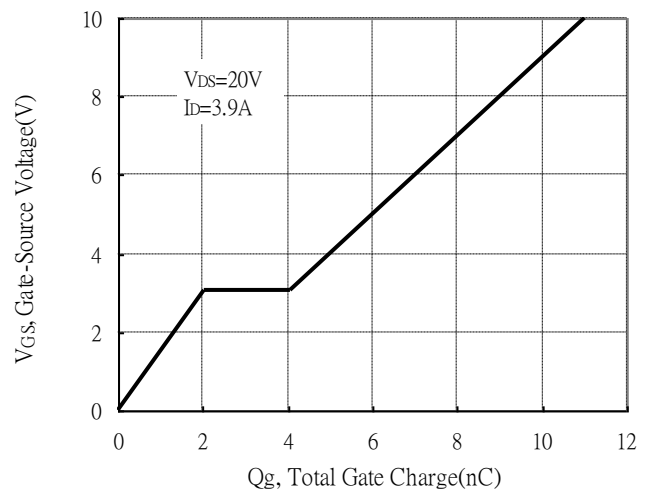
Threshold Voltage vs Junction Temperature



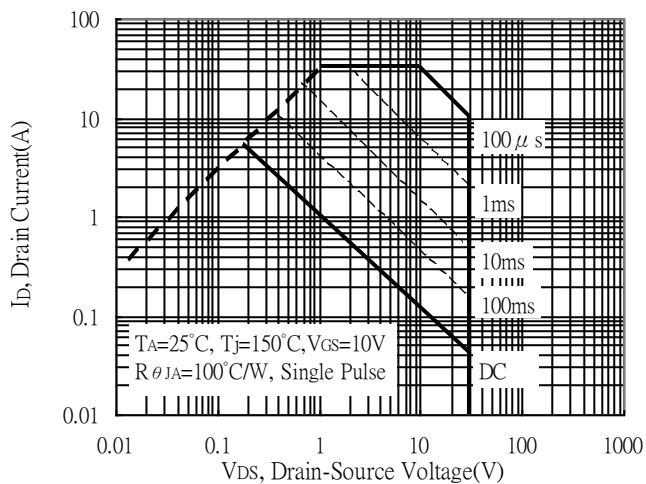
Forward Transfer Admittance vs Drain Current



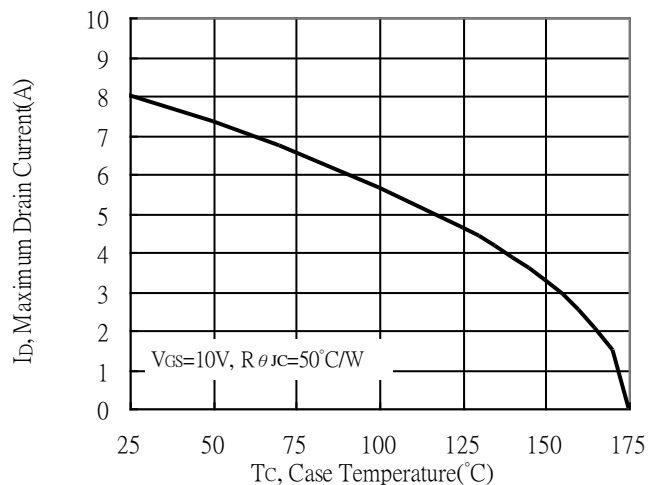
Gate Charge Characteristics



Maximum Safe Operating Area

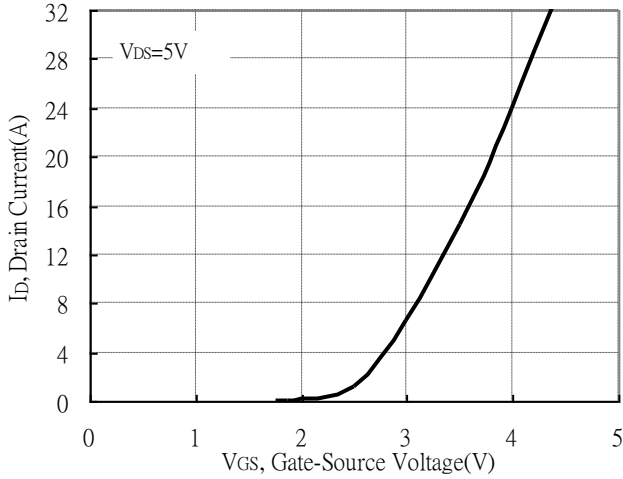


Maximum Drain Current vs Case Temperature

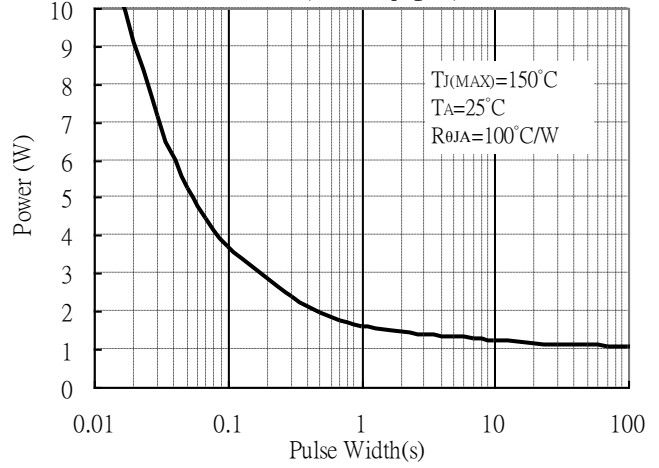


**Typical Characteristics(Cont.)**

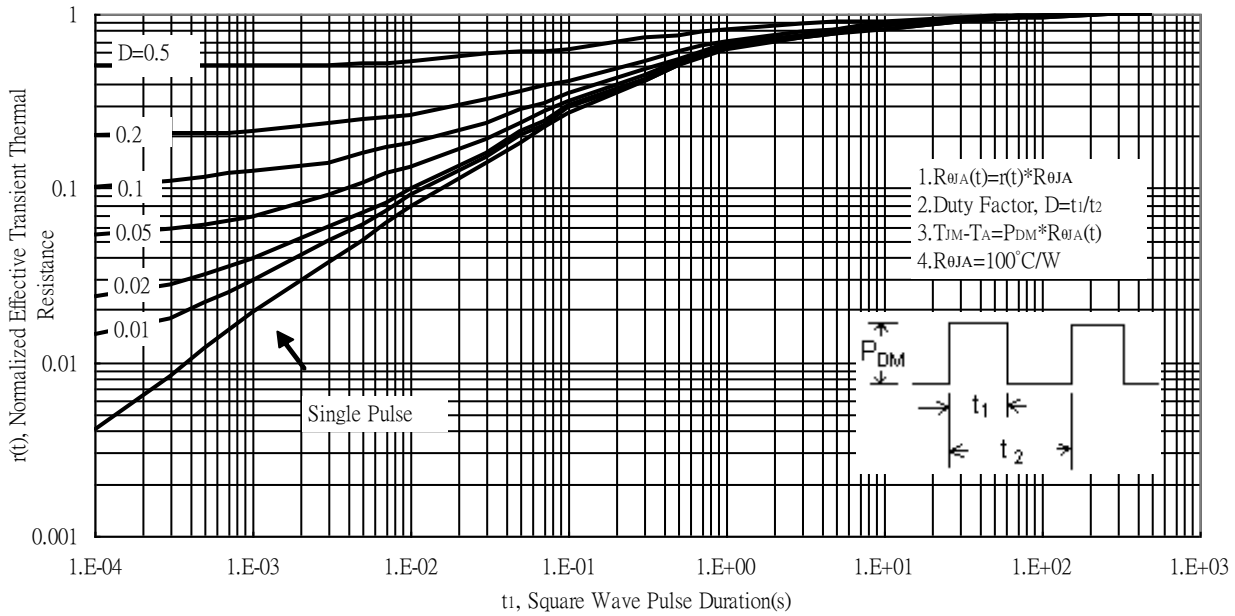
Typical Transfer Characteristics



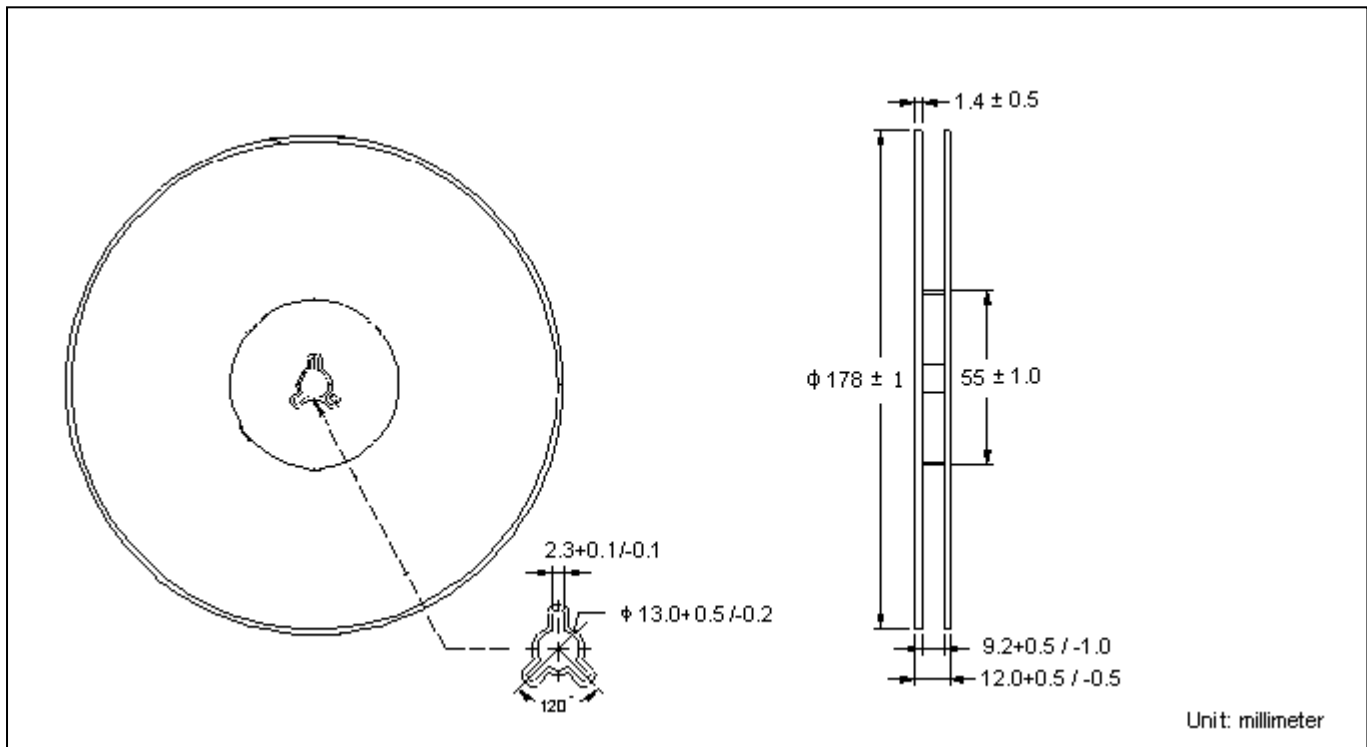
Single Pulse Power Rating, Junction to Ambient  
 (Note on page 2)



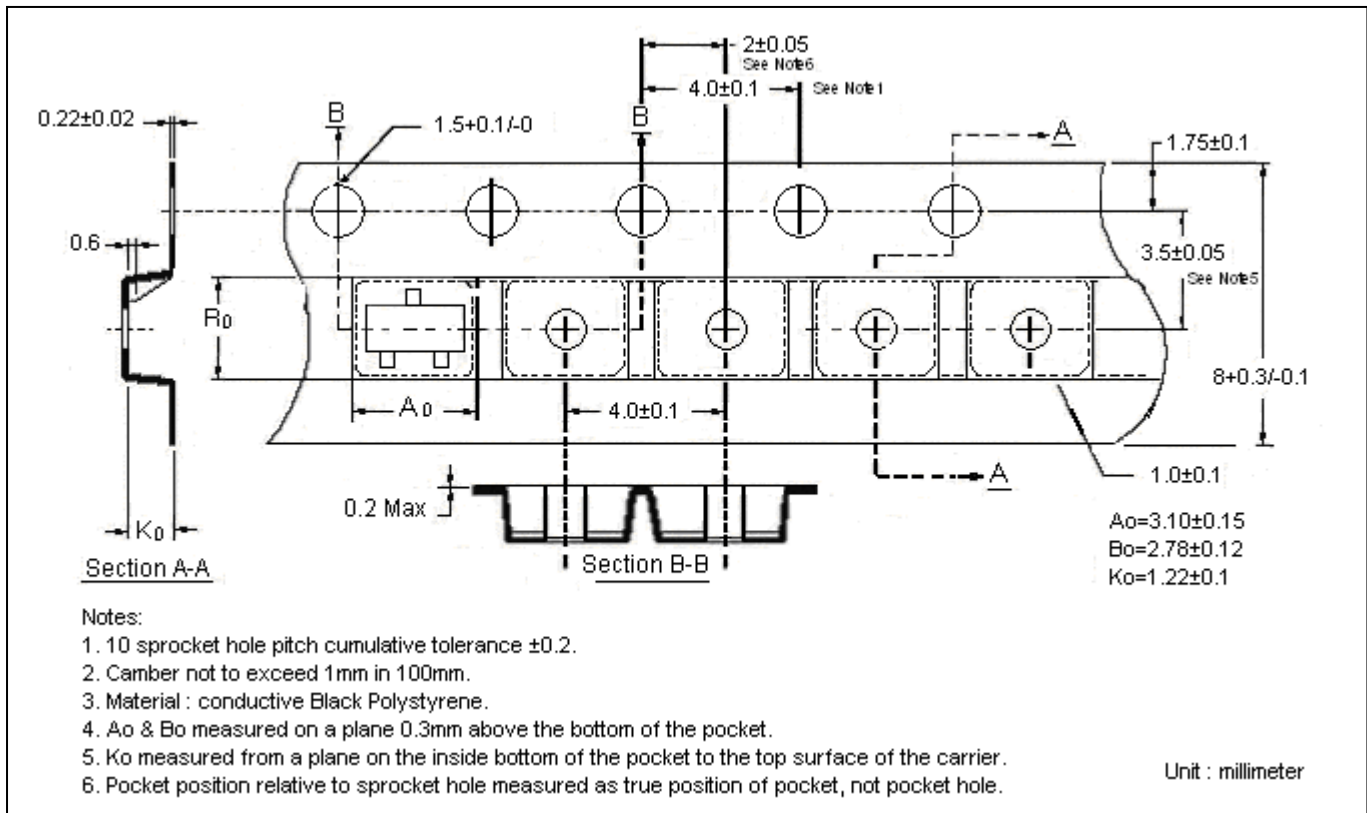
Transient Thermal Response Curves



**Reel Dimension**



**Carrier Tape Dimension**



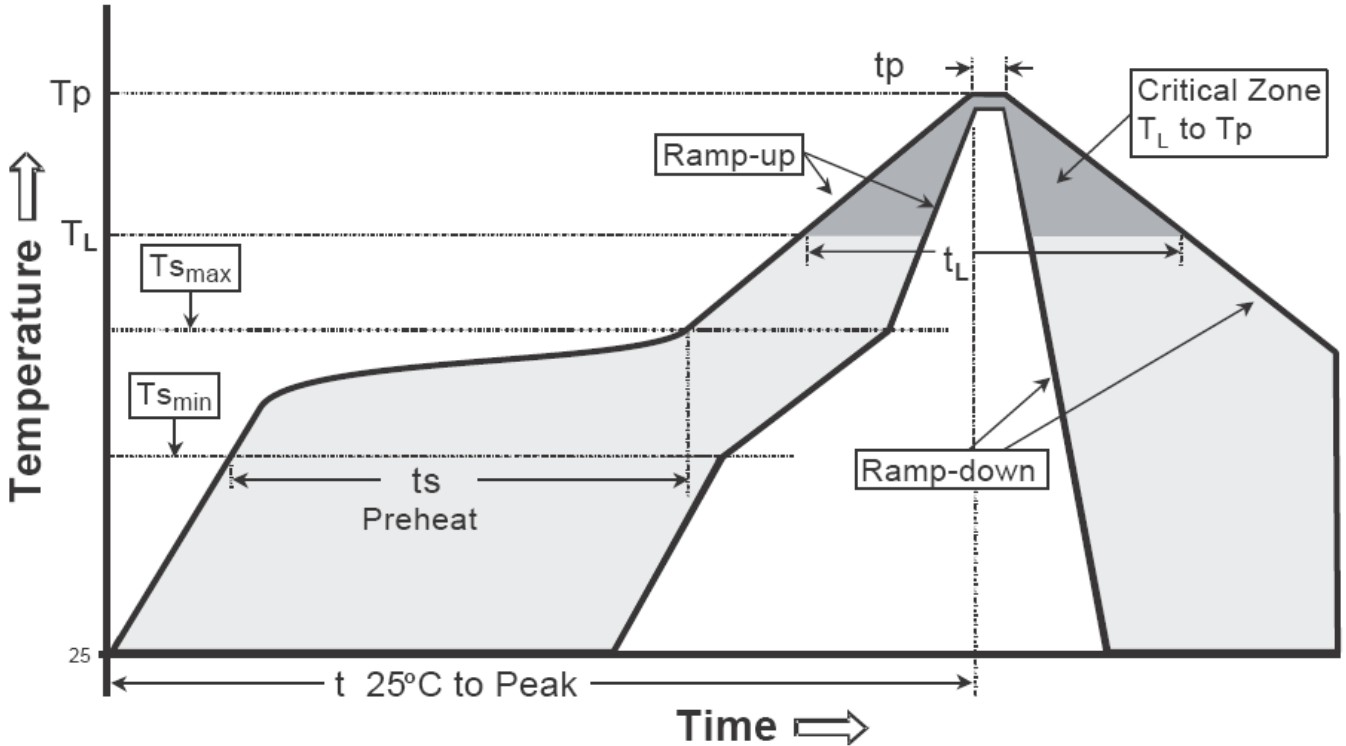
**Notes:**

1. 10 sprocket hole pitch cumulative tolerance  $\pm 0.2$ .
2. Camber not to exceed 1mm in 100mm.
3. Material : conductive Black Polystyrene.
4.  $A_0$  &  $B_0$  measured on a plane 0.3mm above the bottom of the pocket.
5.  $K_0$  measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

**Recommended temperature profile for IR reflow**

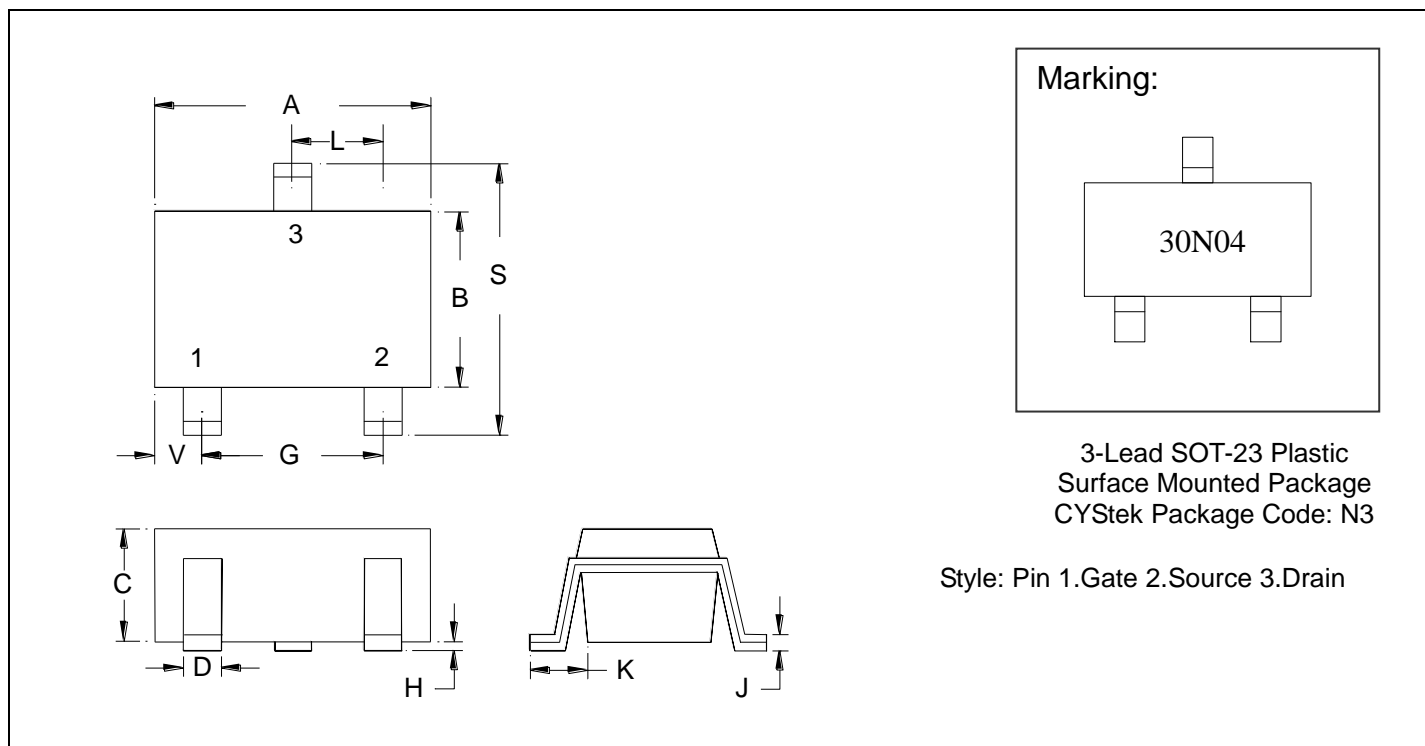


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T <sub>s min</sub> )	100°C	150°C
-Temperature Max(T <sub>s max</sub> )	150°C	200°C
-Time(t <sub>s min</sub> to t <sub>s max</sub> )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60-150 seconds	60-150 seconds
Peak Temperature(T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(t <sub>p</sub> )	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.



## SOT-23 Dimension



\*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1063	0.1220	2.70	3.10	J	0.0034	0.0079	0.085	0.20
B	0.0472	0.0630	1.20	1.60	K	0.0128	0.0266	0.32	0.67
C	0.0335	0.0512	0.89	1.30	L	0.0335	0.0453	0.85	1.15
D	0.0118	0.0197	0.30	0.50	S	0.0830	0.1161	2.10	2.95
G	0.0669	0.0910	1.70	2.30	V	0.0098	0.0256	0.25	0.65
H	0.0000	0.0040	0.00	0.10					

**Notes:** 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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