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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Not recommended
for new design

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HSK120

Silicon Epitaxial Planar Diode for High Speed Switching

REJ03G0192-0400Z
(Previous: ADE-208-171C)
Rev.4.00
Mar.22.2004

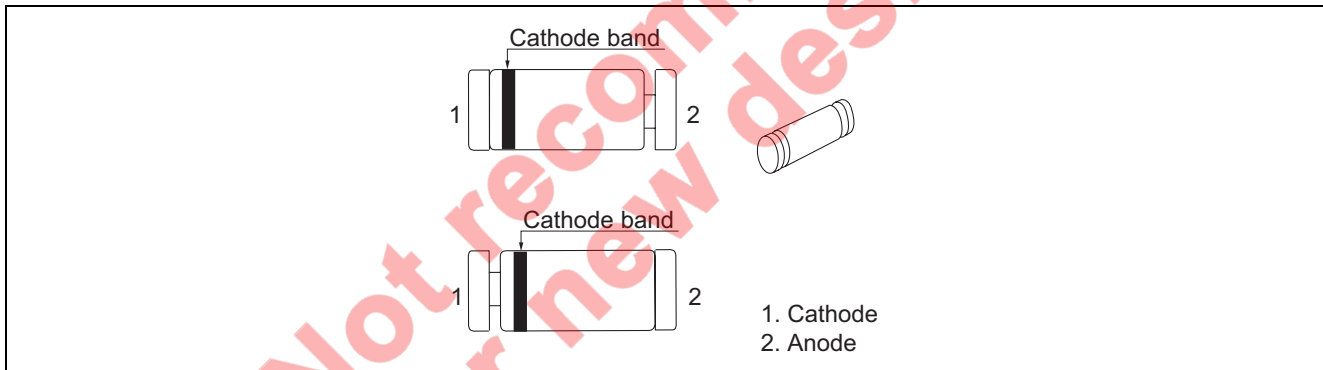
Features

- Low reverse recovery time. ($t_{rr} = 3.0$ ns max)
- LLD package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HSK120	White	LLD

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}	70	V
Reverse voltage	V_R	60	V
Peak forward current	I_{FM}	450	mA
Non-Repetitive peak forward surge current	I_{FSM}^{*1}	4	A
Average rectified current	I_O	150	mA
Junction temperature	T_j	175	°C
Storage temperature	T_{stg}	-65 to +175	°C

Note: 1. Within 1μs forward surge current.

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_F	—	—	0.8	V	$I_F = 10\text{ mA}$
Reverse voltage	V_R	70	—	—	V	$I_R = 5\text{ }\mu\text{A}$
Reverse current	I_R	—	—	0.1	μA	$V_R = 60\text{ V}$
Capacitance	C	—	—	3.0	pF	$V_R = 0\text{ V}, f = 1\text{ MHz}$
Reverse recovery time	t_{rr}	—	—	3.0	ns	$I_F = 10\text{ mA}, V_R = 6\text{ V}, R_L = 50\text{ }\Omega, I_{rr} = 0.1I_R$

Not recommended for new design

Main Characteristics

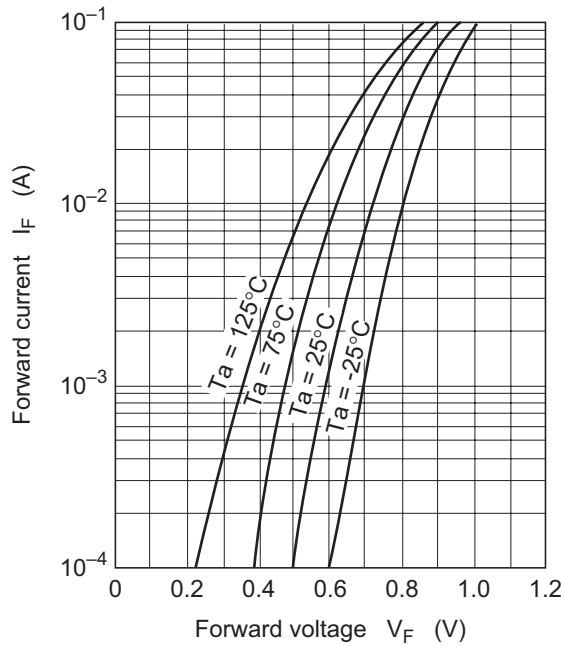


Fig.1 Forward current vs. Forward voltage

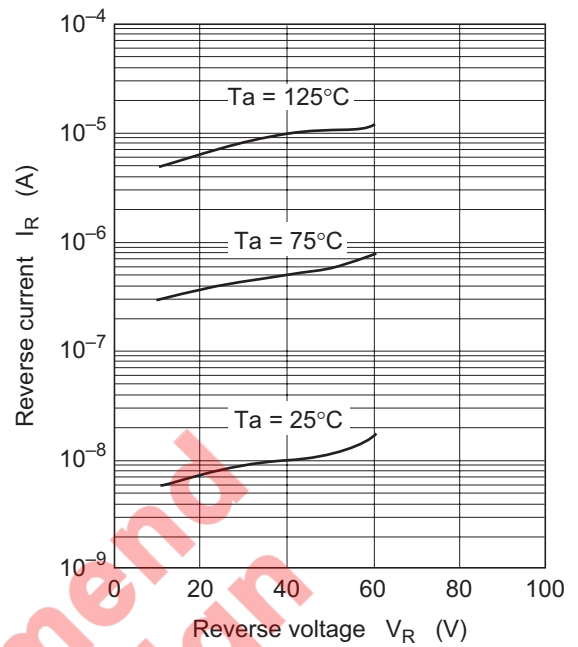


Fig.2 Reverse current vs. Reverse voltage

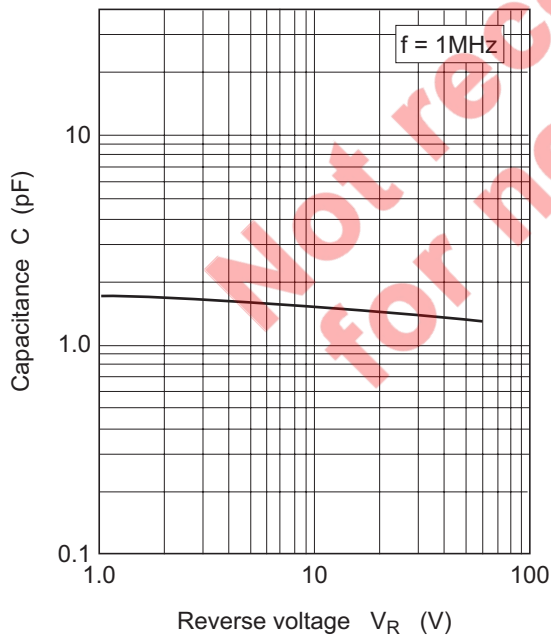
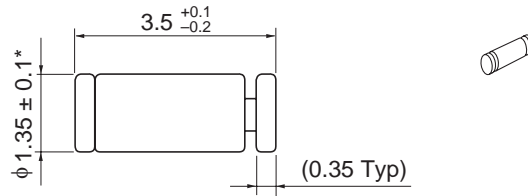


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions

As of January, 2003
Unit: mm



* HSK122: $\phi 1.4 \pm 0.1$ type

Package Code	LLD
JEDEC	—
JEITA	—
Mass (reference value)	0.027 g

Not recommended
for new design

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