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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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1SS81

Silicon Epitaxial Planar Diode for High Voltage Switching

REJ03G0561-0300

(Previous: ADE-208-148B)

Rev.3.00 Mar 22, 2005

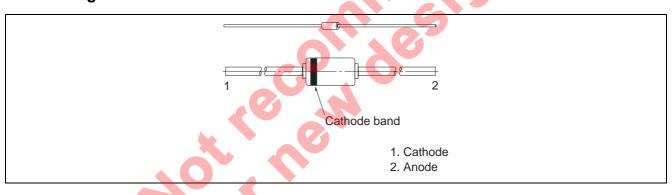
Features

- High reverse voltage. $(V_R = 150 \text{ V})$
- High reliability with glass seal.

Ordering Information

Type No.	Cathode band	Package Name	Package Code (Previous Code)
1SS81	Verdure	DO-35	GRZZ0002ZB-A (DO-35)

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Peak reverse voltage	V _{RM} * ¹	200	V
Reverse voltage	V _R	150	V
Average rectified current	I ₀	200	mA
Peak forward current	I _{FM}	625	mA
Non-Repetitive peak forward surge current	I _{FSM} * ²	1	Α
Power dissipation	Pd	400	mW
Junction temperature	Tj	175	°C
Storage temperature	Tstg	-65 to +175	°C

Notes: 1. Reverse voltage in excess of peak reverse voltage may deteriorate electrical characteristic.

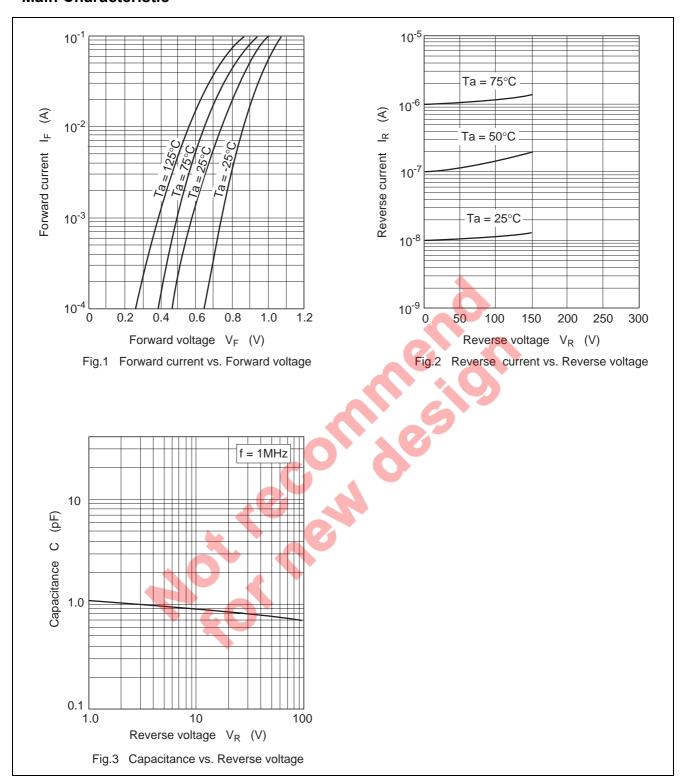
2. Within 1s forward surge current.

Electrical Characteristics

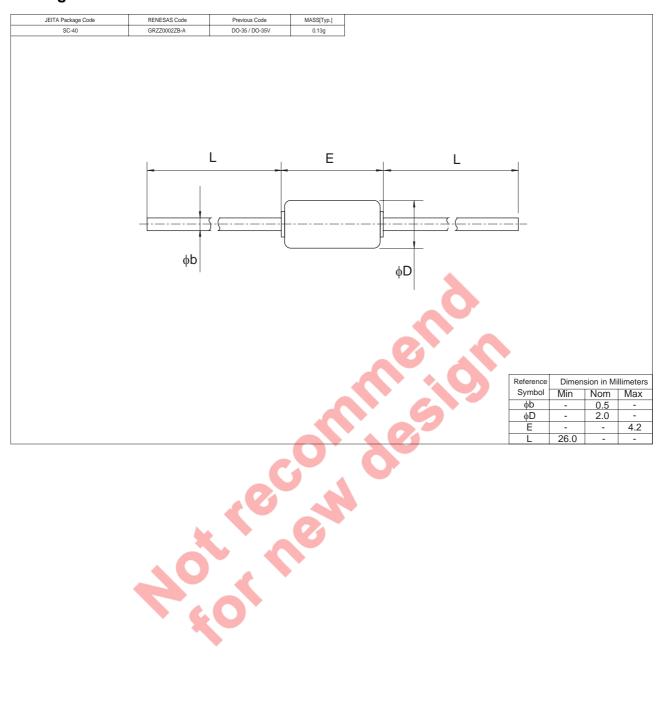
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}			200	nA	V _R = 150 V
	I _{R2}			100	μΑ	V _R = 200 V
Forward voltage	V _F	_	_	1.0	V	I _F = 100 mA
Capacitance	С		1.5	_	pF	$V_R = 0 V$, $f = 1 MHz$
Reverse recovery time	t _{rr}	_	_	100	ns	$I_F = I_R = 30 \text{ mA}, Irr = 3 \text{ mA}, R_L = 100 \Omega$
Active Collides						

Main Characteristic



Package Dimensions



Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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- (ii) use of nontrammaple material of (iii) prevention against any maintention or misnap.

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