

PNP Epitaxial Planar Transistor

BTA1210J3

BV_{CEO}	-120V
I_C	-10A
R_{CESAT}	270m Ω

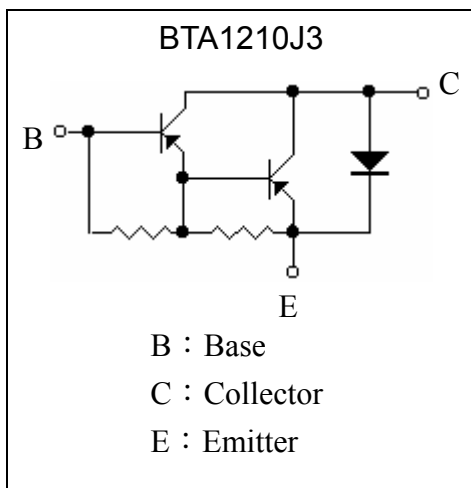
Description

The BTA1210J3 is a PNP Darlington transistor, designed for use in general purpose amplifier and low speed switching application.

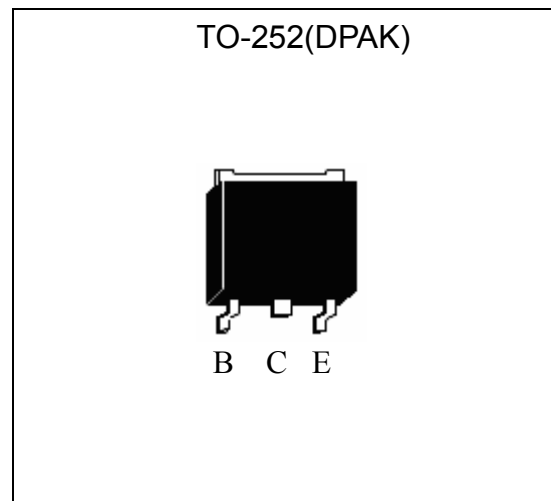
Features

- High BV_{CEO}
- High DC current gain
- High current capability
- Monolithic construction with built-in base-emitter shunt resistors
- RoHS compliant package

Equivalent Circuit



Outline





Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-Base Voltage	V _{CB0}	-120	V
Collector-Emitter Voltage	V _{CE0}	-120	V
Emitter-Base Voltage	V _{EBO}	-5	V
Collector Current (DC)	I _C	-10	A
Collector Current (Pulse)	I _{CP}	-15 (Note)	A
Power Dissipation	Pd(T _A =25°C)	1.75	W
	Pd(T _C =25°C)	20	W
Thermal Resistance, Junction to Ambient	R _{θJA}	83.3	°C/W
Thermal Resistance, Junction to Case	R _{θJC}	6.25	°C/W
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55~+150	°C

Note : Single Pulse Pw ≤ 350μs, Duty ≤ 2%.

Characteristics (Ta=25°C)

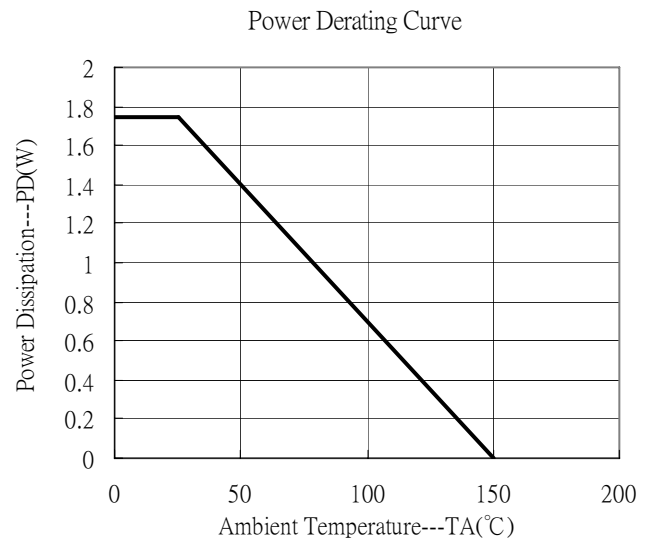
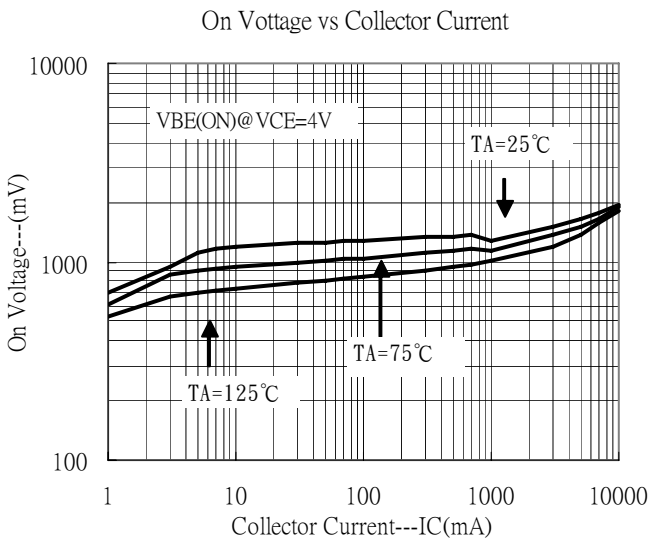
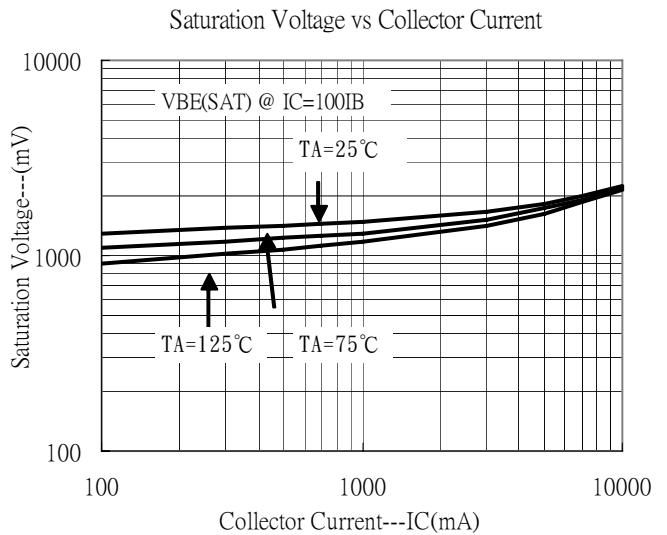
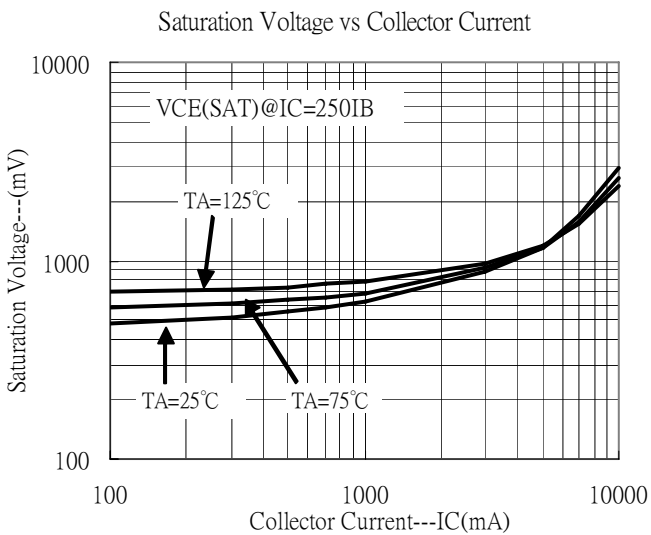
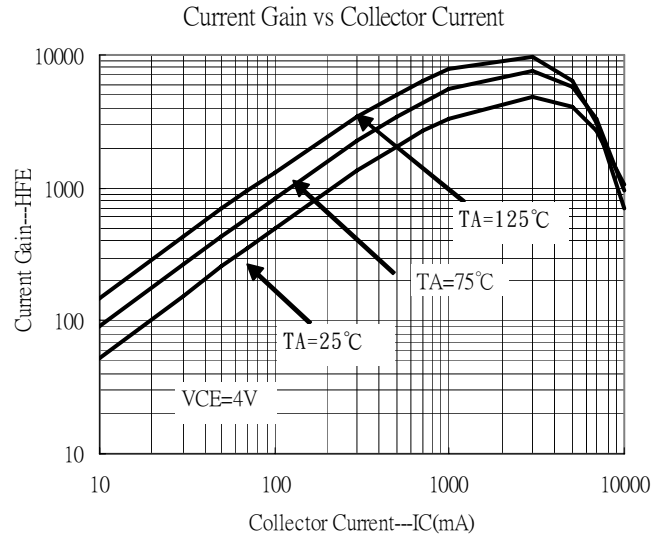
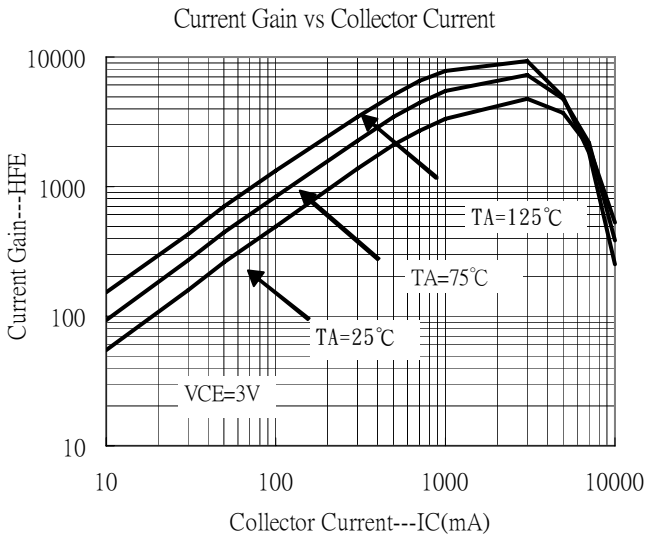
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV _{CE0}	-120	-	-	V	I _C =-1mA, I _B =0
BV _{CB0}	-120	-	-	V	I _C =-100μA, I _E =0
I _{CB0}	-	-	-200	μA	V _{CB} =-120V, I _E =0
I _{CE0}	-	-	-200	μA	V _{CE} =-120V, I _B =0
I _{EBO}	-	-	-2	mA	V _{EB} =-5V, I _C =0
*V _{CE(sat) 1}	-	-	-1.8	V	I _C =-4A, I _B =-16mA
*V _{CE(sat) 2}	-	-	-2.0	V	I _C =-8A, I _B =-80mA
*V _{BE(sat)}	-	-	-2.5	V	I _C =-8A, I _B =-80mA
*V _{BE(on)}	-	-	-2.2	V	V _{CE} =-4V, I _C =-4A
*h _{FE 1}	2	-	-	K	V _{CE} =-4V, I _C =-1A
*h _{FE 2}	4	-	20	K	V _{CE} =-4V, I _C =-4A
*h _{FE 3}	200	-	-	-	V _{CE} =-4V, I _C =-8A
Cob	-	-	300	pF	V _{CB} =-10V, I _E =0A, f=1MHz

*Pulse Test : Pulse Width ≤ 380μs, Duty Cycles ≤ 2%

Ordering Information

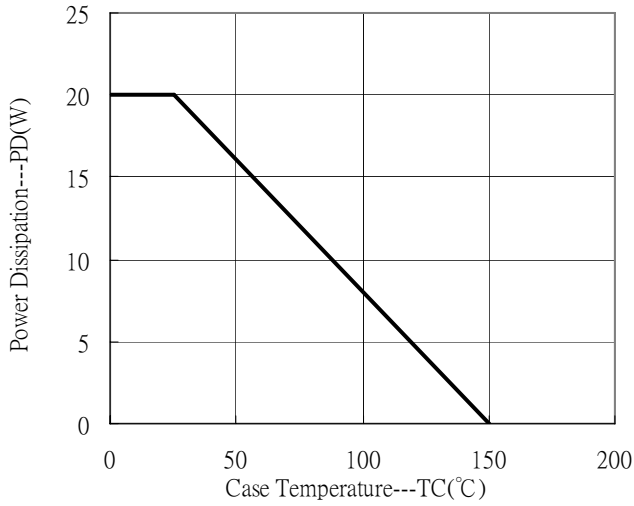
Device	Package	Shipping
BTA1210J3-0-T3-G	TO-252 (Pb-free lead plating and halogen-free package)	2500 pcs / Tape & Reel

Typical Characteristics

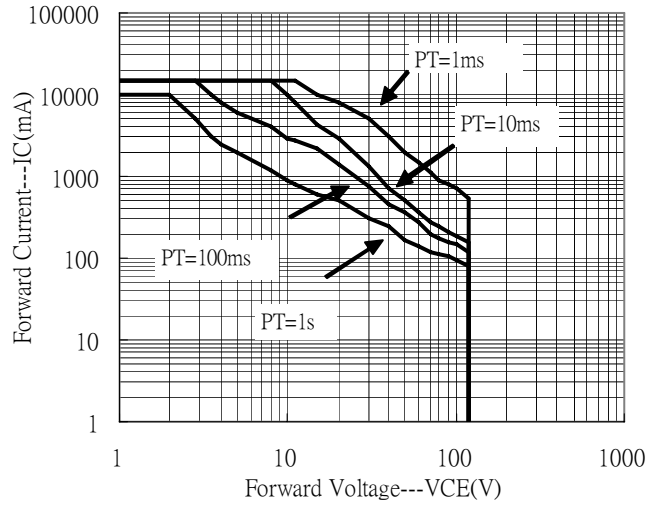


Typical Characteristics(Cont.)

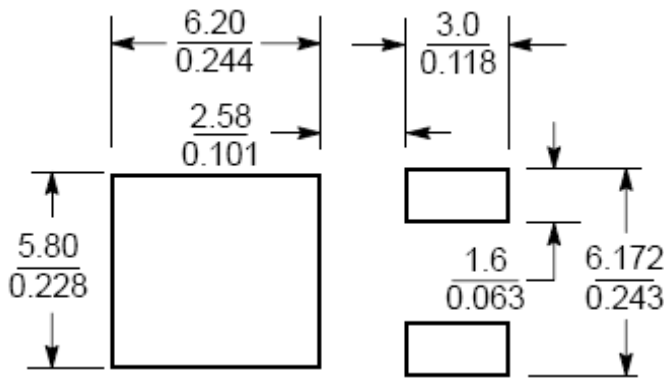
Power Derating Curve



Safe Operating Area

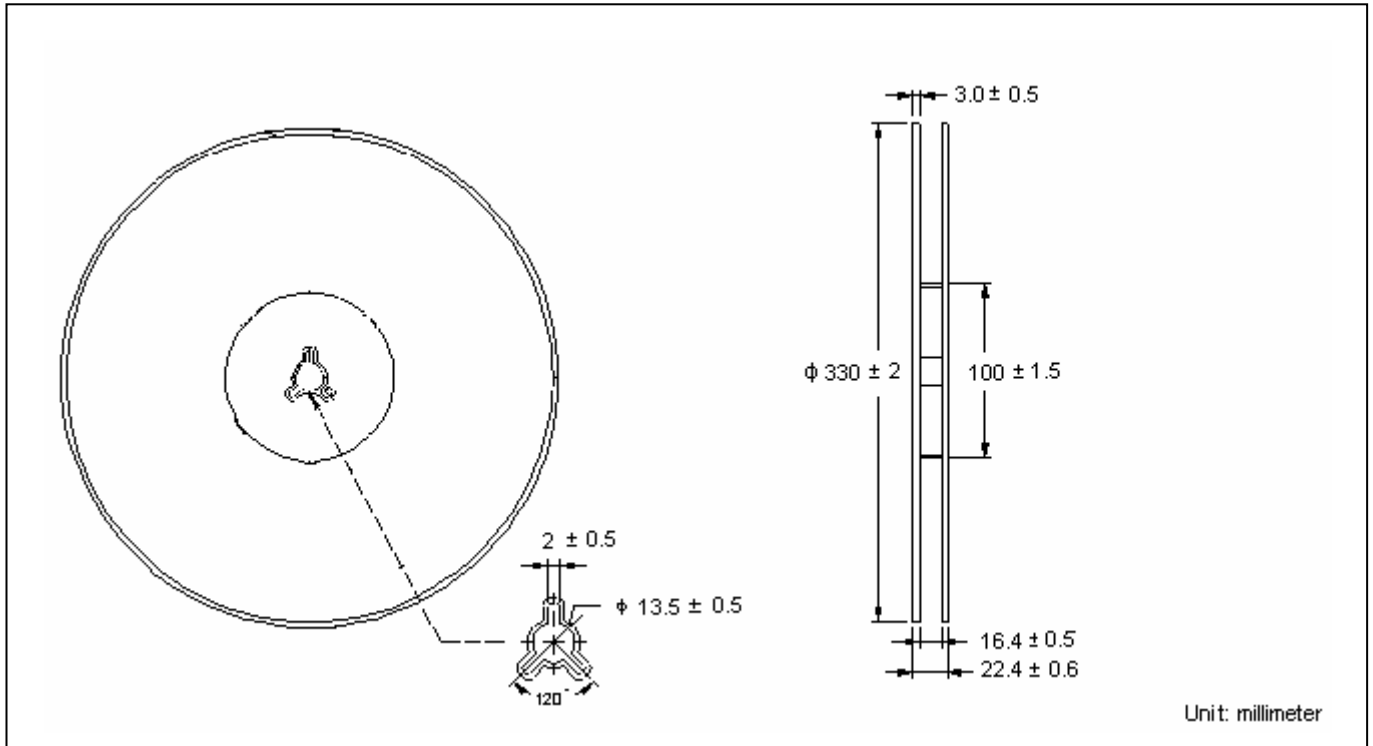


Recommended soldering footprint

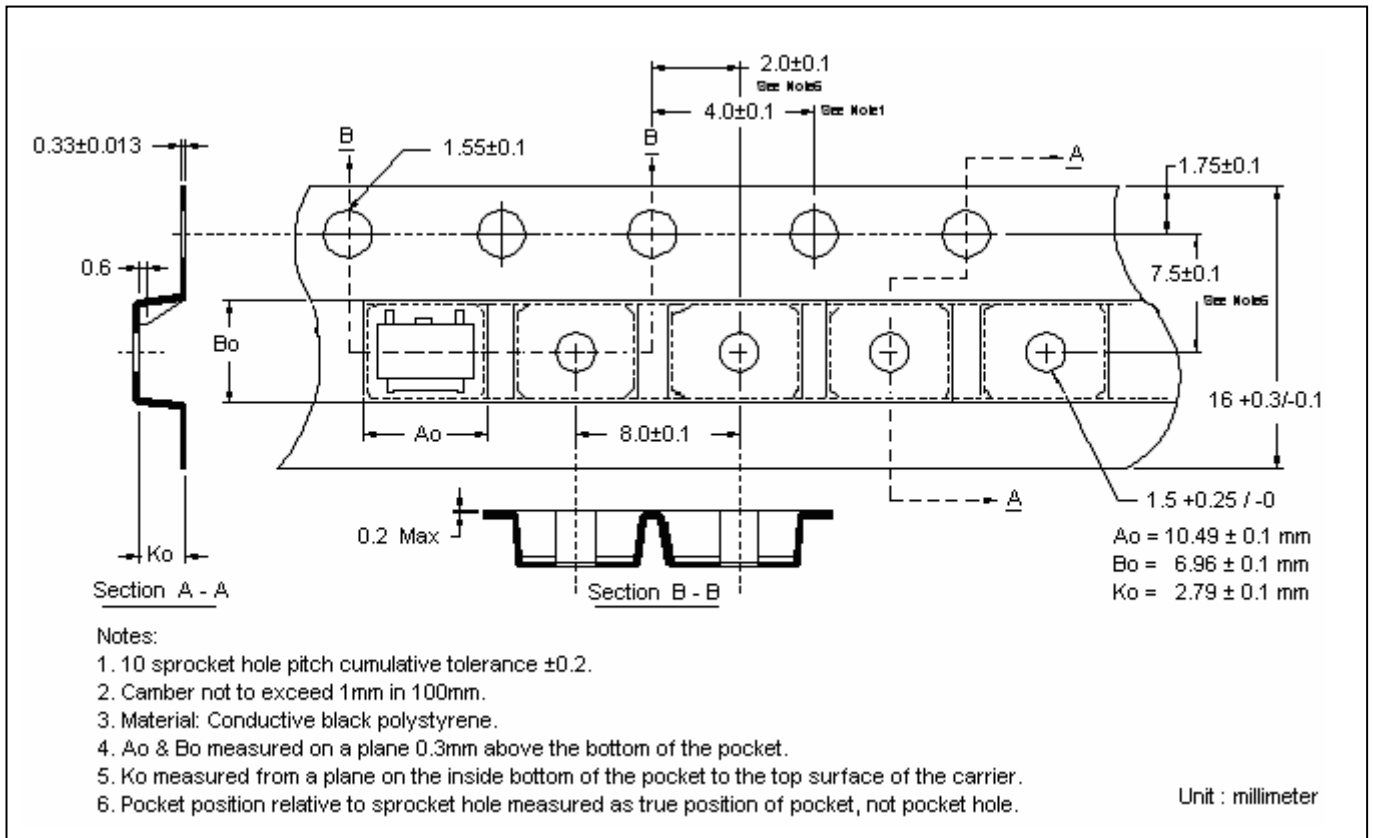


Unit ($\frac{\text{mm}}{\text{inch}}$)

Reel Dimension



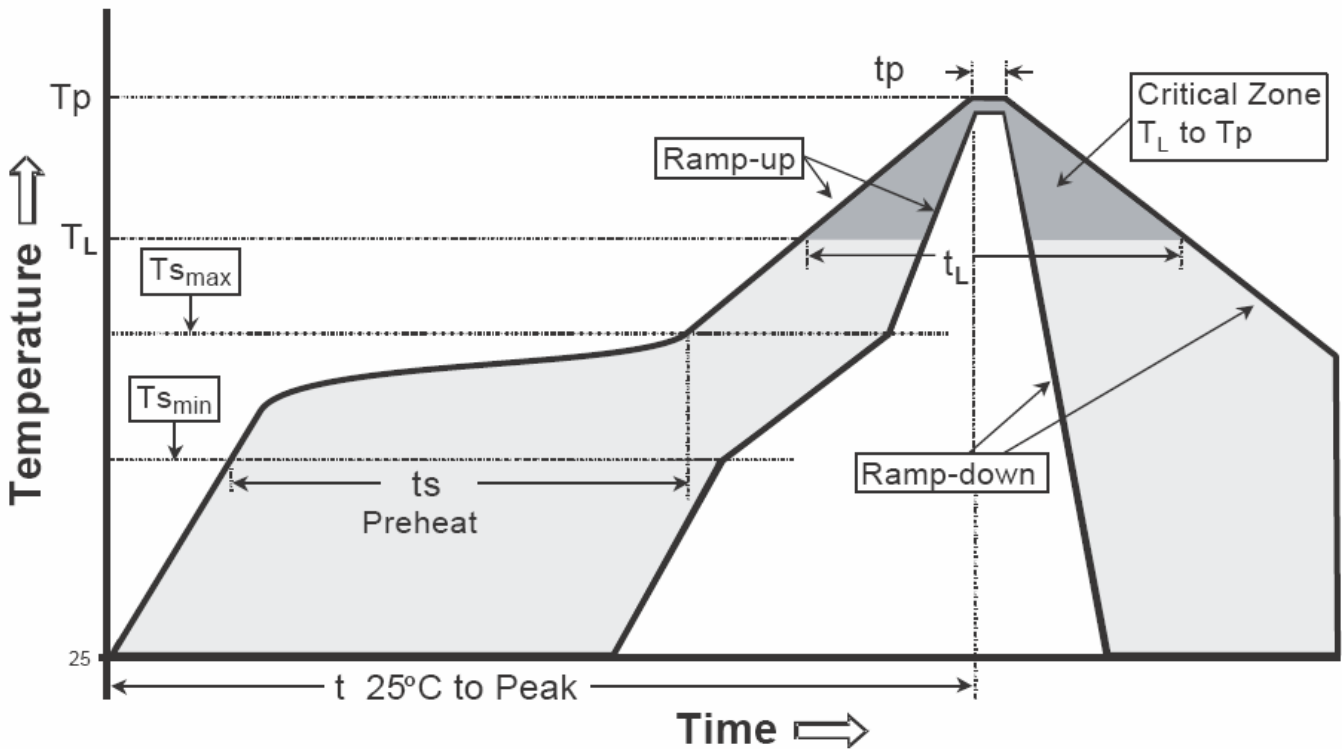
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

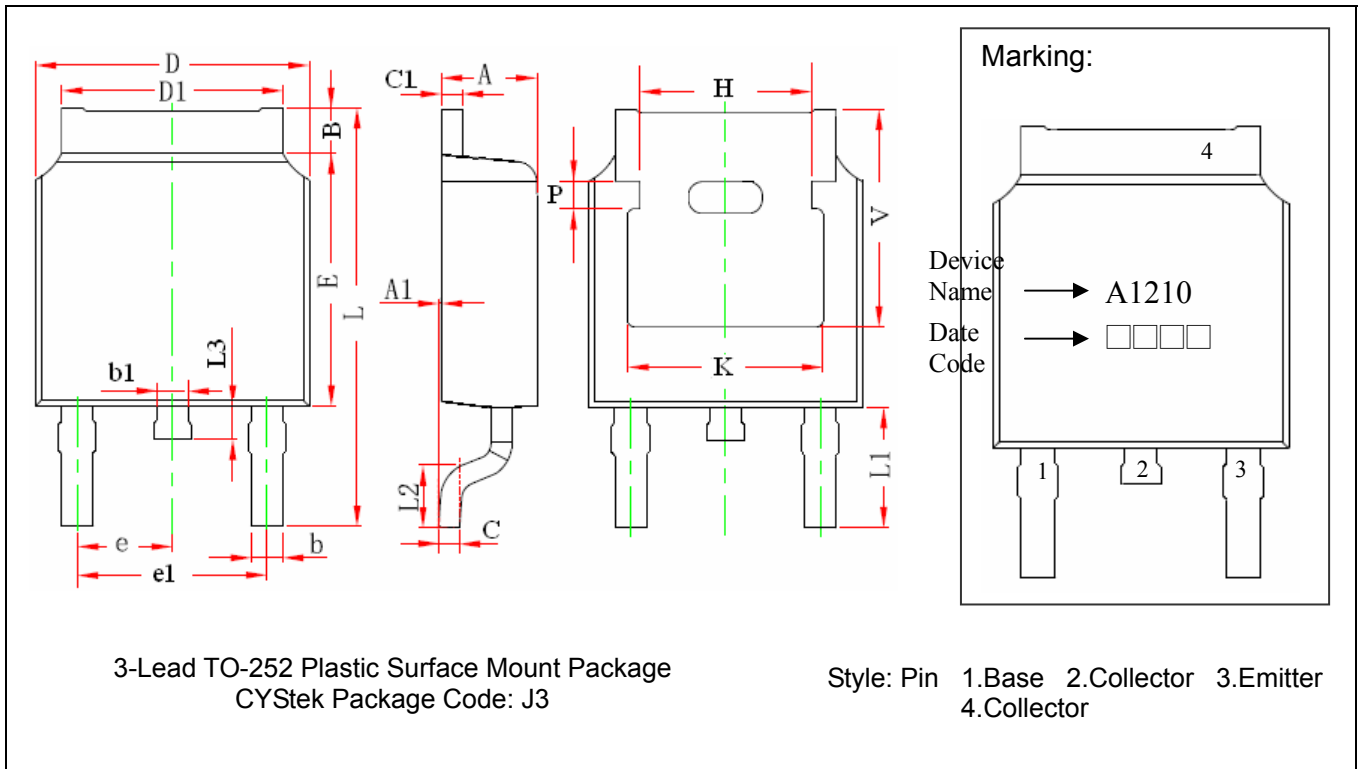
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-252 Dimension



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.087	0.094	2.200	2.400	e	0.086	0.094	2.186	2.386
A1	0.000	0.005	0.000	0.127	e1	0.172	0.188	4.372	4.772
B	0.039	0.048	0.990	1.210	H	0.163	REF	4.140	REF
b	0.026	0.034	0.660	0.860	K	0.190	REF	4.830	REF
b1	0.026	0.034	0.660	0.860	L	0.386	0.409	9.800	10.400
C	0.018	0.023	0.460	0.580	L1	0.114	REF	2.900	REF
C1	0.018	0.023	0.460	0.580	L2	0.055	0.067	1.400	1.700
D	0.256	0.264	6.500	6.700	L3	0.024	0.039	0.600	1.000
D1	0.201	0.215	5.100	5.460	P	0.026	REF	0.650	REF
E	0.236	0.244	6.000	6.200	V	0.211	REF	5.350	REF

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead : Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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