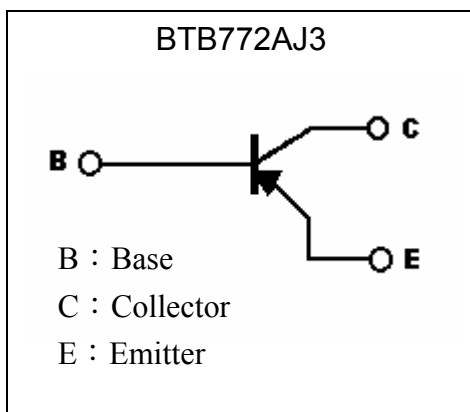
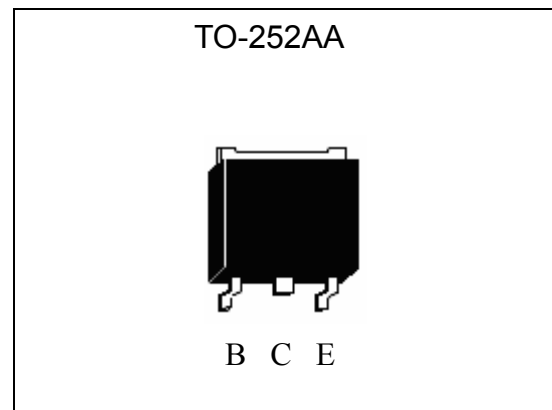


Low Vcesat PNP Epitaxial Planar Transistor

BTB772AJ3

Features

- Low $V_{CE(sat)}$, $V_{CE(sat)} = -0.3 \text{ V (max)}$, at $I_C / I_B = -2\text{A} / -0.1\text{A}$
- Excellent current gain characteristics
- Pb-free lead plating and halogen-free package

Symbol

Outline

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Limits	Unit
Collector-Base Voltage	V_{CB0}	-50	V
Collector-Emitter Voltage	V_{CE0}	-30	V
Emitter-Base Voltage	V_{EB0}	-7	V
Collector Current (DC)	I_C	-3	A
Collector Current (Pulse)	I_{CP}	-5 (Note 1)	A
Power Dissipation	$P_d(T_A = 25^\circ\text{C})$	1	W
	$P_d(T_C = 25^\circ\text{C})$	15	W
Operating Junction and Storage Temperature Range	$T_j ; T_{stg}$	-55~+150	$^\circ\text{C}$

**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	8.33	°C/W
Thermal Resistance, Junction-to-ambient, max	$R_{th,j-a}$	125	°C/W

Note : 1. Single Pulse , Pw=10ms

Characteristics (Ta=25°C)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV_{CBO}	-50	-	-	V	$I_C=-50\mu A, I_E=0$
BV_{CEO}	-30	-	-	V	$I_C=-1mA, I_B=0$
BV_{EBO}	-7	-	-	V	$I_E=-50\mu A, I_C=0$
I_{CBO}	-	-	-100	nA	$V_{CB}=-50V, I_E=0$
I_{EBO}	-	-	-100	nA	$V_{EB}=-7V, I_C=0$
* $V_{CE(sat)}$	-	-0.05	-0.2	V	$I_C=-400mA, I_B=-20mA$
* $V_{CE(sat)}$	-	-0.2	-0.3	V	$I_C=-2A, I_B=-100mA$
* $R_{CE(sat)}$	-	0.1	0.15	Ω	$I_C=-2A, I_B=-100mA$
* $V_{BE(sat)}$	-	-1	-1.2	V	$I_C=-2A, I_B=-200mA$
* h_{FE1}	160	-	-	-	$V_{CE}=-2V, I_C=-100mA$
* h_{FE2}	180	-	390	-	$V_{CE}=-2V, I_C=-500mA$
* h_{FE3}	150	-	-	-	$V_{CE}=-2V, I_C=-1A$
f_T	-	190	-	MHz	$V_{CE}=-10V, I_C=-0.5A, f=100MHz$
Cob	-	33	-	pF	$V_{CB}=-10V, f=1MHz$

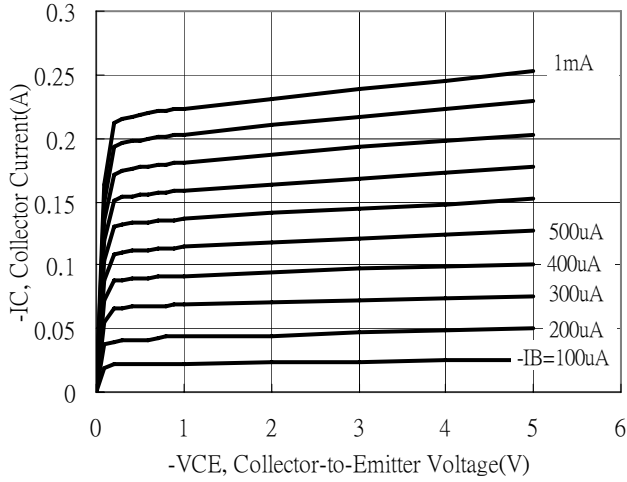
*Pulse Test : Pulse Width $\leq 380\mu s$, Duty Cycle $\leq 2\%$ **Ordering Information**

Device	Package	Shipping
BTB772AJ3-0-T3-G	TO-252AA (Pb-free lead plating and halogen-free package)	2500 pcs / Tape & Reel

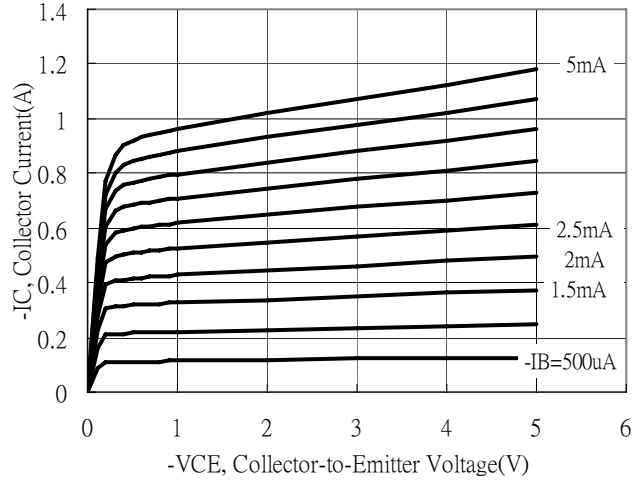


Typical Characteristics

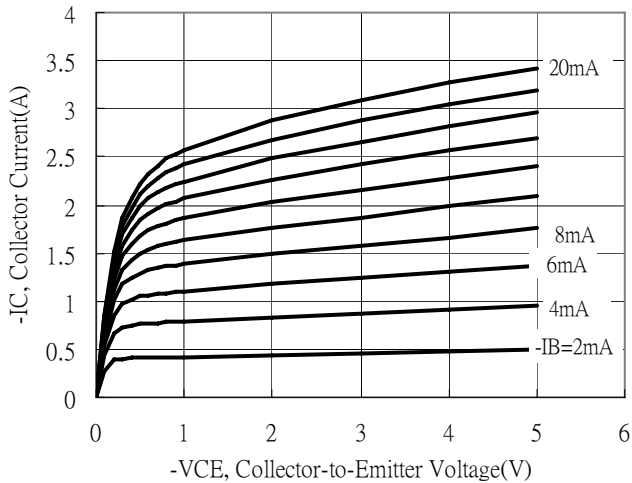
Emitter Grounded Output Characteristics



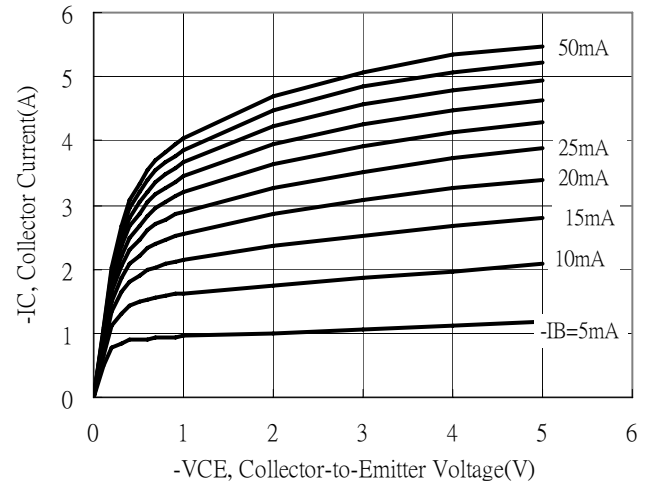
Emitter Grounded Output Characteristics



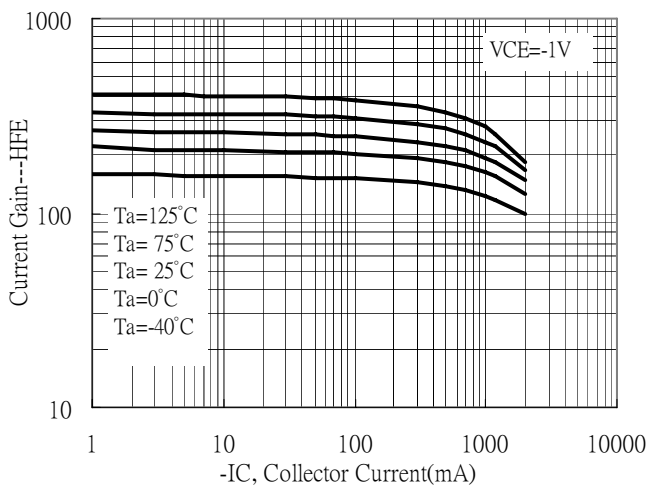
Emitter Grounded Output Characteristics



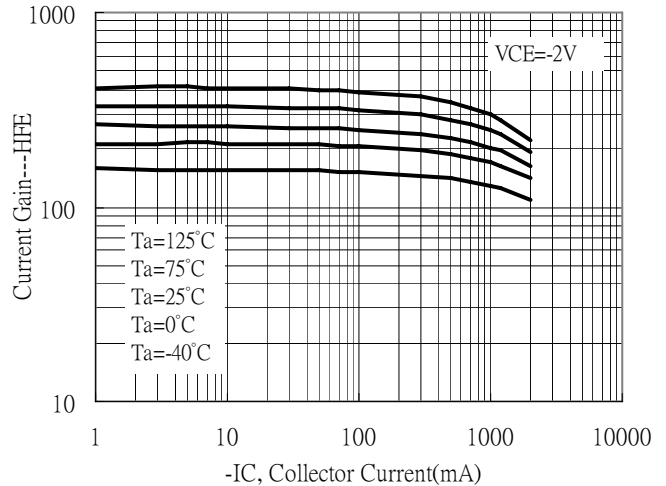
Emitter Grounded Output Characteristics



Current Gain vs Collector Current



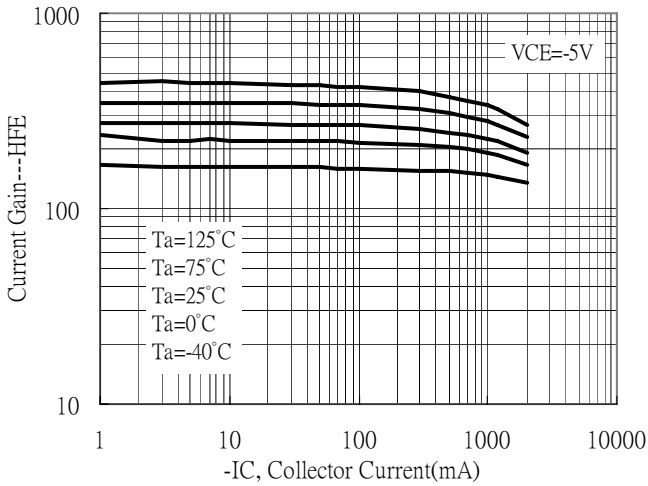
Current Gain vs Collector Current



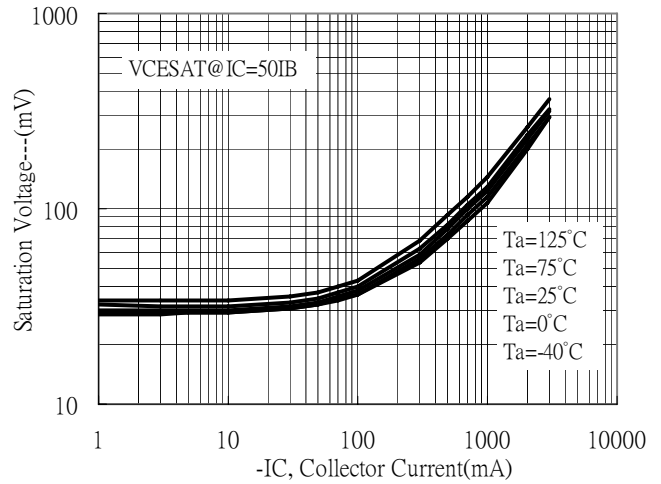


Typical Characteristics(Cont.)

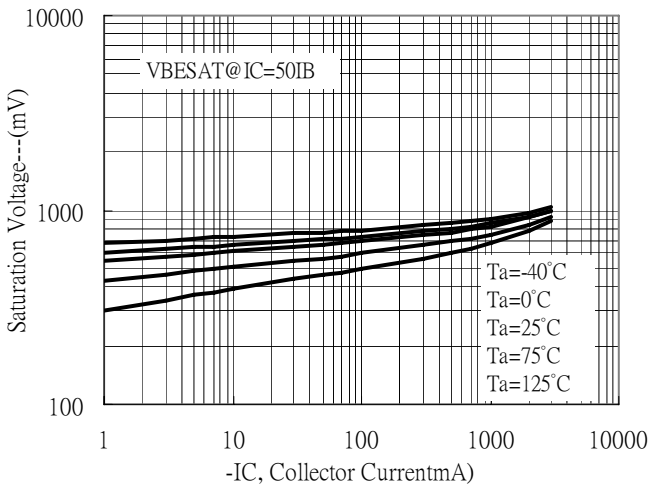
Current Gain vs Collector Current



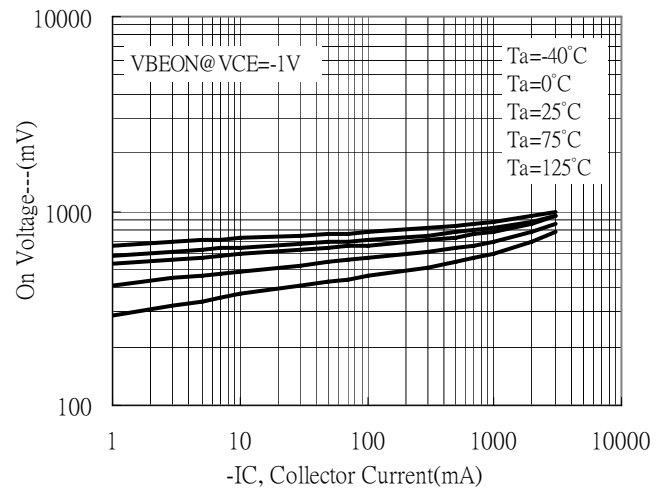
Saturation Voltage vs Collector Current



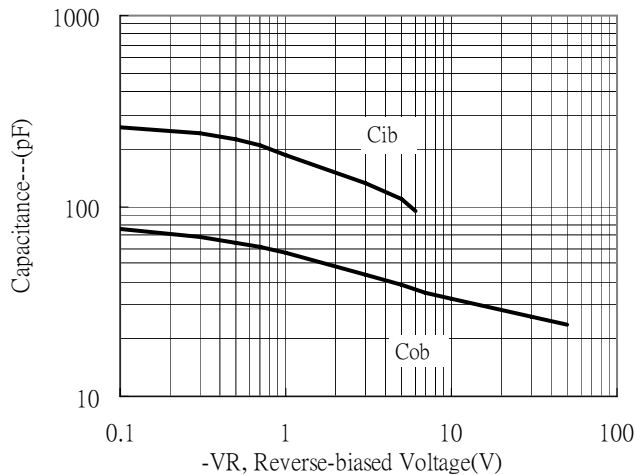
Saturation Voltage vs Collector Current



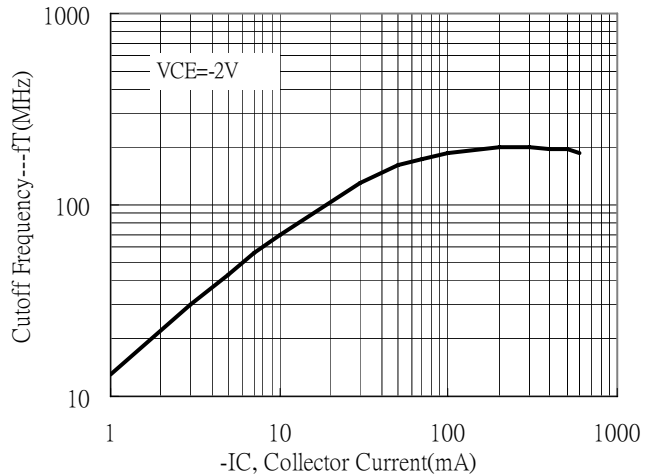
On Voltage vs Collector Current



Capacitance vs Reverse-biased Voltage



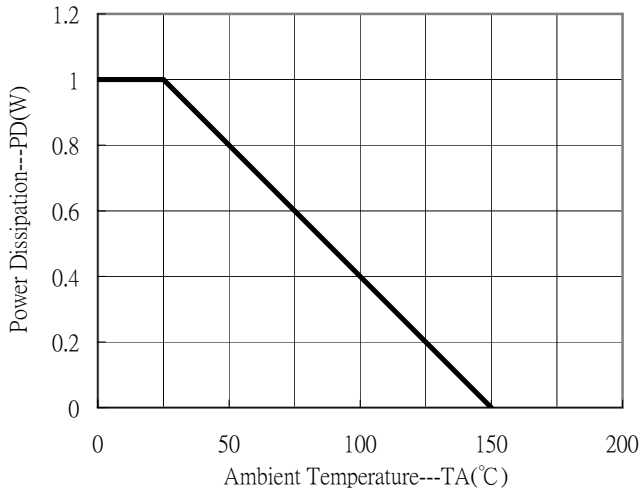
Cutoff Frequency vs Collector Current



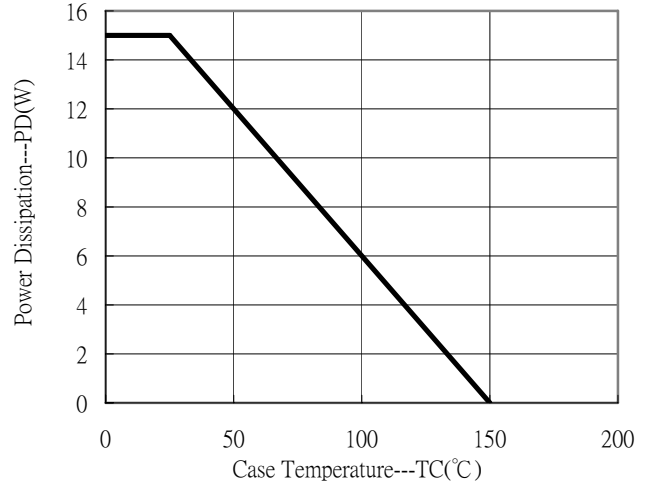


Typical Characteristics(Cont.)

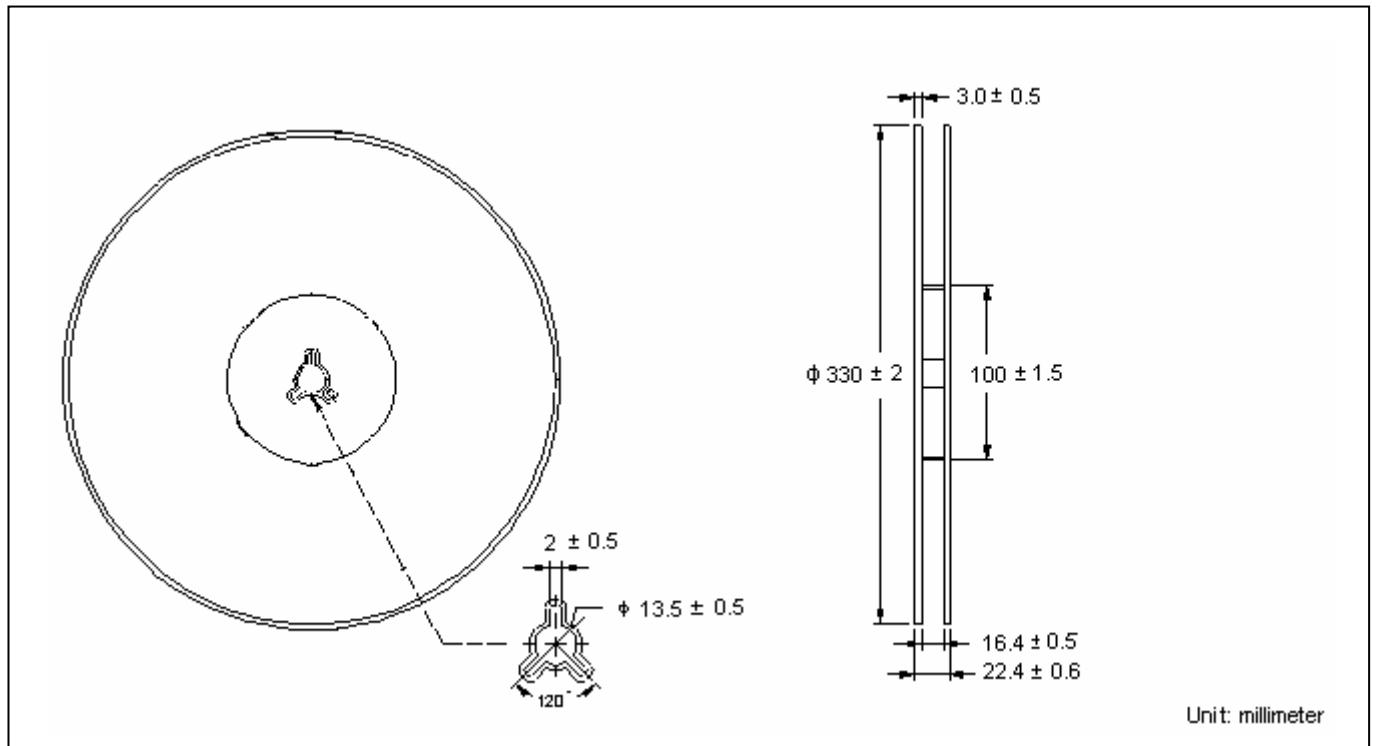
Power Derating Curve



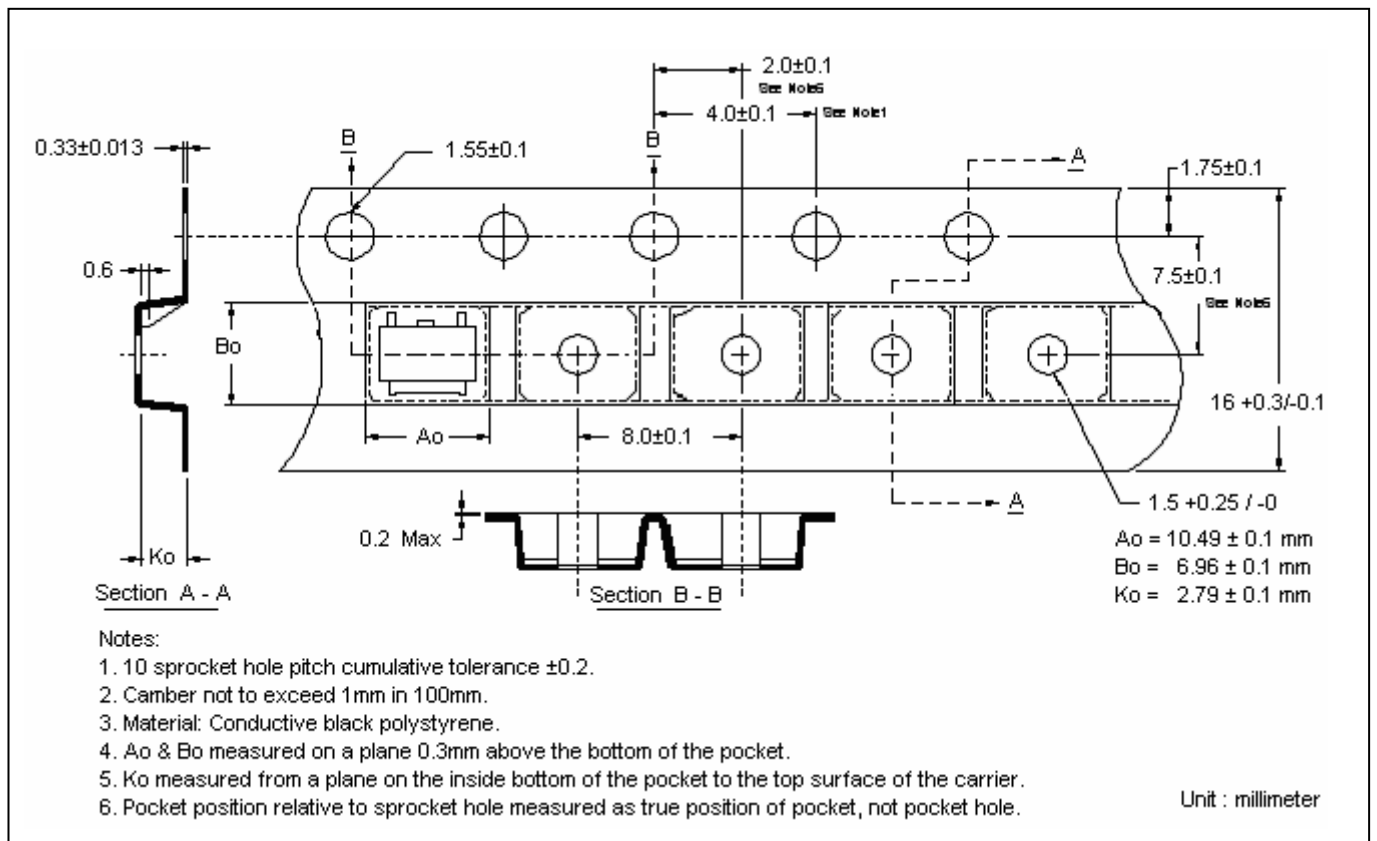
Power Derating Curve



Reel Dimension

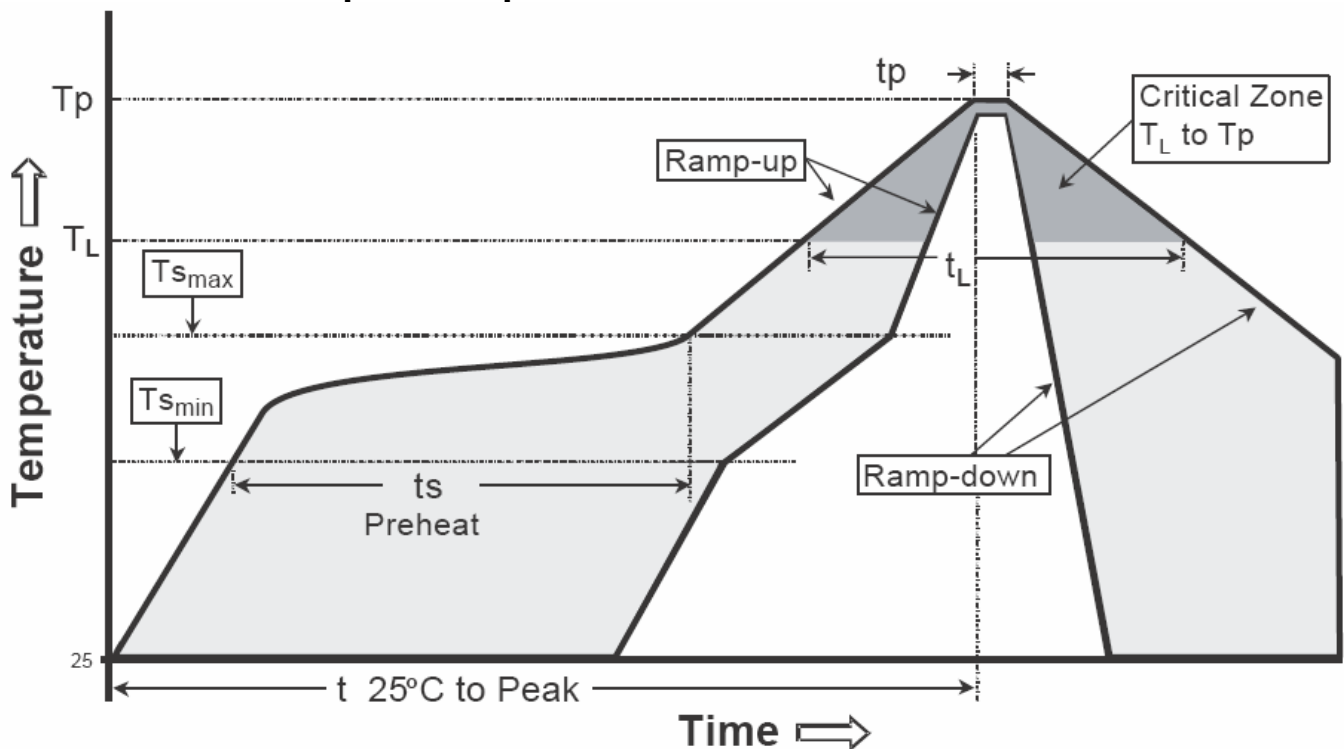


Carrier Tape Dimension



Recommended wave soldering condition

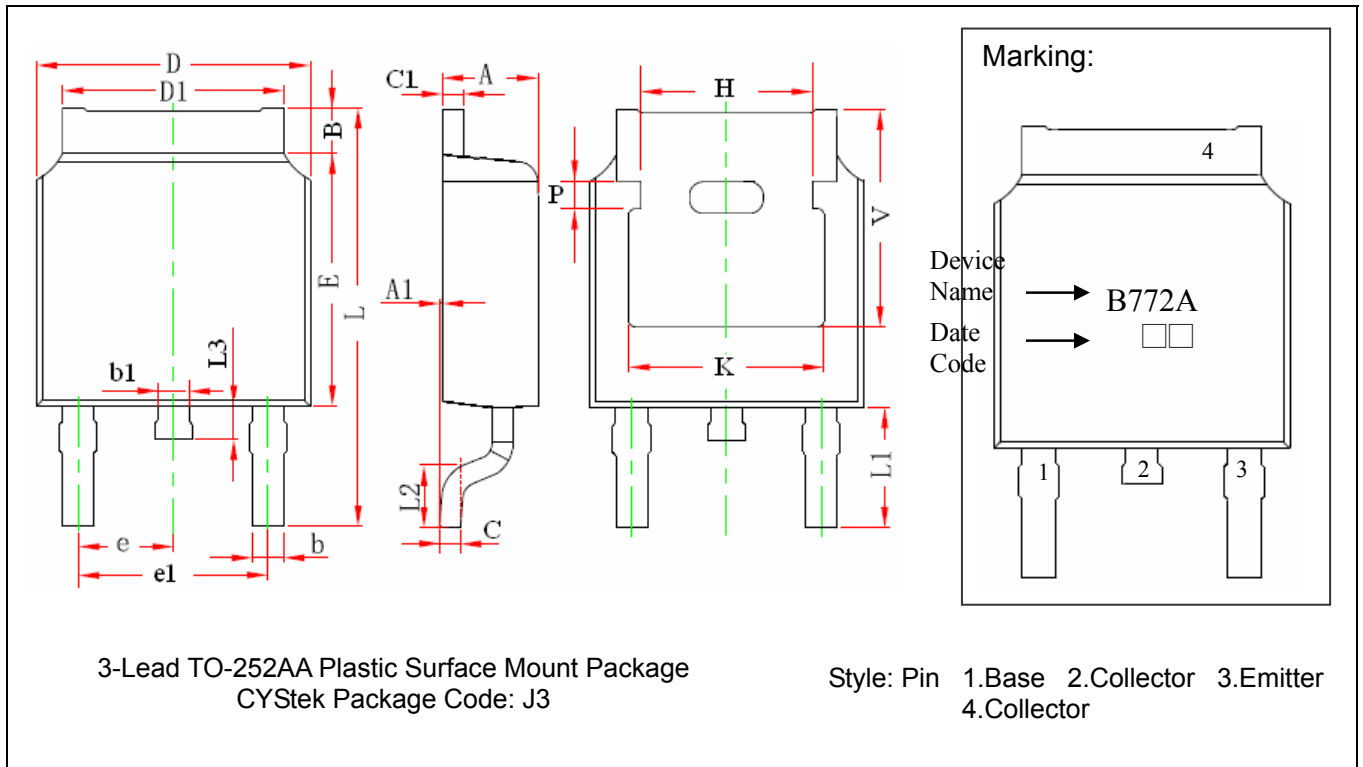
Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (Tl)	183°C	217°C
- Time (tl)	60-150 seconds	60-150 seconds
Peak Temperature(Tp)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-252AA Dimension



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.087	0.094	2.200	2.400	e	0.086	0.094	2.186	2.386
A1	0.000	0.005	0.000	0.127	e1	0.172	0.188	4.372	4.772
B	0.039	0.048	0.990	1.210	H	0.163	REF	4.140	REF
b	0.026	0.034	0.660	0.860	K	0.190	REF	4.830	REF
b1	0.026	0.034	0.660	0.860	L	0.386	0.409	9.800	10.400
C	0.018	0.023	0.460	0.580	L1	0.114	REF	2.900	REF
C1	0.018	0.023	0.460	0.580	L2	0.055	0.067	1.400	1.700
D	0.256	0.264	6.500	6.700	L3	0.024	0.039	0.600	1.000
D1	0.201	0.215	5.100	5.460	P	0.030	REF	0.750	REF
E	0.236	0.244	6.000	6.200	V	0.211	REF	5.350	REF

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead : Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.