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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RKP203KN

Silicon Epitaxial Trench Pin Diode for Antenna Switching

REJ03G1304-0100 Rev.100 Dec 16, 2005

Features

- Adopting the trench structure improves low capacitance. (C = 0.31 pF max)
- Low forward resistance. (rf = $1.5 \Omega \text{ max}$)

2

- Low operation current.
- Ultra small leadless Package (0805type; the use of an undersurface electrode structure) for use in compact and products.

Ordering Information

–			
Type No.	Laser Mark	Package Name	Package Code (Previous Code)
RKP203KN	6	MP8	PXSN0002ZA-A
		() S'	
Pin Arrangement		× 20	
	1 1 0 2	ark 🗣	
		1. Cathode 2. Anode	



Absolute Maximum Ratings

		(Ta = 2)		
ltem	Symbol	Value	Unit	
Reverse voltage	V _R	30	V	
Forward current	I _F	100	mA	
Power dissipation	Pd	100	mW	
Junction temperature	Тј	125	°C	
Storage temperature	Tstg	-55 to +125	°C	

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

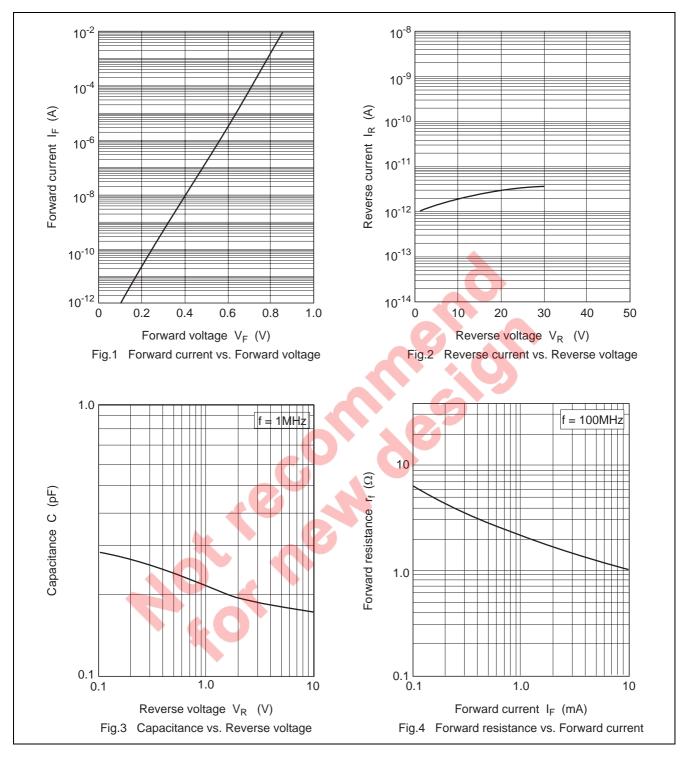
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _R	—	—	100	nA	$V_R = 30 V$
Forward voltage	V _F	—	—	1.0	V	I _F = 10 mA
Capacitance	С	—	—	0.31	pF	$V_R = 1 V$, f = 1 MHz
Forward resistance	r _f	_	—	2.5	Ω	I _F = 2 mA, f = 100 MHz
		_	—	1.5		I _F = 10 mA, f = 100 MHz
ESD-Capability *1	—	100	—	_	V	$C = 200 \text{ pF}$, $RL = 0 \Omega$, Both forward
						and reverse direction 1 pulse.

Notes: 1. Failure criterion ; $I_R > 100 \mbox{ nA}$ at V_R = 30 V

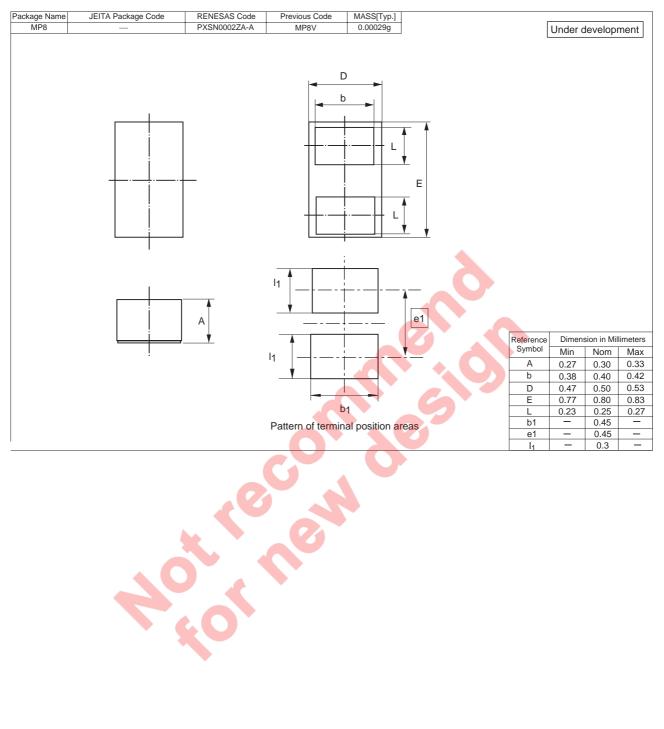
2. Please do not use the soldering iron due to avoid high stress to the MP8 package.



Main Characteristic



Package Dimensions





Renesas Technology Corp. sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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Renesas Technology America, Inc. 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K. Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd. Unit 205, AZIA Center, No.133 Yincheng Rd (n), Pudong District, Shanghai 200120, China Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7898

Renesas Technology Hong Kong Ltd. 7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2730-6071

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Renesas Technology Singapore Pte. Ltd. 1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510