To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RKP202KN

Silicon Epitaxial Trench Pin Diode for Antenna Switching

REJ03G1312-0100
Rev.1.00
Dec 16, 2005

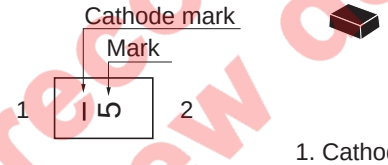
Features

- Adopting the trench structure improves low capacitance. (C = 0.43 pF max)
- Low forward resistance. (rf = 1.80 Ω max)
- Low operation current.
- Ultra small leadless Package (0805type; the use of an undersurface electrode structure) for use in compact and products.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
RKP202KN	5	MP8	PXSN0002ZA-A

Pin Arrangement



1. Cathode
2. Anode

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V_R	30	V
Forward current	I_F	100	mA
Power dissipation	Pd	100	mW
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	°C

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I_R	—	—	100	nA	V_R = 30 V
Forward voltage	V_F	—	—	0.90	V	I_F = 2 mA
Capacitance	C	—	—	0.43	pF	V_R = 1 V, f = 1 MHz
Forward resistance	r_f	—	—	1.80	Ω	I_F = 2 mA, f = 100 MHz
ESD-Capability *[1]	—	100	—	—	V	C = 200 pF, R = 0 Ω, Both forward and reverse direction 1 pulse.

Notes: 1. Failure criterion ; I_R > 100 nA at V_R = 30 V

2. Please do not use the soldering iron due to avoid high stress to the MP8 package.

Main Characteristic

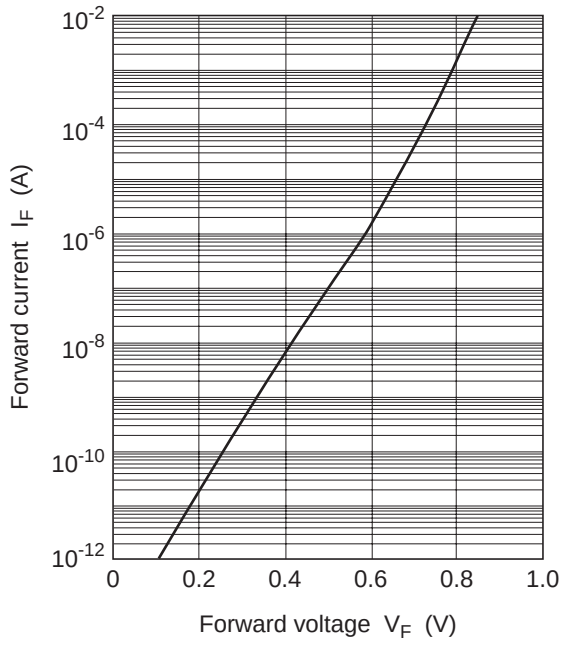


Fig.1 Forward current vs. Forward voltage

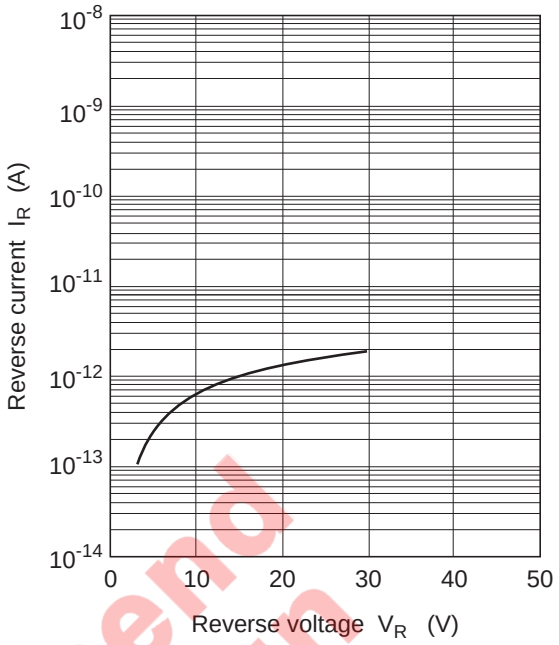


Fig.2 Reverse current vs. Reverse voltage

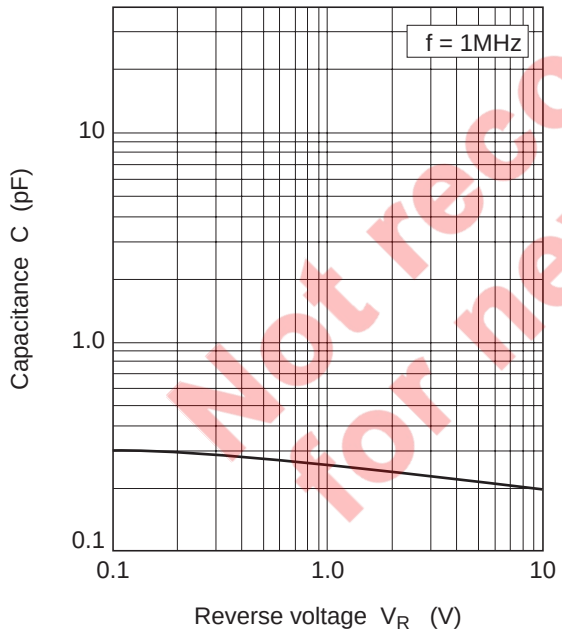


Fig.3 Capacitance vs. Reverse voltage

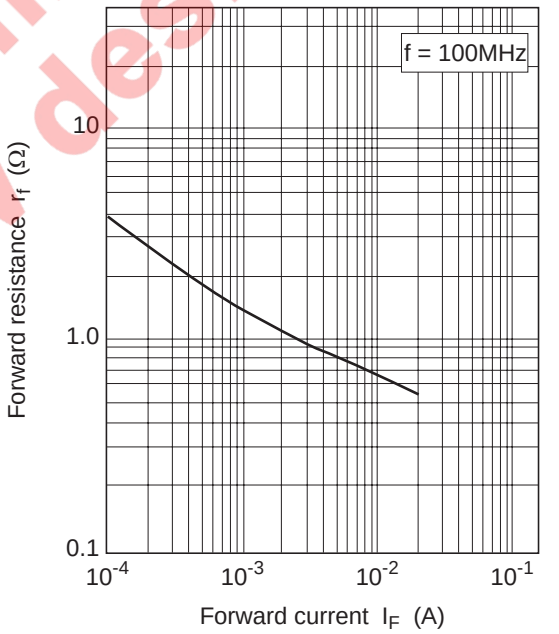
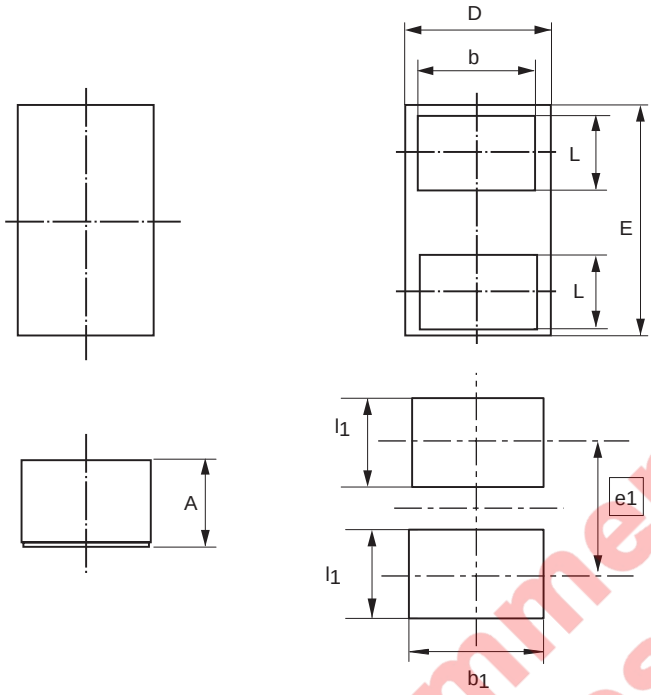


Fig.4 Forward resistance vs. Forward current

Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
MP8	—	PXSN0002ZA-A	MP8V	0.00029g

Under development



Pattern of terminal position areas

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	0.27	0.30	0.33
b	0.38	0.40	0.42
D	0.47	0.50	0.53
E	0.77	0.80	0.83
L	0.23	0.25	0.27
b1	—	0.45	—
e1	—	0.45	—
l_1	—	0.3	—

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