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# RKP201KM

Silicon Epitaxial Trench Pin Diode for Antenna Switching

REJ03G1226-0200 Rev.2.00 Nov 22, 2005

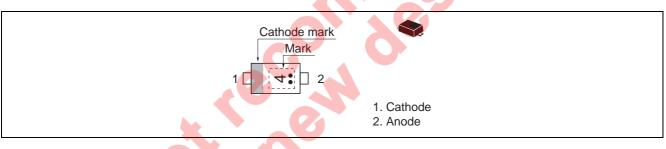
## Features

- Adopting the trench structure minimize terminal capacitance. (C = 0.35 pF max)
- Low forward resistance. ( $rf = 2.0 \Omega max$ )
- Low operation current.
- Thin Extremely small Flat Lead Package (TEFP) is suitable for surface mount design.

# **Ordering Information**



# **Pin Arrangement**





# **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$	
Item	Symbol	Value	Unit	
Reverse voltage	V <sub>R</sub>	30	V	
Forward current	I <sub>F</sub>	100	mA	
Power dissipation	Pd	100	mW	
Junction temperature	Тј	125	°C	
Storage temperature	Tstg	-55 to +125	°C	

# **Electrical Characteristics**

 $(Ta = 25^{\circ}C)$ 

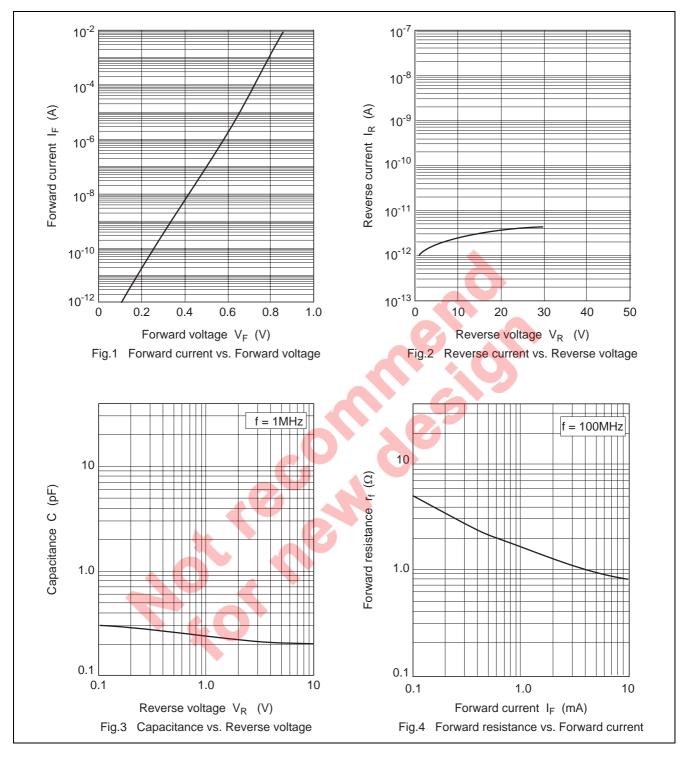
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I <sub>R</sub>	_	—	100	nA	V <sub>R</sub> = 30 V
Forward voltage	V <sub>F</sub>	_	—	0.9	V	$I_F = 2 \text{ mA}$
Capacitance	С	_	—	0.35	pF	$V_R = 1 V$ , f = 1 MHz
Forward resistance	r <sub>f</sub>	_	—	2.0	Ω	I <sub>F</sub> = 2 mA, f = 100 MHz
ESD-Capability *1	—	100	—	—	V	$C = 200 \text{ pF}, R = 0 \Omega$ , Both forward
						and reverse direction 1 pulse.

Notes: 1. Failure criterion ;  $I_R > 100 \mbox{ nA}$  at  $V_R$  = 30 V

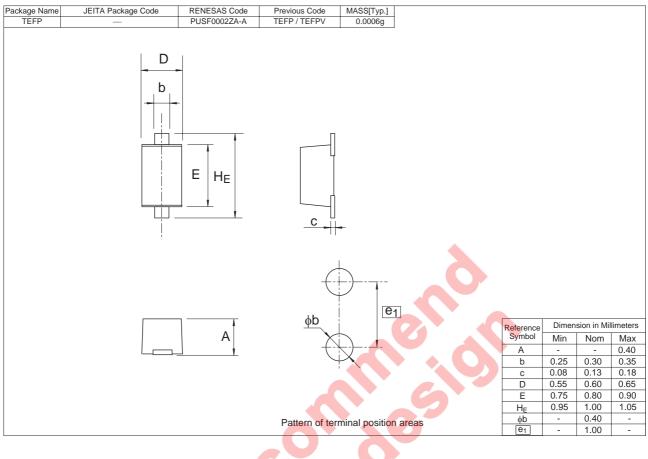
2. For TEFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.



# **Main Characteristic**



# **Package Dimensions**





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