

# RKP201KL

## Silicon Epitaxial Trench Pin Diode for Antenna Switching

REJ03G1225-0200

Rev.2.00

Sep 06, 2005

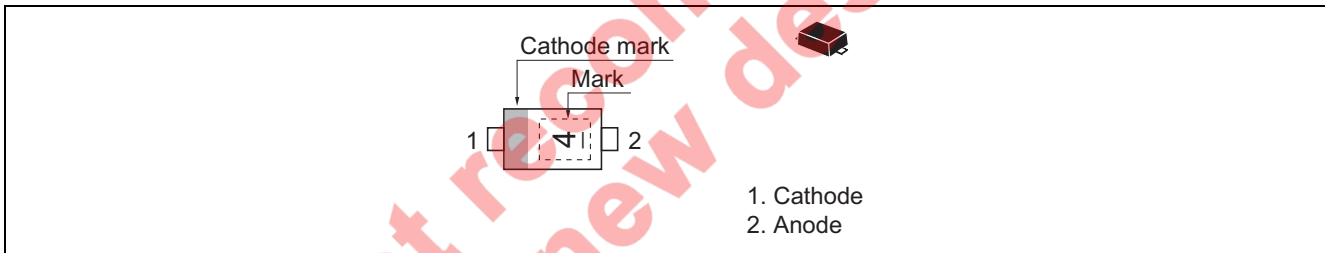
### Features

- Adopting the trench structure minimize terminal capacitance. ( $C = 0.35 \text{ pF max}$ )
- Low forward resistance. ( $r_f = 2.0 \Omega \text{ max}$ )
- Low operation current.
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

### Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
RKP201KL	4	EFP	PXSF0002ZA-A (EFP)

### Pin Arrangement



## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	$V_R$	30	V
Forward current	$I_F$	100	mA
Power dissipation	$P_d$	100	mW
Junction temperature	$T_j$	125	°C
Storage temperature	$T_{stg}$	-55 to +125	°C

## Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	$I_R$	—	—	100	nA	$V_R = 30\text{ V}$
Forward voltage	$V_F$	—	—	0.9	V	$I_F = 2\text{ mA}$
Capacitance	C	—	—	0.35	pF	$V_R = 1\text{ V}, f = 1\text{ MHz}$
Forward resistance	$r_f$	—	—	2.0	$\Omega$	$I_F = 2\text{ mA}, f = 100\text{ MHz}$
ESD-Capability *1	—	100	—	—	V	C = 200 pF, R = 0 $\Omega$ , Both forward and reverse direction 1 pulse.

Notes: 1. Failure criterion ;  $I_R > 100\text{ nA}$  at  $V_R = 30\text{ V}$

2. EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Not recommended  
for new designs

Main Characteristic

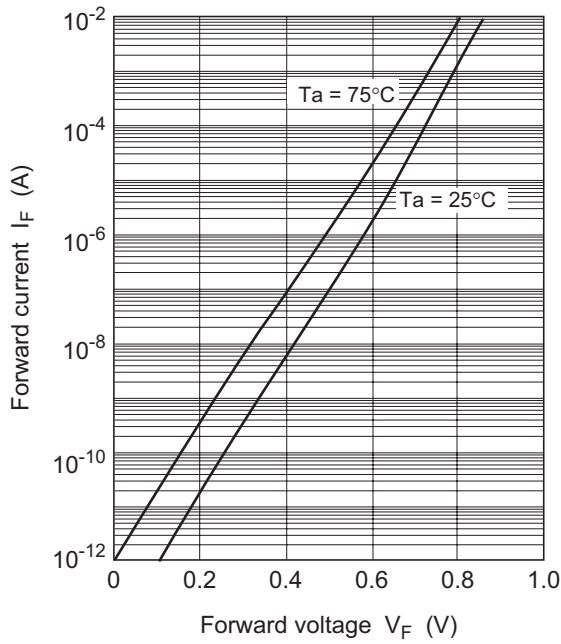


Fig.1 Forward current vs. Forward voltage

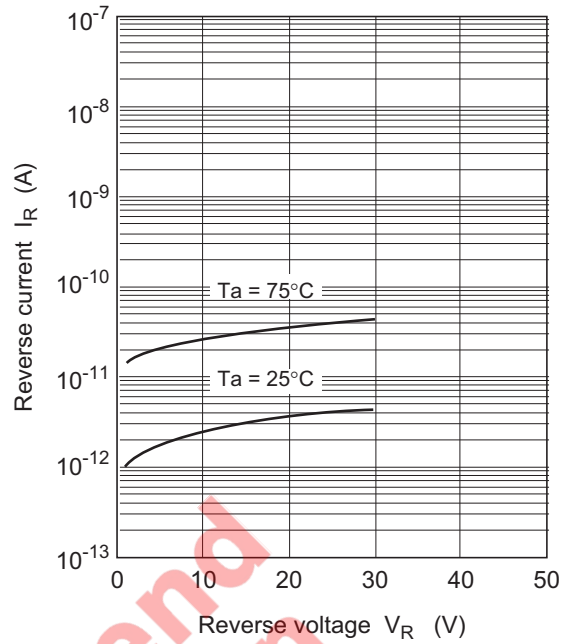


Fig.2 Reverse current vs. Reverse voltage

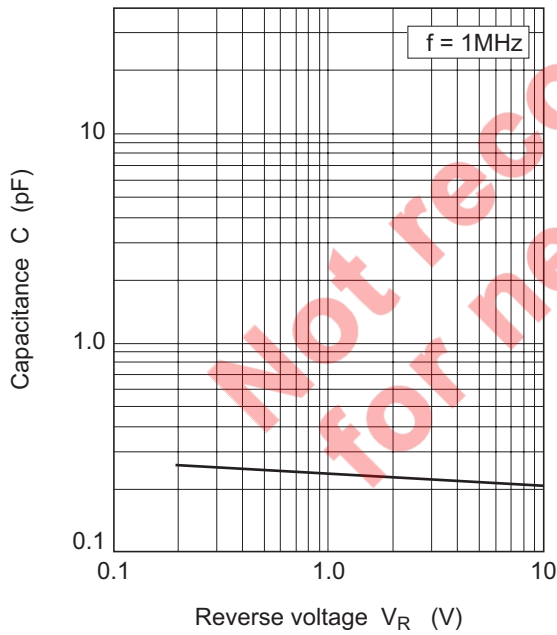


Fig.3 Capacitance vs. Reverse voltage

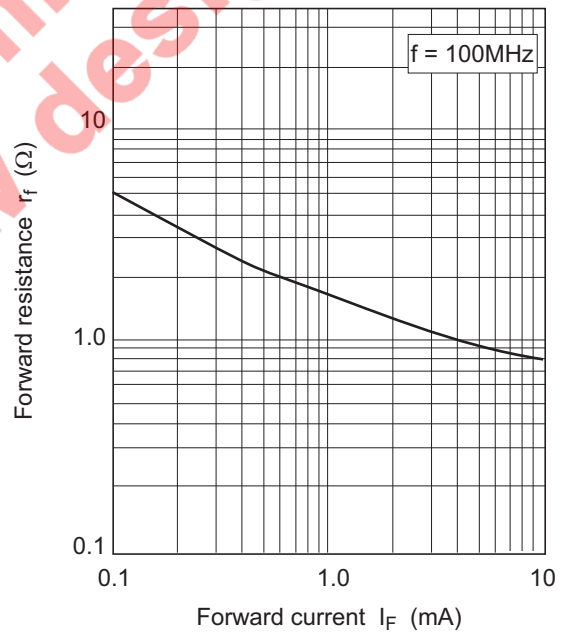
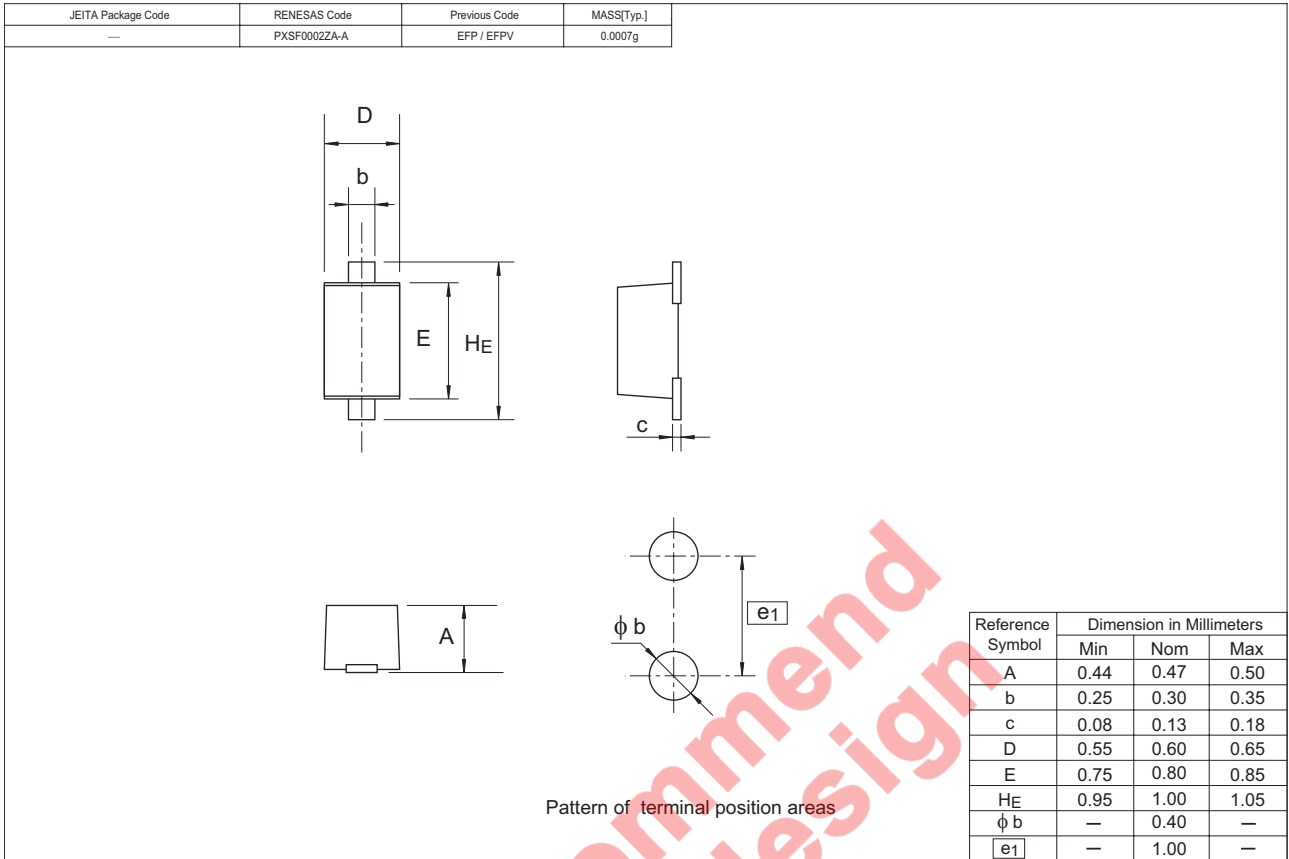


Fig.4 Forward resistance vs. Forward current

### Package Dimensions



Not recommend for new design

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