

HVD142A

Silicon Epitaxial Planar Pin Diode for Antenna Switching

REJ03G0428-0200 Rev.2.00 Sep 21, 2005

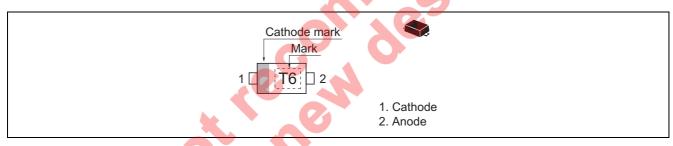
Features

- An optimal solution for antenna switching in mobile phones.
- Low capacitance. (C = 0.35 pF max)
- Low forward resistance. (rf = $1.3 \Omega \text{ max}$)
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
HVD142A	Т6	SFP	PUSF0002ZB-A
			(SFP)

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Reverse voltage	V _R	30	V
Forward current	I _F	100	mA
Power dissipation	Pd	150	mW
Junction temperature	Tj	125	°C
Storage temperature	Tstg	−55 to +125	°C

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

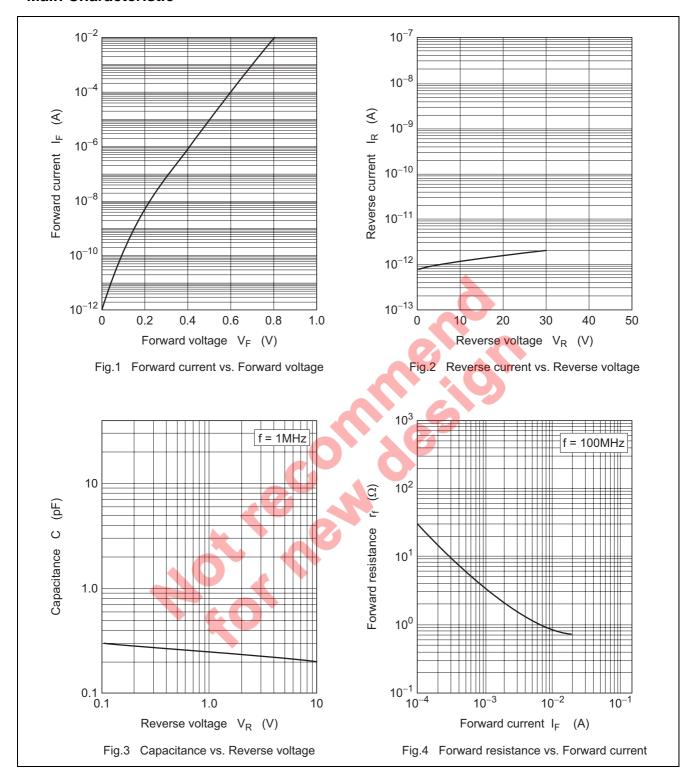
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _R	_	_	100	nA	$V_R = 30 \text{ V}$
Forward voltage	V _F	_	_	1.0	V	$I_F = 10 \text{ mA}$
Capacitance	С	_	_	0.35	pF	$V_R = 1 \text{ V}, f = 1 \text{ MHz}$
Forward resistance	r _f	_	_	1.3	Ω	I _F = 10 mA, f = 100 MHz
ESD-Capability *1	_	100	_	_	V	$C = 200 \text{ pF}, R = 0 \Omega$, Both forward
						and reverse direction 1 pulse.

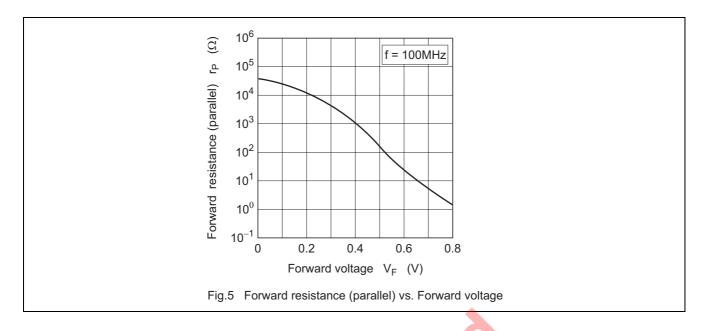
Notes: 1. Failure criterion; $I_R > 100 \text{ nA}$ at $V_R = 30 \text{ V}$

2. For SFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

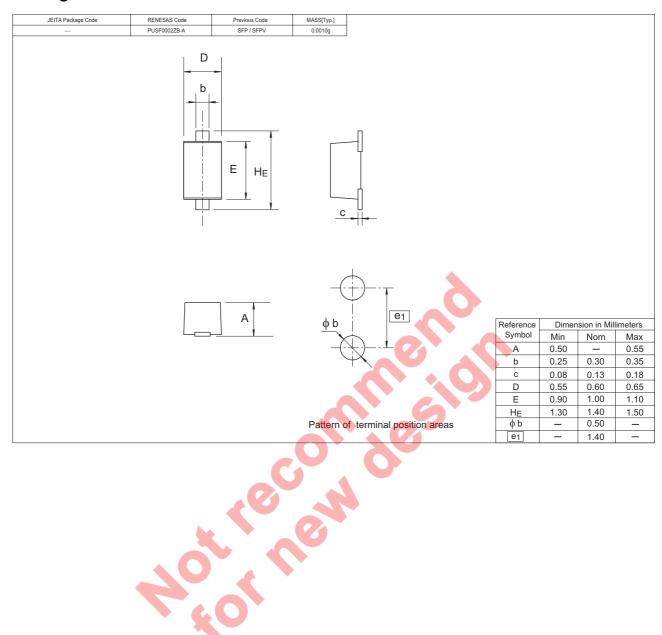


Main Characteristic





Package Dimensions



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