Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas E<mark>lect</mark>ronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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Silicon Epitaxial Trench Pin Diode for Antenna Switching



ADE-208-958A (Z)

Rev.1 May 2001

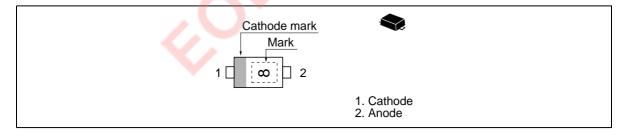
Features

- Adopting the trench structure improves low capacitance. (C = 0.9 pF max)
- Low forward resistance. (rf = $1.1 \Omega \text{ max}$)
- Low operation current.
- Super small Flat Package (SFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HVD138	8	SFP

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit	
Reverse voltage	V_R	30	V	
Forward current	I _F	100	mA	
Power dissipation	Pd	150	mW	
Junction temperature	Tj	125	°C	
Storage temperature	Tstg	-55 to +125	°C	

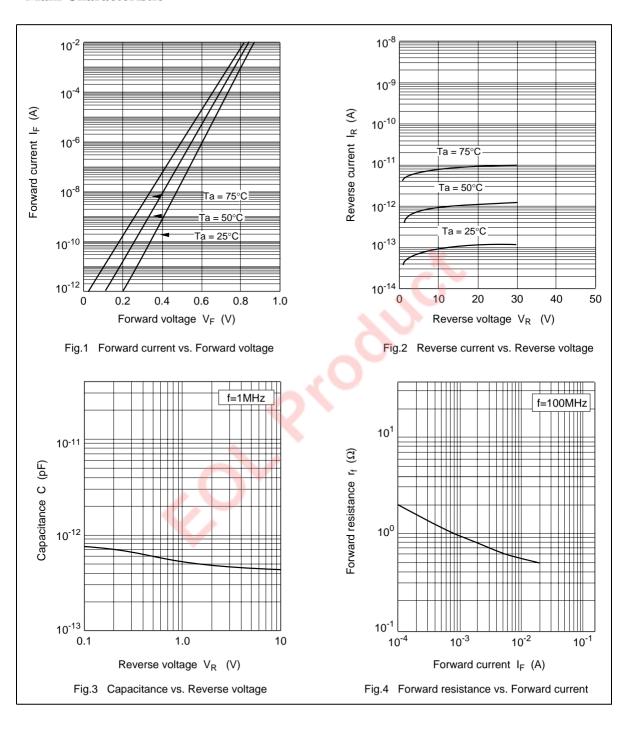
Electrical Characteristics

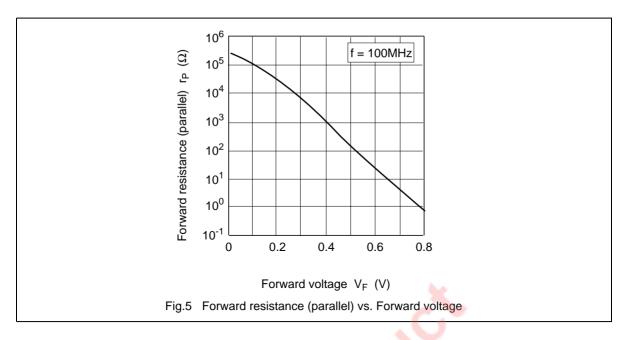
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _R	_	_	10	nA	V _R = 25 V
Forward voltage	V _F	_	_	0.9	V	I _F = 2 mA
Capacitance	С	_	_	0.9	pF	$V_R = 1 \text{ V, f} = 1 \text{ MHz}$
Forward resistance	r _f	_	_	1.1	Ω	I _F = 2 mA, f = 100 MHz

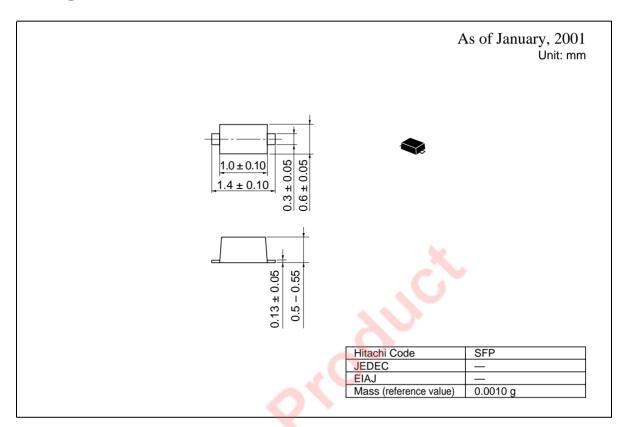
Note: 1. Please do not use the soldering iron due to avoid high stress to the SFP package.

Main Characteristic





Package Dimensions



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Semiconductor & Integrated Circuits Nippon Bldg., 2-6-2, Ohte-machi, Ch<mark>iyoda-ku</mark>, Tokyo 100-0004, Japan Tel: (03) 3270-2111 Fax: (03) 3270-5109

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For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive San Jose, CA 95134 Tel: <1> (408) 433-1990 Maidenhead

Electronic Components Group Whitebrook Park Lower Cookham Road Fax: <1>(408) 433-0223 Berkshire SL6 8YA, United Kingdom Fax: <65>-538-6933/538-3877

Tel: <44> (1628) 585000 Fax: <44> (1628) 585200

Hitachi Europe GmbH Electronic Components Group Dornacher Straße 3 D-85622 Feldkirchen, Munich Germany Tel: <49> (89) 9 9180-0

Fax: <49> (89) 9 29 30 00

Hitachi Asia Ltd. Hitachi Tower 16 Collyer Quay #20-00 Singapore 049318 Tel: <65>-538-6533/538-8577 URL: http://www.hitachi.com.sg

Hitachi Asia Ltd (Taipei Branch Office) 4/F. No. 167. Tun Hwa North Road Hung-Kuo Building

Taipei (105), Taiwan Tel: <886>-(2)-2718-3666 Fax: <886>-(2)-2718-8180 Telex: 23222 HAS-TP URL: http://www.hitachi.com.tw Hitachi Asia (Hong Kong) Ltd. Group III (Electronic Components) 7/F., North Tower World Finance Centre. Harbour City, Canton Road Tsim Sha Tsui, Kowloon Hong Kong Tel: <852>-(2)-735-9218

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