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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Not recommended
for new design

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HVD133A

Silicon Epitaxial Planar Pin Diode for Antenna Switching

REJ03G0171-0100Z

Rev.1.00

Jan.21.2004

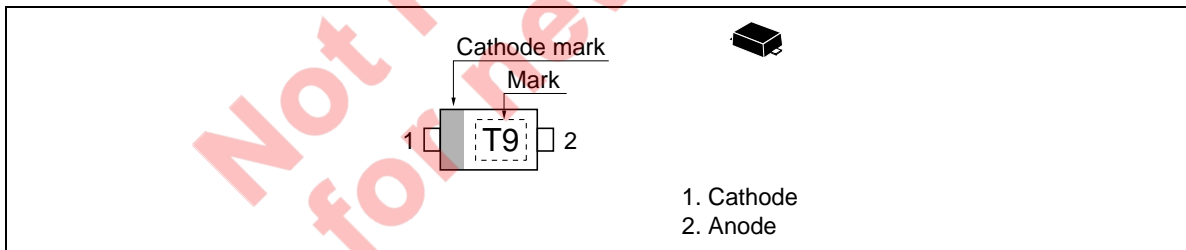
Features

- An optimal solution for antenna switching in mobile phones.
- Low capacitance. ($C1 = 1.0 \text{ pF max}$)
- Low forward resistance. ($r_f = 0.7 \Omega \text{ max}$)
- Super small Flat Package (SFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HVD133A	T9	SFP

Pin Arrangement



Absolute Maximum Ratings

(T_a = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V _R	30	V
Power dissipation	P _d	150	mW
Junction temperature	T _j	125	°C
Storage temperature	T _{stg}	-55 to +125	°C

Electrical Characteristics

(T_a = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse voltage	V _R	30	—	—	V	I _R = 1 μA
Reverse current	I _R	—	—	100	nA	V _R = 25 V
Forward voltage	V _F	—	—	0.85	V	I _F = 2 mA
Capacitance	C ₁	—	—	1.00	pF	V _R = 1 V, f = 1 MHz
	C ₆	—	—	0.90		V _R = 6 V, f = 1 MHz
Forward resistance	r _f	—	0.55	0.70	Ω	I _F = 2 mA, f = 100 MHz

Note: Please do not use the soldering iron due to avoid high stress to the SFP package.

Main Characteristic

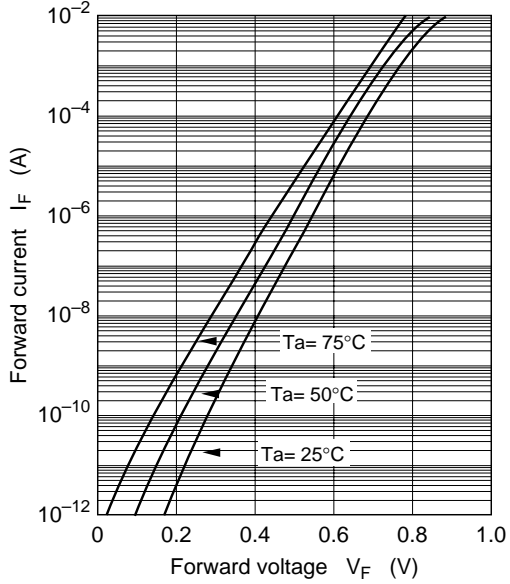


Fig.1 Forward current vs. Forward voltage

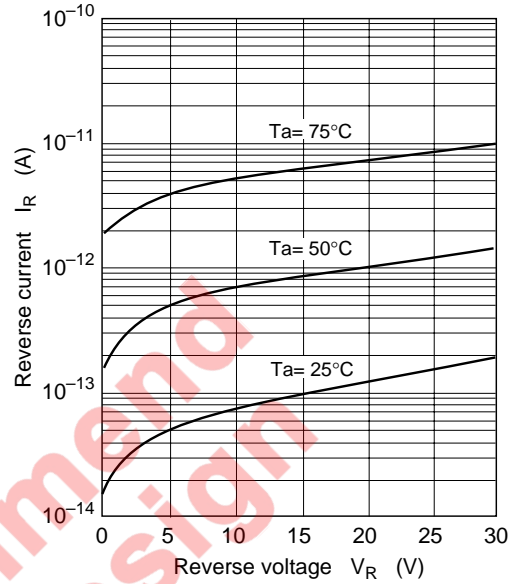


Fig.2 Reverse current vs. Reverse voltage

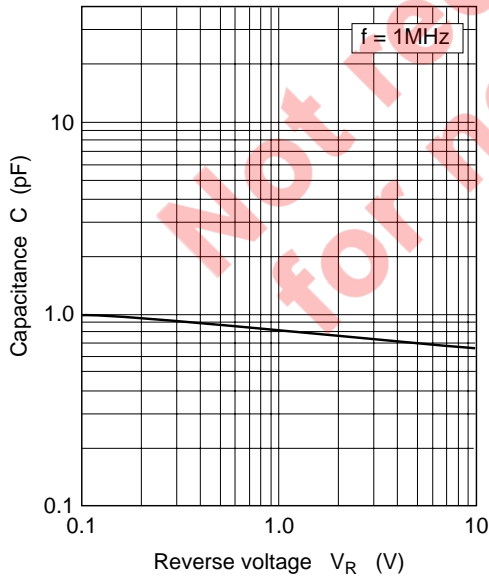


Fig.3 Capacitance vs. Reverse voltage

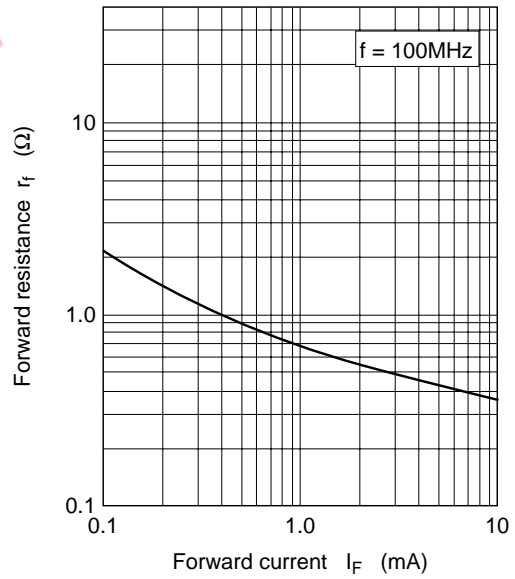
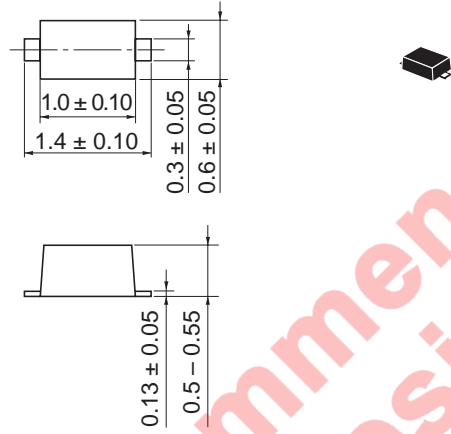


Fig.4 Forward resistance vs. Forward current

Package Dimensions

As of January, 2003
Unit: mm



Package Code	SFP
JEDEC	—
JEITA	—
Mass (reference value)	0.0010 g

Not recommend
for new design

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