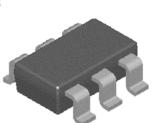
Analog Power AM3906N

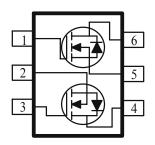
Dual N-Channel Logical Level MOSFET

These miniature surface mount MOSFETs utilize High Cell Density process. Low $r_{DS(on)}$ assures minimal power loss and conserves energy, making this device ideal for use in power management circuitry. Typical applications are DC-DC converters, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}$ (OHM)	$I_{D}(A)$		
20	$0.099 @ V_{GS} = 10 V$	2.5		
30	$0.142 @ V_{GS} = 4.5V$	2.0		

- Low r_{DS(on)} Provides Higher Efficiency and Extends Battery Life
- Miniature TSOP-6 Surface Mount Package Saves Board Space
- Very fast switching
- Lower gate charge (2.2 nC)





ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Maximum	Units			
Drain-Source Voltage			30	V		
Gate-Source Voltage		V_{GS}	±20	V		
Continuous Drain Current ^a	$T_A=25^{\circ}C$		2.5			
Continuous Drain Current	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	П	2	A		
Pulsed Drain Current ^b		I_{DM}	10			
Continuous Source Current (Diode Conduction) ^a		I_S	±0.8	A		
D D: a	$T_A=25^{\circ}C$	D	0.95	W		
Power Dissipation ^a	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	L D	0.7			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Maximum	Units		
Maximum lunation to Ambient ^a	t <= 5 sec	D	130	°C/W		
Maximum Junction-to-Ambient ^a	Steady-State	K_{THJA}	176			

1

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

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SPECIFICATIONS (T _A = 25°C UNLESS OTHERWISE NOTED)							
Parameter	Crowbal	Test Conditions	Limits			T I24	
Parameter	Symbol	Symbol Test Conditions		Тур	Max	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_{D} = -250 \text{ uA}$	30			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \text{ uA}$	1.00	1.85	3.1]	
Gate-Body Leakage	Igss	$V_{DS} = 0 \text{ V}, V_{GS} = 20 \text{ V}$			±100	nA	
Zana Cata Valta aa Duain Cumant	T	$V_{DS} = 240 \text{ V}, V_{GS} = 0 \text{ V}$			1	- A	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^{\circ}\text{C}$			10	uA	
On-State Drain Current ^A	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	10			Α	
- · · · · · · · · · · · · · · · · · · ·	***************************************	$V_{GS} = 10 \text{ V}, I_D = -2.5 \text{ A}$		0.082	0.099	Ω	
Drain-Source On-Resistance ^A	fDS(on)	$V_{GS} = 4.5 \text{ V}, I_D = 2.0 \text{ A}$		0.113	0.142	2.2	
Forward Tranconductance ^A	gs	$V_{DS} = 5 \text{ V}, I_D = 2.5 \text{ A}$		3		S	
Diode Forward Voltage	V _{SD}	$I_S = -1.6 \text{ A}, V_{GS} = 0 \text{ V}$		-0.7	-1.2	V	
Dynamic ^b							
Total Gate Charge	Qg			2.3	3.3		
Gate-Source Charge	Qgs	$V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 2.5 \text{ A}$		0.7	1.0	nC	
Gate-Drain Charge	Qgd			0.9	1.4		
Switching	-						
Turn-On Delay Time	td(on)			6.5	13		
Rise Time	tr	$V_{DD} = 5 \text{ V}, \qquad \text{ID} = 2.5 \text{ A}, \qquad V_{GEN}$		11	19] "	
Turn-Off Delay Time	td(off)	$= 10 \text{ V}, \text{R}_{\text{G}} = 6 \Omega$		13	24	ns	
Fall-Time	tf			3	7		

Notes

- a. Pulse test: $PW \le 300us duty cycle \le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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Analog Power AM3906N

Typical Electrical Characteristics

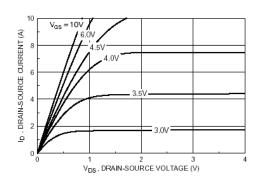


Figure 1. On-Region Characteristics

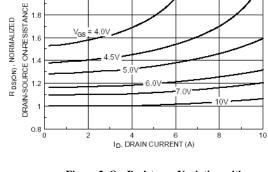


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage

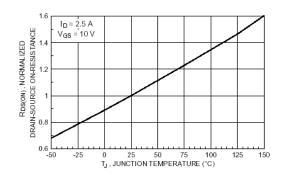


Figure 3. On-Resistance Variation with Temperature

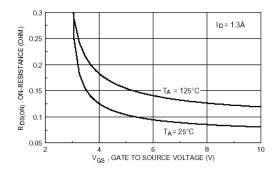


Figure 4. On-Resistance Variation with Gate to Source Voltage

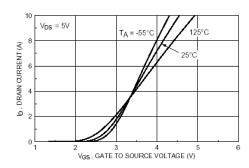


Figure 5. Transfer Characteristics

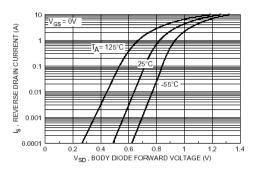


Figure 6. Body Diode Forward Voltage

Variation with Source Current and

Temperature

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Typical Electrical Characteristics

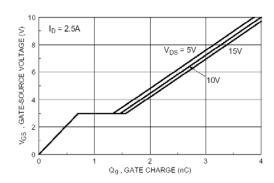


Figure 7. Gate Charge Characteristics.

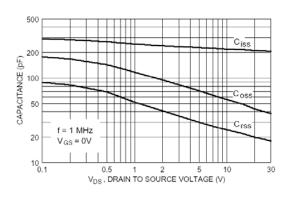


Figure 8. Capacitance Characteristics.

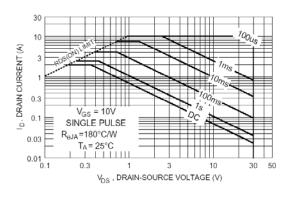


Figure 9. Maximum Safe Operating Area

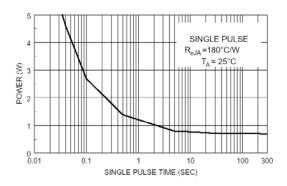


Figure 10. Single Pulse Maximum Power
Dissipation

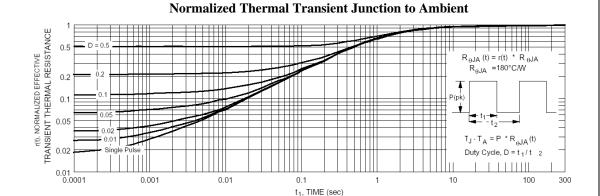


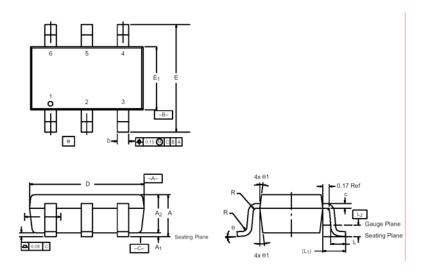
Figure 11. Transient Thermal Response Curve.

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Package Information

TSOP-6: 6LEAD



	MILLIMETERS INCHES			;		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.91	-	1.10	0.036	-	0.043
A ₁	0.01	-	0.10	0.0004	-	0.004
A ₂	0.84	-	1.00	0.033	0.038	0.039
b	0.30	0.32	0.45	0.012	0.013	0.018
С	0.10	0.15	0.20	0.004	0.006	0.008
D	2.95	3.05	3.10	0.116	0.120	0.122
E	2.70	2.85	2.98	0.106	0.112	0.117
E ₁	1.55	1.65	1.70	0.061	0.065	0.067
е	1.00 BSC			0.0394 BSC		
L	0.35	_	0.50	0.014	_	0.020
L ₁	0.60 Ref			0.024 Ref		
L ₂		0.25 BSC		0.010 BSC		
R	0.10	_	-	0.004	_	_
θ	0°	4°	8°	0°	4°	8°
θ_1	7° Nom			7° Nom		