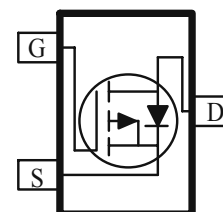
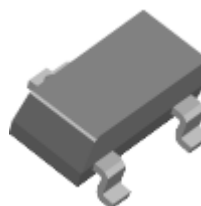


P - Channel 40V (D-S) MOSFET

These miniature surface mount MOSFETs utilize High Cell Density process. Low $r_{DS(on)}$ assures minimal power loss and conserves energy, making this device ideal for use in power management circuitry. Typical applications are lower voltage application, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low $r_{DS(on)}$ Provides Higher Efficiency and Extends Battery Life
- Fast Switch
- Low Gate Charge
- Miniature SOT-23 Surface Mount Package Saves Board Space

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
-40	0.082 @ $V_{GS} = -10$ V	-3.2
	0.130 @ $V_{GS} = -4.5$ V	-2.6



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)			
Parameter	Symbol	Ratings	Units
Drain-Source Voltage	V_{DS}	-40	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ^a	I_D	$T_A=25^\circ\text{C}$	± 3.2
		$T_A=70^\circ\text{C}$	± 2.7
Pulsed Drain Current ^b	I_{DM}	± 10	A
Continuous Source Current (Diode Conduction) ^a	I_S	0.4	A
Power Dissipation ^a	P_D	$T_A=25^\circ\text{C}$	1.25
		$T_A=70^\circ\text{C}$	0.8
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	R_{THJA}	t \leq 5 sec	100
		Steady-State	150

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

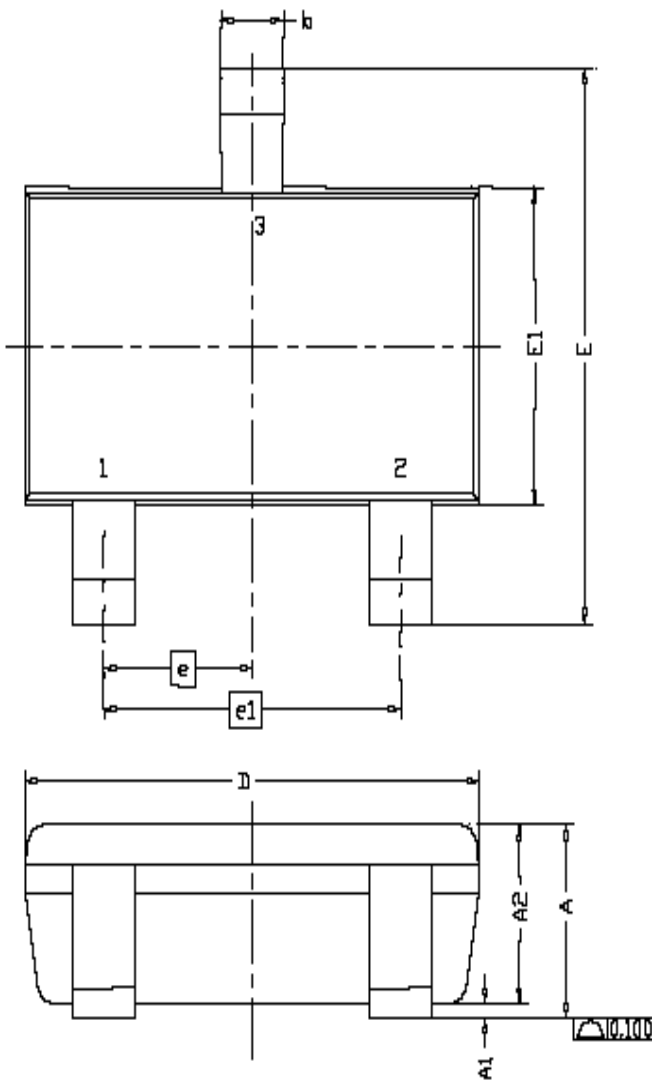
SPECIFICATIONS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Conditions	Limits			Unit
			Min	Typ	Max	
Switch Off Characteristics						
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -32\text{ V}, V_{GS} = 0\text{ V}$			-1	μA
		$V_{DS} = -32\text{ V}, V_{GS} = 0\text{ V}, T_J = 55^\circ\text{C}$			-10	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-1.0			V
On-State Drain Current ^A	$I_{D(on)}$	$V_{DS} = -5\text{ V}, V_{GS} = -4.5\text{ V}$	-2			A
Drain-Source On-Resistance ^A	$r_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -3.2\text{ A}$			82	m Ω
		$V_{GS} = -4.5\text{ V}, I_D = -2.6\text{ A}$			130	
Forward Transconductance ^A	g_{fs}	$V_{DS} = -5\text{ V}, I_D = -3.6\text{ A}$		2		S
Diode Forward Voltage	V_{SD}	$I_S = -0.4\text{ A}, V_{GS} = 0\text{ V}$		-0.70		V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = -10\text{ V}, V_{GS} = -5\text{ V},$ $I_D = -3.6\text{ A}$		15		nC
Gate-Source Charge	Q_{gs}			2.0		
Gate-Drain Charge	Q_{gd}			2.0		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = -15\text{ V}, I_D = -1\text{ A},$ $R_G = 50\text{ }\Omega, V_{GEN} = -10\text{ V}$		10		ns
Rise Time	t_r			2.8		
Turn-Off Delay Time	$t_{d(off)}$			53.6		
Fall-Time	t_f			46		

Notes

- Pulse test: $PW \leq 300\mu\text{s}$ duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.

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Package Information



DIM.	MILLIMETERS		
	MIN	NOM	MAX
A	0.935	0.95	1.10
A1	0.01	---	0.10
A2	0.85	0.90	0.925
b	0.30	0.40	0.50
c	0.10	0.15	0.25
D	2.70	2.90	3.10
E	2.60	2.80	3.00
E1	1.40	1.60	1.80
e	0.95 BSC		
e1	1.90 BSC		
L	0.30	0.40	0.60
L1	0.60REF		
L2	0.25BSC		
R	0.10	---	---
θ	0°	4°	8°
θ1	7°NOM		

