

## P-Channel 60-V (D-S) MOSFET

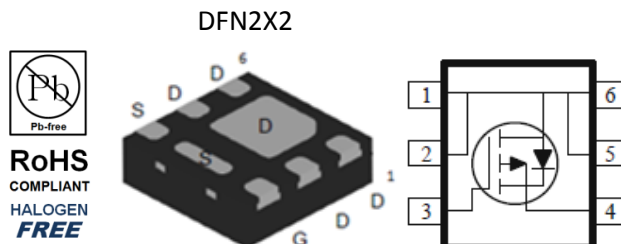
### Key Features:

- Low  $r_{DS(on)}$  trench technology
- Low thermal impedance
- Fast switching speed

### Typical Applications:

- Load Switches
- DC/DC Conversion
- Motor Drives

PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ (m $\Omega$ )	$I_D$ (A)
-60	82 @ $V_{GS} = -10V$	-5.0
	100 @ $V_{GS} = -4.5V$	-4.5



ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Units
Drain-Source Voltage		$V_{DS}$	-60	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	
Continuous Drain Current <sup>a</sup>	$T_A = 25^\circ\text{C}$	$I_D$	-5.0	A
	$T_A = 70^\circ\text{C}$		-4	
Pulsed Drain Current <sup>b</sup>		$I_{DM}$	-25	
Continuous Source Current (Diode Conduction) <sup>a</sup>		$I_S$	-4.1	A
Power Dissipation <sup>a</sup>	$T_A = 25^\circ\text{C}$	$P_D$	3	W
	$T_A = 70^\circ\text{C}$		1.9	
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Maximum	Units
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 10$ sec	$R_{\theta JA}$	40	$^\circ\text{C/W}$
	Steady State		90	

### Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature

## Electrical Characteristics

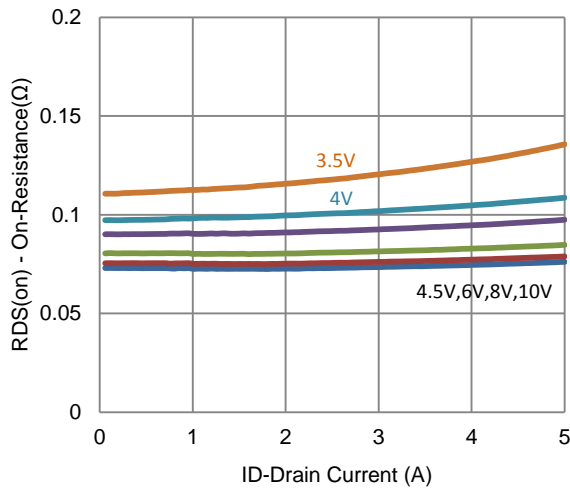
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static</b>						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-1			V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -48 V, V_{GS} = 0 V$			-1	uA
		$V_{DS} = -48 V, V_{GS} = 0 V, T_J = 55^\circ C$			-10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} = -5 V, V_{GS} = -10 V$	-7.5			A
Drain-Source On-Resistance <sup>a</sup>	$r_{DS(on)}$	$V_{GS} = -10 V, I_D = -4 A$			82	mΩ
		$V_{GS} = -4.5 V, I_D = -3.2 A$			100	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -15 V, I_D = -4 A$		9		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = -2.1 A, V_{GS} = 0 V$		-0.83		V
<b>Dynamic <sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = -30 V, V_{GS} = -4.5 V,$ $I_D = -4 A$		10		nC
Gate-Source Charge	$Q_{gs}$			4.2		
Gate-Drain Charge	$Q_{gd}$			3.1		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = -30 V, R_L = 7.5 \Omega,$ $I_D = -4 A,$ $V_{GEN} = -10 V, R_{GEN} = 6 \Omega$		7		ns
Rise Time	$t_r$			5		
Turn-Off Delay Time	$t_{d(off)}$			37		
Fall Time	$t_f$			14		
Input Capacitance	$C_{iss}$	$V_{DS} = -15 V, V_{GS} = 0 V, f = 1 Mhz$		1146		pF
Output Capacitance	$C_{oss}$			84		
Reverse Transfer Capacitance	$C_{rss}$			60		

## Notes

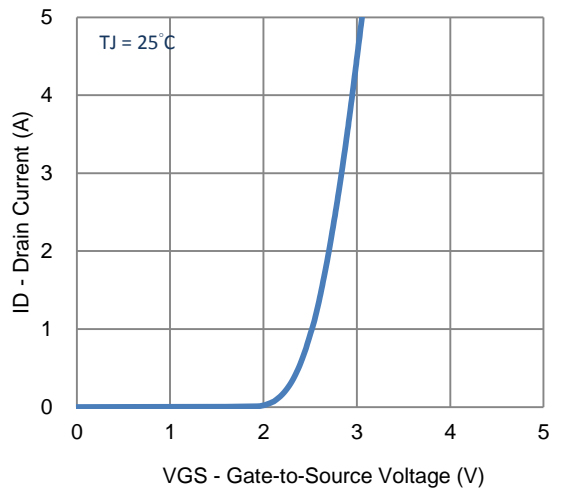
- Pulse test: PW ≤ 300us duty cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.

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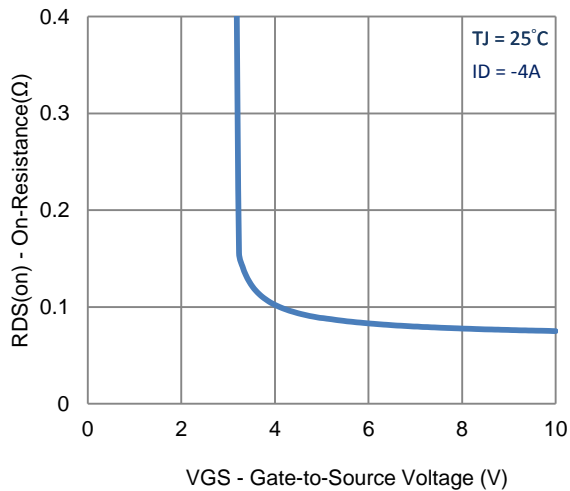
Typical Electrical Characteristics



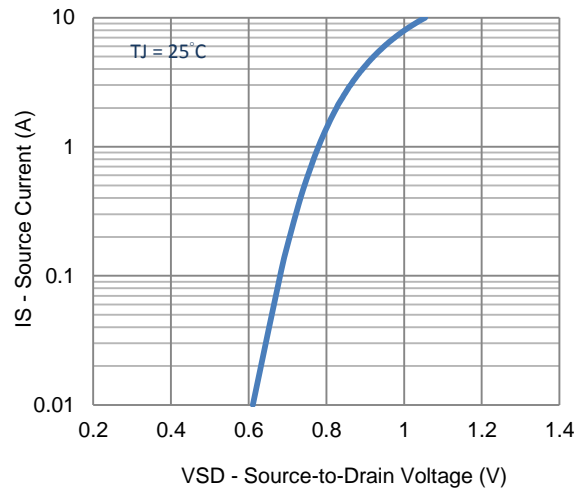
1. On-Resistance vs. Drain Current



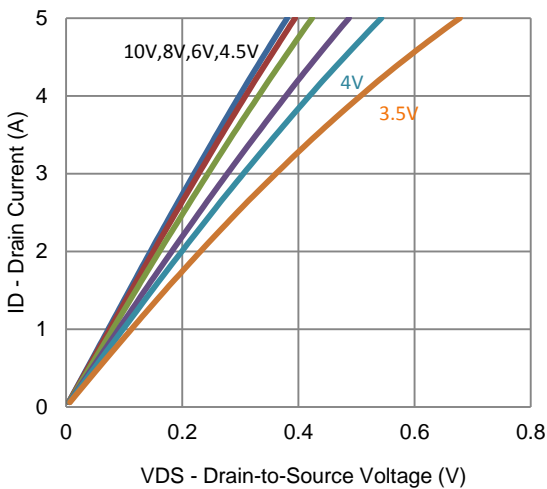
2. Transfer Characteristics



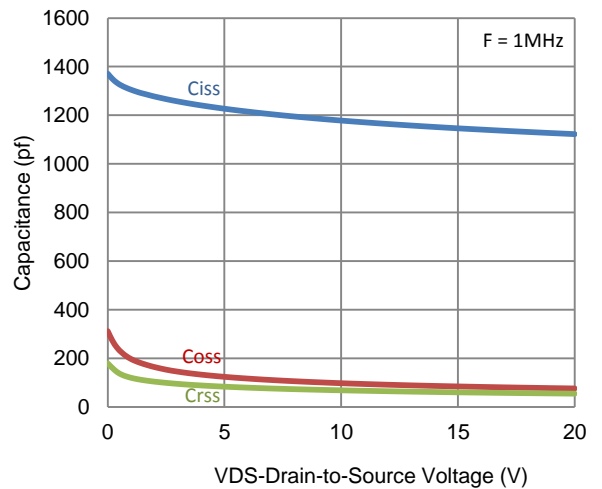
3. On-Resistance vs. Gate-to-Source Voltage



4. Drain-to-Source Forward Voltage

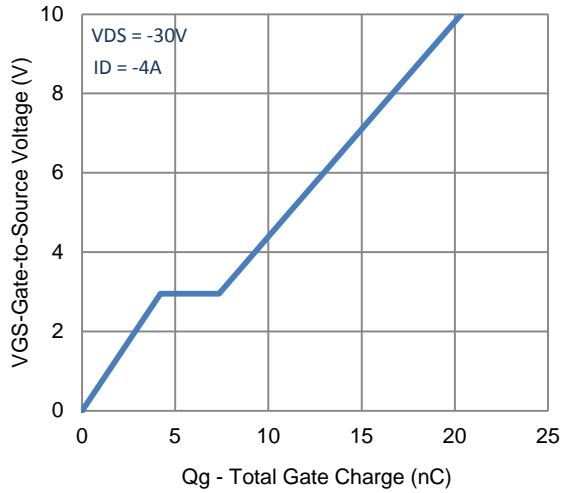


5. Output Characteristics

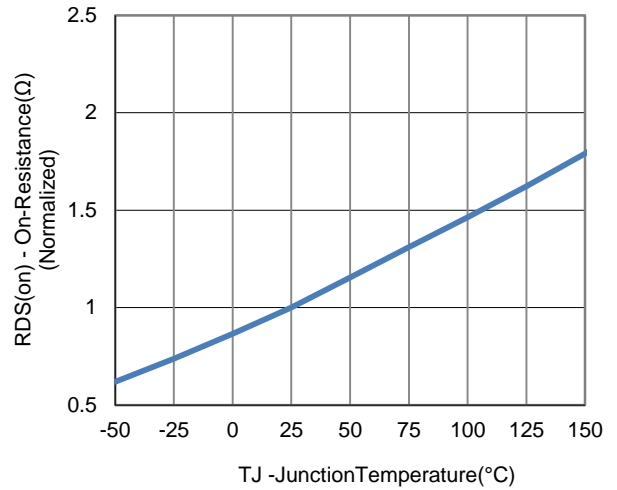


6. Capacitance

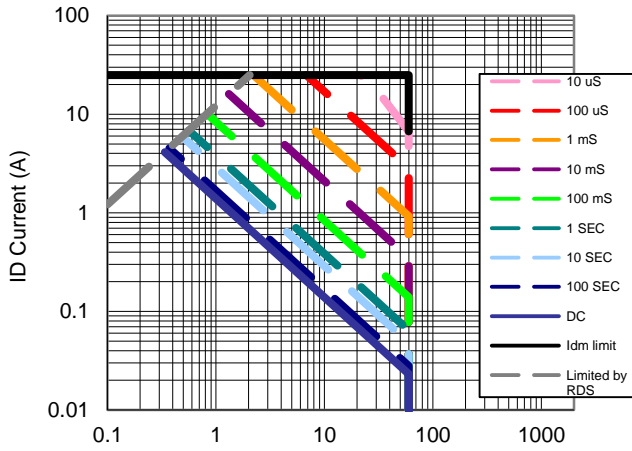
Typical Electrical Characteristics



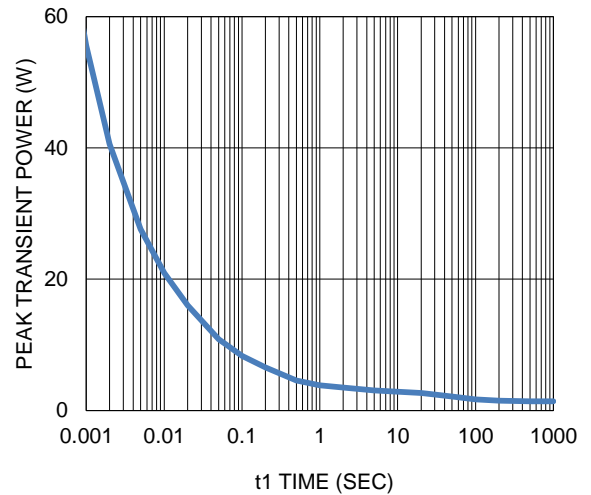
7. Gate Charge



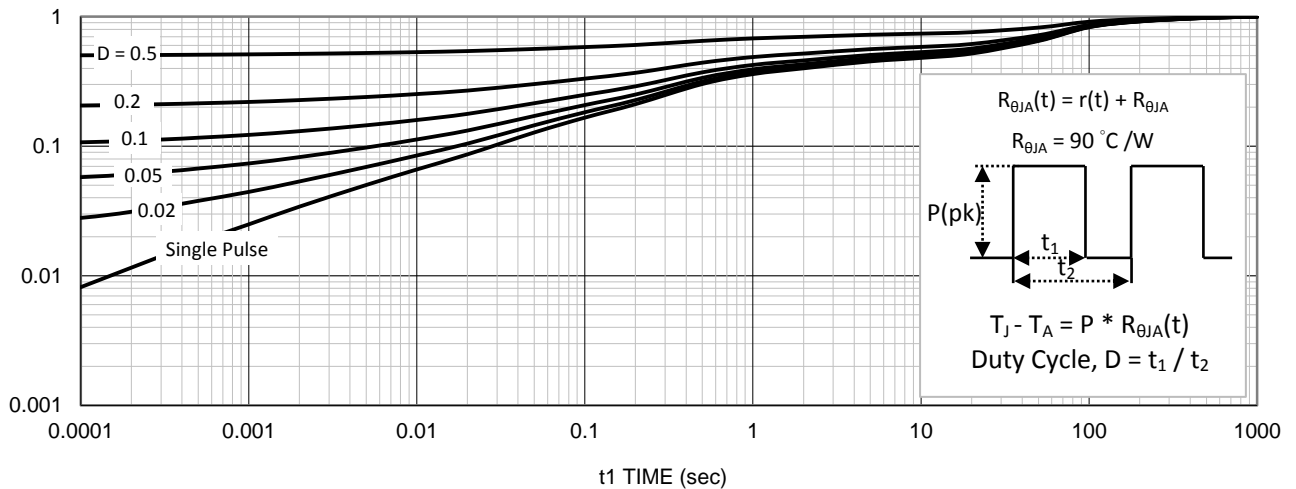
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area

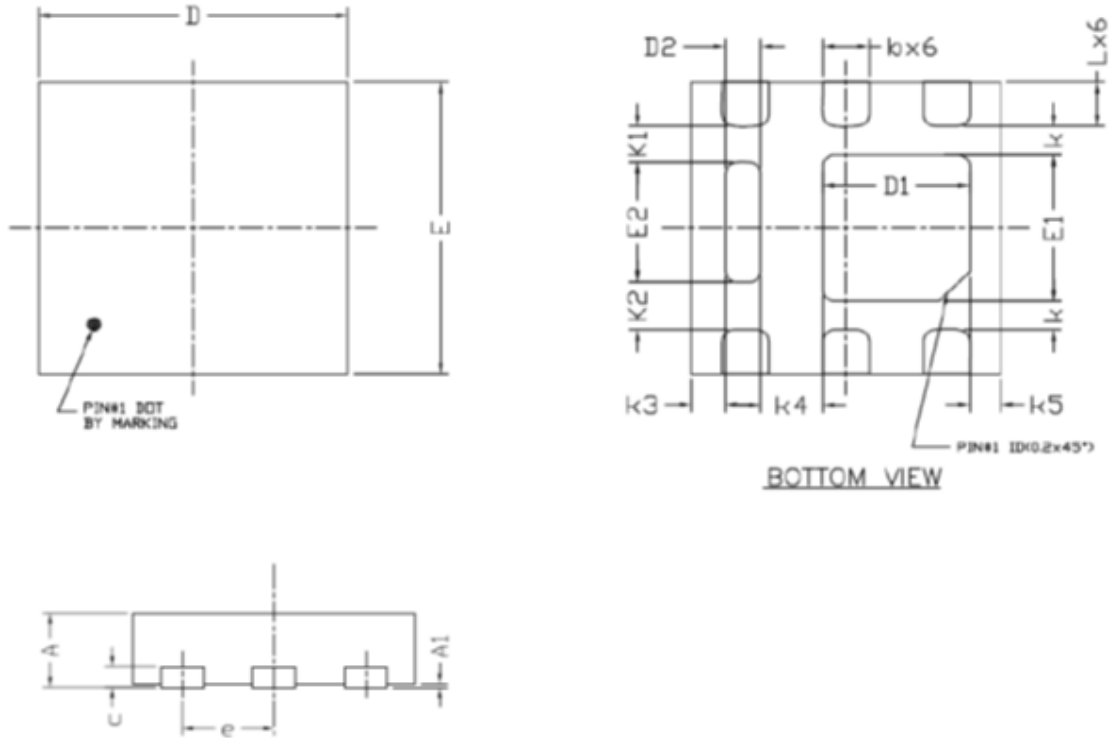


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.50	0.55	0.60	0.020	0.022	0.024
A1	0.00	—	0.05	0.000	—	0.002
b	0.25	0.30	0.35	0.010	0.012	0.014
c	0.152 REF			0.006 REF		
D	1.90	2.00	2.10	0.075	0.079	0.083
D1	0.85	0.95	1.05	0.033	0.037	0.041
D2	0.13	0.23	0.33	0.005	0.009	0.013
E	1.90	2.00	2.10	0.075	0.079	0.083
E1	0.90	1.00	1.10	0.035	0.039	0.043
E2	0.72	0.82	0.92	0.028	0.032	0.036
e	0.65 BSC			0.026 BSC		
K	0.20 BSC			0.008 BSC		
K1	0.25 BSC			0.010 BSC		
K2	0.33 BSC			0.013 BSC		
K3	0.22 BSC			0.009 BSC		
K4	0.40 BSC			0.016 BSC		
K5	0.20 BSC			0.008 BSC		
L	0.25	0.30	0.35	0.010	0.012	0.014