

P-Channel 150-V (D-S) MOSFET

Key Features:

- Low $r_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed

Typical Applications:

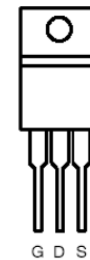
- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (m Ω)	I_D (A)
-150	75 @ $V_{GS} = -10V$	-52 ^a
	80 @ $V_{GS} = -4.5V$	



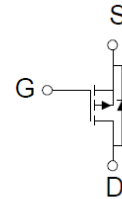
RoHS
COMPLIANT
HALOGEN
FREE

TO-220AB



Top View

DRAIN
connected
to TAB



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	Limit	Units
Drain-Source Voltage	V_{DS}	-150	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ^a	I_D	-52	A
Pulsed Drain Current ^b	I_{DM}	-210	
Continuous Source Current (Diode Conduction) ^a	I_S	-52	A
Power Dissipation ^a	P_D	300	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$
Maximum Junction-to-Case	$R_{\theta JC}$	1	

Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature

Electrical Characteristics

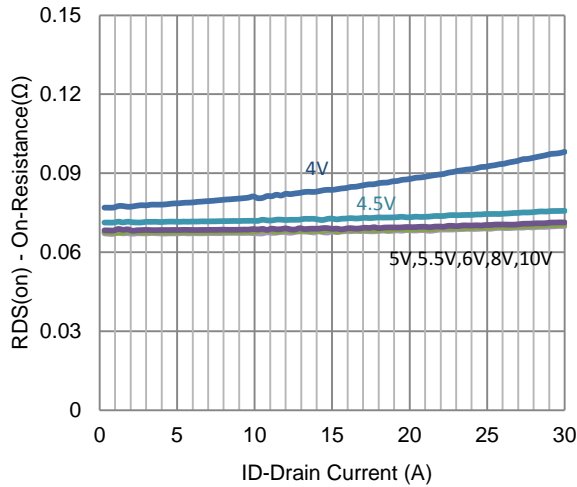
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-1			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -120 V, V_{GS} = 0 V$			-1	uA
		$V_{DS} = -120 V, V_{GS} = 0 V, T_J = 55^\circ C$			-25	
On-State Drain Current	$I_{D(on)}$	$V_{DS} = -5 V, V_{GS} = -10 V$	-110			A
Drain-Source On-Resistance	$r_{DS(on)}$	$V_{GS} = -10 V, I_D = -26 A$			75	m Ω
		$V_{GS} = -4.5 V, I_D = -25 A$			80	
Forward Transconductance	g_{fs}	$V_{DS} = -15 V, I_D = -20 A$		40		S
Diode Forward Voltage	V_{SD}	$I_S = -26 A, V_{GS} = 0 V$		-0.86		V
Dynamic						
Total Gate Charge	Q_g	$V_{DS} = -75 V, V_{GS} = -4.5 V,$ $I_D = -20 A$		138		nC
Gate-Source Charge	Q_{gs}			51		
Gate-Drain Charge	Q_{gd}			76		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = -75 V, R_L = 3.8 \Omega,$ $I_D = -20 A,$ $V_{GEN} = -10 V, R_{GEN} = 6 \Omega$		27		ns
Rise Time	t_r			55		
Turn-Off Delay Time	$t_{d(off)}$			258		
Fall Time	t_f			103		
Input Capacitance	C_{iss}	$V_{DS} = -15 V, V_{GS} = 0 V, f = 1 MHz$		14124		pF
Output Capacitance	C_{oss}			557		
Reverse Transfer Capacitance	C_{rss}			527		

Notes

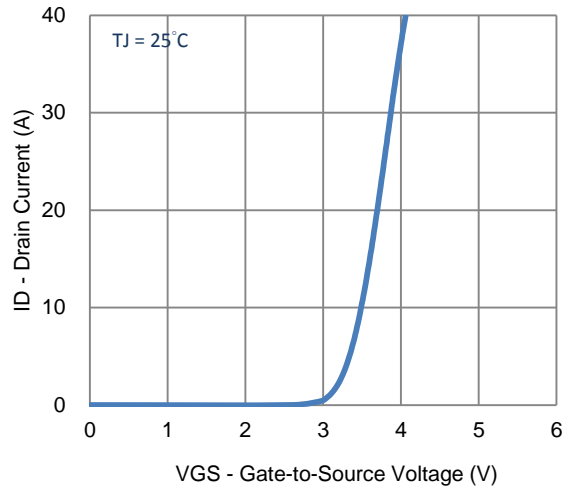
- Pulse test: PW \leq 300us duty cycle \leq 2%.
- Guaranteed by design, not subject to production testing.

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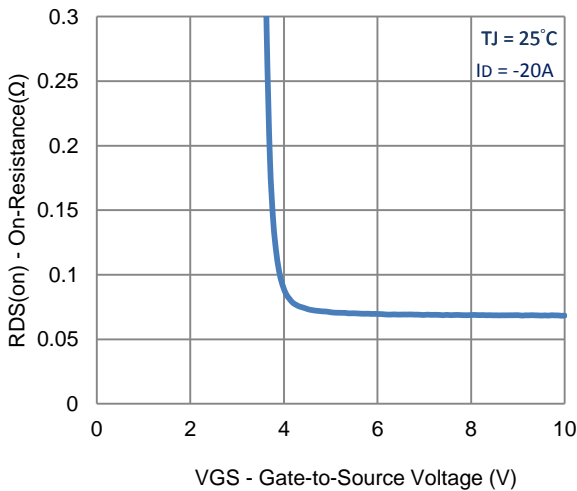
Typical Electrical Characteristics



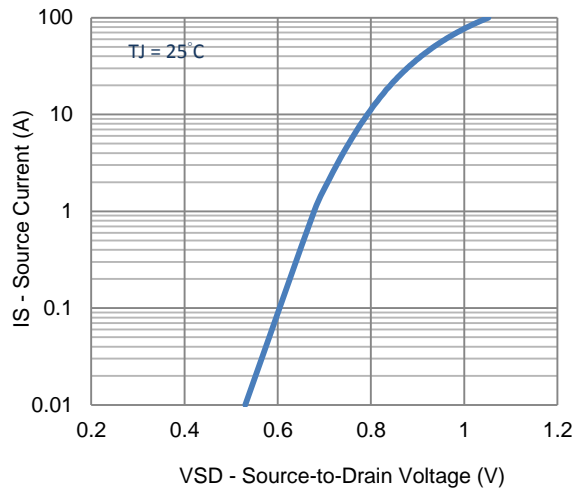
1. On-Resistance vs. Drain Current



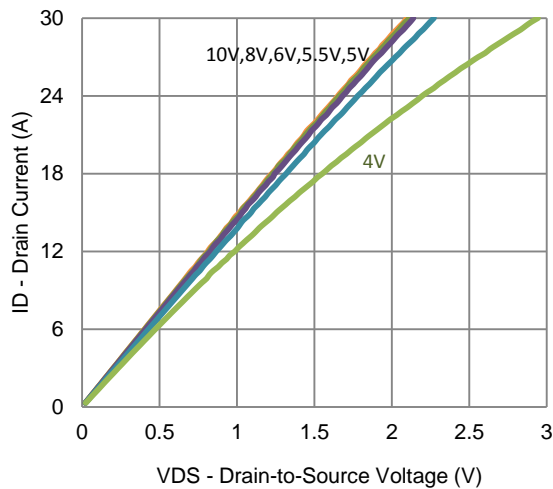
2. Transfer Characteristics



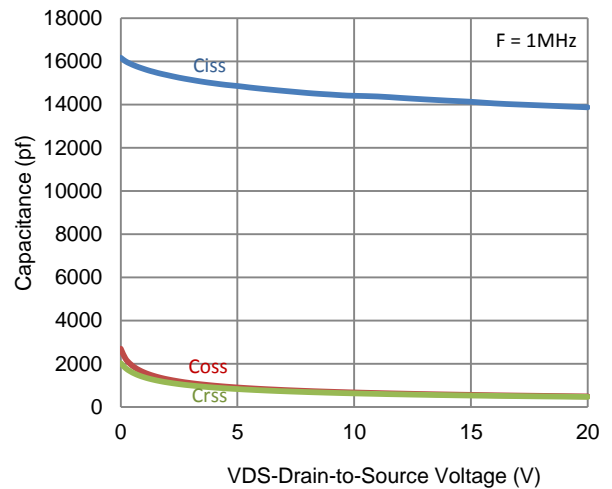
3. On-Resistance vs. Gate-to-Source Voltage



4. Drain-to-Source Forward Voltage

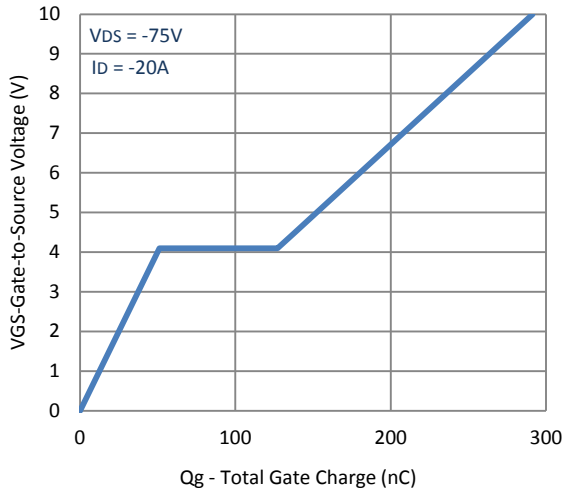


5. Output Characteristics

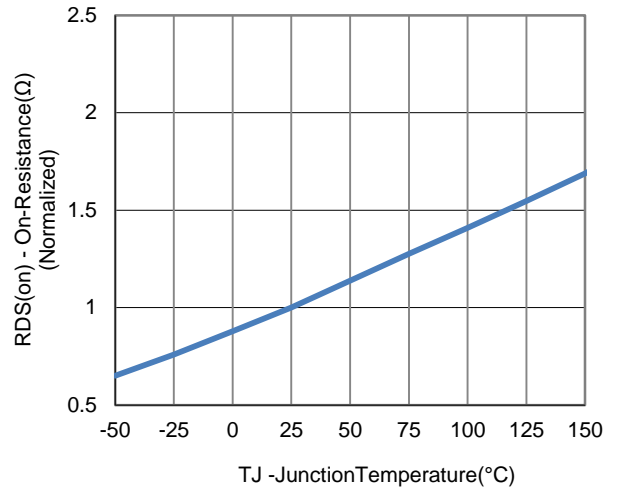


6. Capacitance

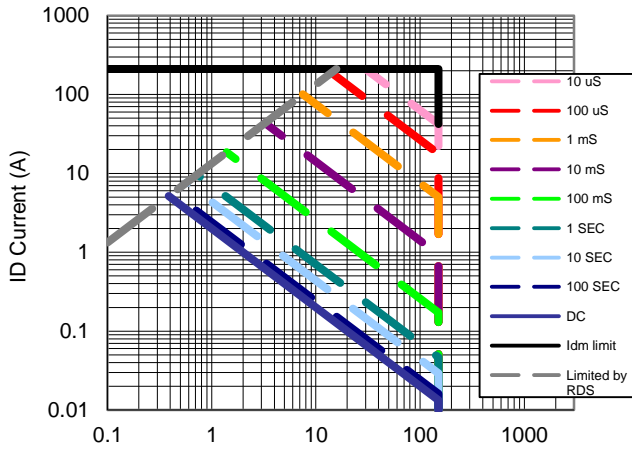
Typical Electrical Characteristics



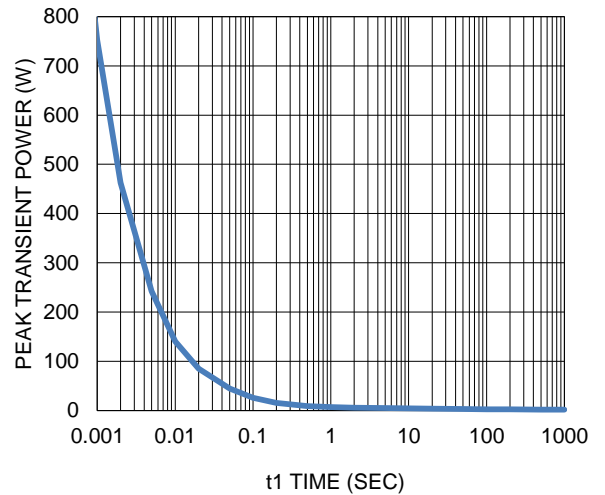
7. Gate Charge



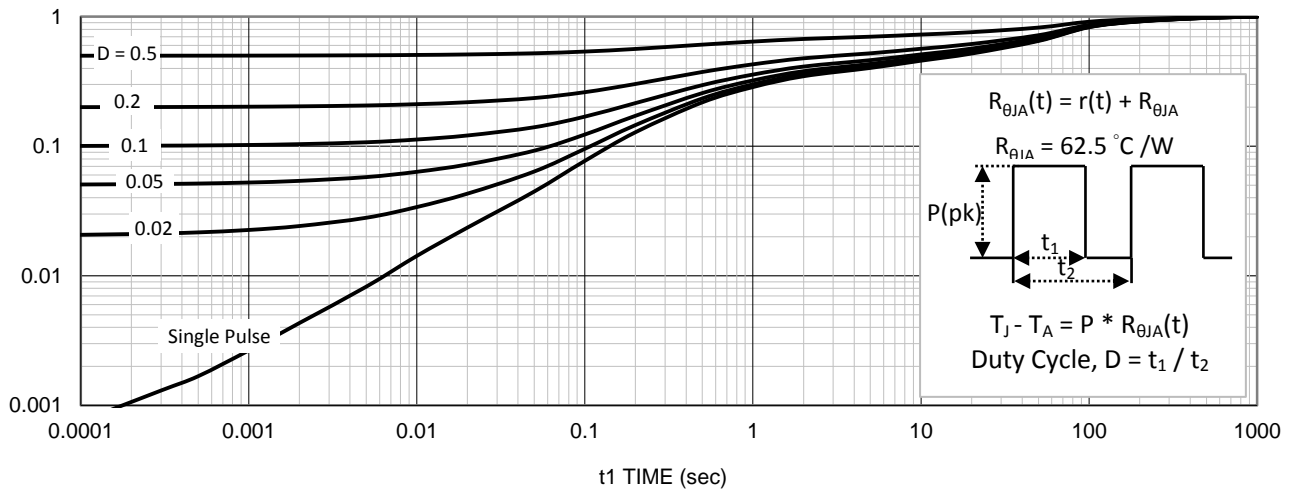
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area



10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information

