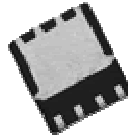


**N-Channel 40-V (D-S) MOSFET**

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low  $r_{DS(on)}$  and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

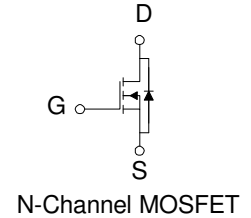
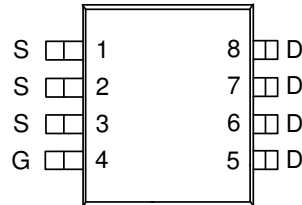
- Low  $r_{DS(on)}$  provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe DFN3x3-8PP saves board space
- Fast switching speed
- High performance trench technology



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

PRODUCT SUMMARY		
V <sub>DS</sub> (V)	r <sub>DS(on)</sub> m(Ω)	I <sub>D</sub> (A)
40	5.5 @ V <sub>GS</sub> = 10V	21
	7 @ V <sub>GS</sub> = 4.5V	19

DFN3x3-8PP  
Top View



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V <sub>DS</sub>	40	V
Gate-Source Voltage		V <sub>GS</sub>	±20	
Continuous Drain Current <sup>a</sup>	T <sub>A</sub> =25°C	I <sub>D</sub>	21	A
	T <sub>A</sub> =70°C		17	
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	±40	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	2.9	A
Power Dissipation <sup>a</sup>	T <sub>A</sub> =25°C	P <sub>D</sub>	3.5	W
	T <sub>A</sub> =70°C		2	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Maximum	Units
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 sec	R <sub>θJA</sub>	35	°C/W
	Steady State		81	°C/W

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

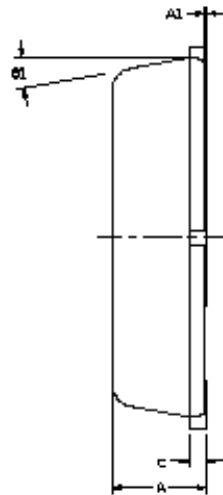
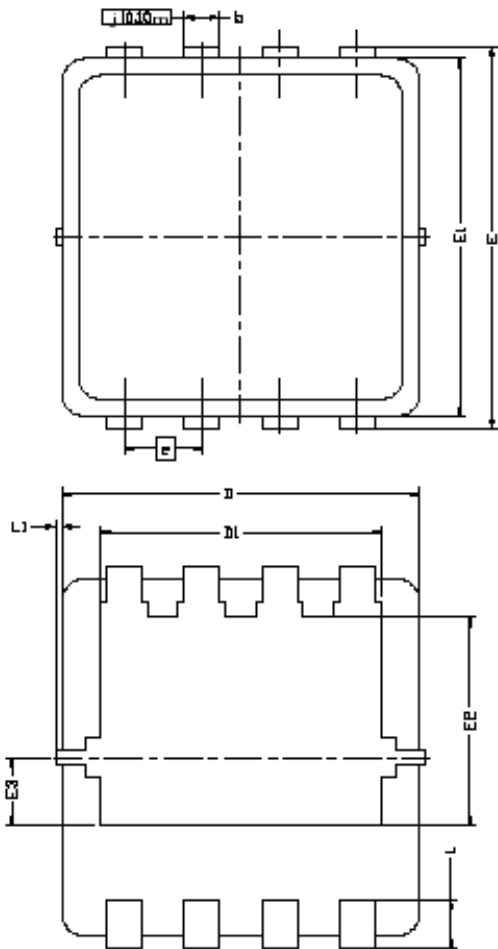
SPECIFICATIONS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Conditions	Limits			Unit
			Min	Typ	Max	
<b>Static</b>						
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 uA	1			V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 32 V, V <sub>GS</sub> = 0 V			1	uA
		V <sub>DS</sub> = 32 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55°C			25	
On-State Drain Current <sup>A</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	20			A
Drain-Source On-Resistance <sup>A</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1 A			5.5	mΩ
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1 A			7	
Forward Transconductance <sup>A</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 1 A		40		S
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = 1 A, V <sub>GS</sub> = 0 V		0.7		V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1 A		30		nC
Gate-Source Charge	Q <sub>gs</sub>			9		
Gate-Drain Charge	Q <sub>gd</sub>			10		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 10 V, R <sub>L</sub> = 6 Ω, I <sub>D</sub> = 1 A, V <sub>GEN</sub> = 10 V		9		nS
Rise Time	t <sub>r</sub>			10		
Turn-Off Delay Time	t <sub>d(off)</sub>			100		
Fall-Time	t <sub>f</sub>			30		

## Notes

- Pulse test: PW ≤ 300us duty cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.

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# Package Information



DIM.	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.700	0.80	0.900	0.0276	0.0315	0.0354
A1	0.00	---	0.05	0.000	---	0.002
b	0.24	0.30	0.35	0.009	0.012	0.014
c	0.10	0.152	0.25	0.004	0.006	0.010
D	3.00 BSC			0.118 BSC		
D1	2.35 BSC			0.093 BSC		
E	3.20 BSC			0.126 BSC		
E1	3.00 BSC			0.118 BSC		
E2	1.75 BSC			0.069 BSC		
E3	0.575 BSC			0.023 BSC		
e	0.65 BSC			0.026 BSC		
L	0.30	0.40	0.50	0.0118	0.0157	0.0197
L1	0	---	0.100	0	---	0.004
θ1	0°	10°	12°	0°	10°	12°