

### **DESCRIPTION**

Available in SOT-323 package.

### **FEATURES**

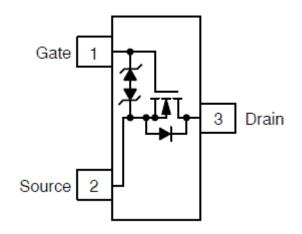
ESD Protected: 1000V

• Available in SOT-323 package

### **ORDERING INFORMATION**

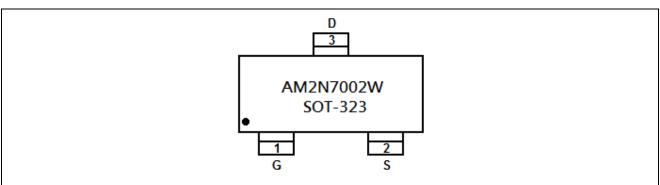
Package Type	Part Number		
SOT-323	Co	AM2N702WC3R	
(SC70-3)	C3	AM2N702WC3VR	
	V: Halogen free Package		
Note	R: Tape & Reel		
	SPQ: 3,000pcs/Reel		
AiT provides all RoHS products			
Suffix "V" means Halogen free Package			

### N CHANNEL MOSFET



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# PIN DESCRIPTION



Top View

Pin#	Symbol	Function
1	G	Gate
2	S	Source
3	D	Drain

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### **ABSOLUTE MAXIMUM RATINGS**

V <sub>DSS</sub> , Drain-Source Voltage		60Vdc
V <sub>DGR</sub> , Drain-Gate Voltage (R <sub>GS</sub> = 1.0MΩ)		60Vdc
Drain Current		
I <sub>D</sub> , Continuous	$T_C = 25^{\circ}C^{\text{NOTE1}}$	±115mAdc
	$T_C = 100^{\circ}C$ NOTE1	±75mAdc
I <sub>DM</sub> , Pulsed NOTE		±800mAdc
Gate-Source Voltage		
V <sub>GS</sub> , Continuous		±20Vdc
V <sub>GSM</sub> , Non-repetitive (tp ≤ 50µs)		±40Vpk

Stresses above may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the Electrical Characteristics are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

NOTE1: The Power Dissipation of the package may result in a lower continuous drain current.

NOTE2: Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2.0%.

### THERMAL CHARACTERISTICS

Parameter	Symbol	Max.	Unit
Total Device Dissipation FR-5 Board	P <sub>D</sub>	225	mW
NOTE3 $T_A = 25^{\circ}C$		1.8	mW/°C
Derate above 25°C			
Thermal Resistance, Junction to Ambient	R <sub>0JA</sub>	556	°C/W
Total Device Dissipation	P <sub>D</sub>	300	mW
Alumina Substrate, NOTE4 TA = 25°C		2.4	mW/°C
Derate above 25°C			
Thermal Resistance, Junction to Ambient	Reja	417	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

NOTE3: FR-5 =  $1.0 \times 0.75 \times 0.062$  in.

NOTE4: Alumina =  $0.4 \times 0.3 \times 0.025$  in 99.5% alumina.

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# **ELECTRICAL CHARACTERISTICS**

T<sub>A</sub> = 25°C, unless otherwise specified

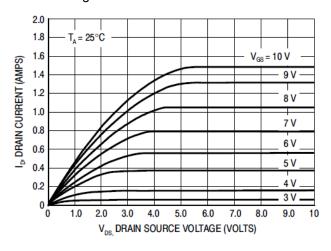
Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0$ , $I_D = 10 \mu Adc$		60	-	-	Vdc
Zana Oata Waltana Baile Oana t		V <sub>GS</sub> = 0,	T <sub>J</sub> = 25°C	-	-	1.0	A
Zero Gate Voltage Drain Current	IDSS	$I_{DSS}$ $V_{DS} = 60 \text{Vdc}$ $T_J = 125^{\circ}\text{C}$		-	-	500	μAdc
Gate-Body Leakage Current,	I <sub>GSSF</sub>	V <sub>GS</sub> = 20Vdc				1	μΛdc
Forward	IGSSF			-	-	1	μAdc
Gate–Body Leakage Current,	I <sub>GSSR</sub>	I <sub>GSSR</sub> V <sub>GS</sub> = -20Vdc		_	_	-1	μAdc
Reverse	10331	VG3 20 VG0				'	μπασ
ON CHARACTERISTICS NOTE2	T	T		T	Т		
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{DS} = V_{GS}$ , $I_D = 2$	250µAdc	1.0	1.6	2.5	Vdc
On-State Drain Current	I <sub>D(ON)</sub>	V <sub>DS</sub> ≥ 2.0V <sub>DS(ON</sub>	), V <sub>GS</sub> =10Vdc	500	-	-	mA
Static Drain–Source	V <sub>DS(ON)</sub>	V <sub>GS</sub> = 10Vdc, I <sub>D</sub>	= 500mAdc	-	-	3.75	Vdc
On-State Voltage	V DS(ON)	$V_{GS}$ = 5.0Vdc, I	$V_{GS}$ = 5.0Vdc, $I_D$ = 50mAdc		-	0.375	Vdc
		$V_{GS}$ = 10 $V$ ,	T <sub>C</sub> = 25°C	-	1.4	7.5	
Static Drain–Source	rpo(oN)	I <sub>D</sub> = 500mAdc	T <sub>C</sub> = 125°C	-	-	13.5	Ω
On–State Resistance	rds(on)	$V_{GS} = 5.0 Vdc$	T <sub>C</sub> = 25°C	-	1.8	7.5	12
		I <sub>D</sub> = 50mAdc	T <sub>C</sub> = 125°C	-	-	13.5	
Forward Transconductance	<b>g</b> FS	$V_{DS} \ge 2.0 V_{DS(ON)}, I_D = 200 \text{mAdc}$		80	-	-	mS
DYNAMIC CHARACTERISTICS				T	1		
Input Capacitance	Ciss	V <sub>DS</sub> = 25Vcd,		-	1.7	50	
Output Capacitance	Coss	V <sub>GS</sub> = 0,		-	10	25	pF
Reverse Transfer Capacitance	C <sub>RSS</sub>	f = 1.0MHz		-	2.5	5.0	
SWITCHING CHARACTERISTICS	NOTE7	T		Γ	Г		
Turn-On Delay Time	t <sub>d(ON)</sub>	$V_{DD}$ = 25Vdc , $I_D$ $\cong$ 500mAdc, $R_G$ = 25 $\Omega$ , $R_L$ = 50 $\Omega$ , $V_{GEN}$ = 10V		-	7	20	ne
Turn-Off Delay Time	t <sub>d(OFF)</sub>			-	11	40	ns
BODY-DRAIN DIODE RATINGS							
Diode Forward On-Voltage	V <sub>SD</sub>	I <sub>S</sub> = 115mAdc, V <sub>GS</sub> = 0V		-	-	-1.5	Vdc
Source Current Continuous	ls	Body Diode		-	-	-115	mAdc
Source Current Pulsed	I <sub>SM</sub>			-	-	-800	mAdc

NOTE2: Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2.0%.

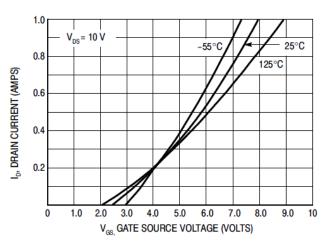
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### TYPICAL PERFORMANCE CHARACTERISTICS

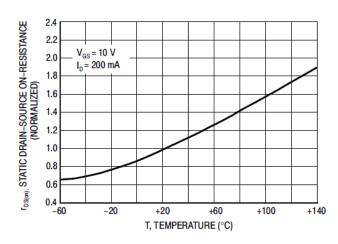
### 1. Ohmic Region



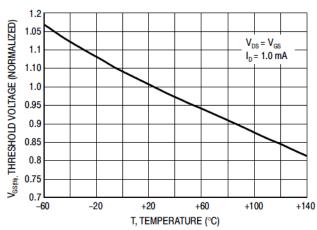
#### 2. Transfer Characteristics



#### 3. Temperature vs. Static Drain-Source On-Resistance



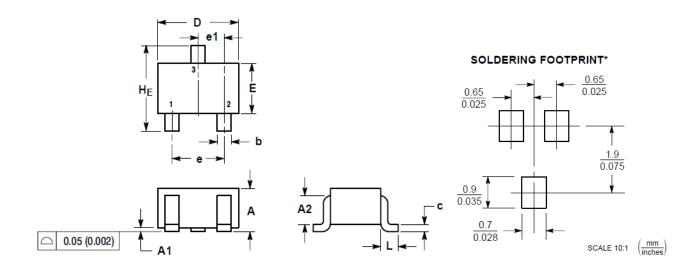
### 4. Temperature vs. Gate Threshold Voltage



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# **PACKAGE INFORMATION**

Dimension in SOT-323(SC-70) Package (Unit: mm)



SYMBOL	MIN	MAX		
Α	0.800	1.000		
A1	0.000	0.100		
A2	0.700 REF			
b	0.300	0.400		
С	0.100	0.250		
D	1.800	2.200		
E	1.150	1.350		
е	1.200	1.400		
e1	0.650 BSC			
L	0.425 REF			
HE	2.000	2.400		

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