

DESCRIPTION

The AM8882 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 1.8V. The device is suitable for use as a load switch or in PWM applications. It is ESD protested.

AM8882 is available in a DFN8 (2 x 3) package.

ORDERING INFORMATION

Package Type	Part Number			
DFN8	J8	AM8882J8R		
		AM8882J8VR		
Note	V: Halogen free Package			
Note	R: Tape & Reel			
AiT provides all RoHS products				
Suffix " V " means Halogen free Package				

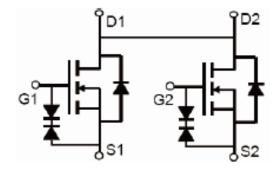
FEATURES

- V_{DS}= 20V, I_D= 8A
 - Typ.R_{DS(ON)} = $9.5m\Omega$ @ V_{GS}= 4.5V
 - Typ.R_{DS(ON)} = $13m\Omega$ @ V_{GS}= 2.5V
 - ESD Rating: 2000V HBM
- High Power and current handing capability
- Surface Mount Package
- Available in a DFN8 (2 x 3) package.

APPLICATION

- Uni-directional load switch
- Bi-directional load switch

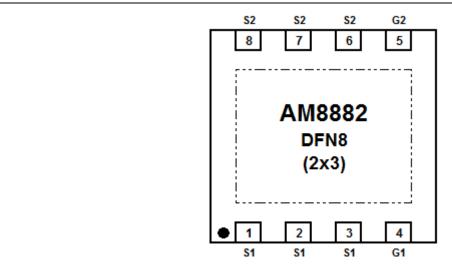
PIN DESCRIPTION



Schematic diagram

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PIN DESCRIPTION



Top View

Pin#	Symbol	Function
1	S1	Source
2	S1	Source
3	S1	Source
4	G1	Gate
5	G2	Gate
6	S2	Source
7	S2	Source
8	S2	Source

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ABSOLUTE MAXIMUM RATINGS

T_A = 25°C, unless otherwise noted

V _{DS} , Drain-Source Voltage	20V
V _{GS} , Gate-Source Voltage	±12V
I _D , Drain Current-Continuous	8A
I _{DM} , Drain Current-Pulsed NOTE1	30A
P _D , Maximum Power Dissipation	2W
T _J ,T _{STG} , Operating Junction and Storage Temperature Range	-55°C~150°C

Stress beyond above listed "Absolute Maximum Ratings" may lead permanent damage to the device. These are stress ratings only and operations of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

THERMAL CHARACTERISTICS

Parameter	Symbol	Limit	Units
Thermal Resistance, Junction-to-Ambient NOTE2	R _{θJA}	62.5	°C/W

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ELECTRICAL CHARACTERISTICS

 $T_A = 25^{\circ}C$, unless otherwise noted

Parameter	Symbol	Conditions	Min	Тур.	Max	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =250μA	20	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V,V _{GS} =0V	-	ı	1	μA
Gate-Body Leakage Current	Igss	V _{GS} =±10V, V _{DS} =0V	-	ı	±10	μA
On Characteristics NOTE3						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{DS}=250\mu A$	0.45	0.7	1.0	V
<u> </u>	D	V _{GS} =4.5V,I _D =5A	-	9.5	13	mΩ
Drain-Source On-state Resistance	R _{DS(ON)}	V _{GS} =2.5V,I _D =4A	-	13	17	
Forward Transconductance	g FS	V _{DS} =5V,I _D =8A	-	15	-	S
Dynamic Characteristics NOTE3			•			
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V,	-	1800	-	pF
Output Capacitance	Coss		-	230	-	
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	200	-	
Switching Characteristics NOTE3						
Turn-on Delay Time	$t_{D(ON)}$		-	2.5	-	
Turn-on Rise Time	t R	V_{DD} =10V, R_L =1.2 Ω ,	-	7.2	-	-0
Turn-off Delay Time	t _{D(OFF)}	V_{GS} =10V, R_{GEN} =3 Ω	-	49	-	nS
Turn-off Fall Time	t₅	1	-	10.8	-	
Total Gate Charge	Q_{G}	V _{DS} =10V, I _D =8A,	-	17.9	-	
Gate-Source Charge	Q_GS		-	1.5	-	nC
Gate-Drain Charge	Q_{GD}	V _{GS} =4.5V	-	4.7	-	
Drain-Source Diode Characteristics	;					
Diode Forward Voltage NOTE3	V_{SD}	V _{GS} =0V, I _S =1A	_	-	1.2	V
Diode Forward Current NOTE2	I_S		-	-	7	Α

NOTE1: Repetitive Rating: Pulse width limited by maximum junction temperature

NOTE2: Surface Mounted on FR4 Board, $t \le 10$ sec.

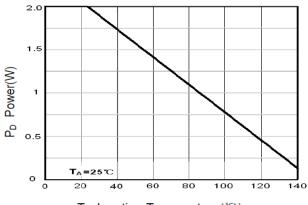
NOTE3: Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.

NOTE4: Guaranteed by design, not subject to production

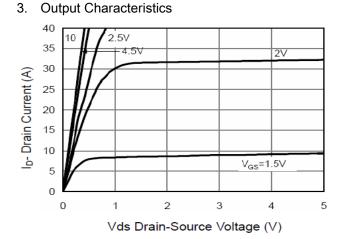
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TYPICAL CHARACTERISTICS

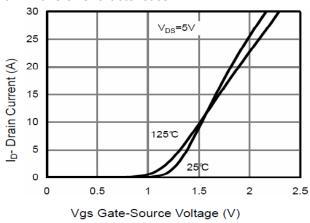
1. Power Dissipation



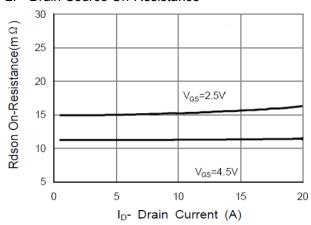
 $\mathsf{T}_{\mathsf{J}}\text{-Junction Temperature}({}^{\circ}\!\mathbb{C}\,)$



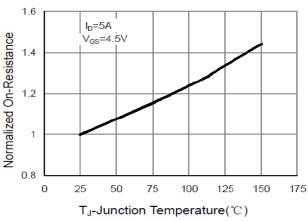
5. Transfer Characteristics



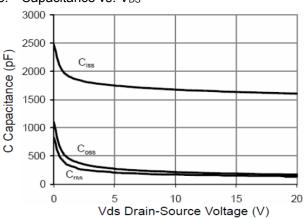
2. Drain-Source On-Resistance



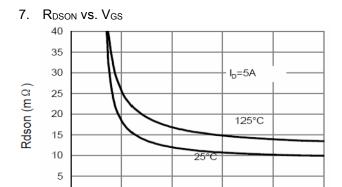
4. Drain-Source On-Resistance



6. Capacitance vs. V_{DS}



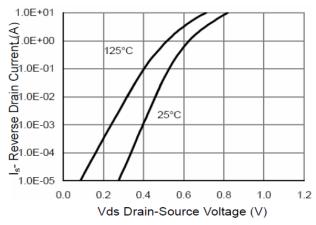
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Vgs Gate-Source Voltage (V)

6

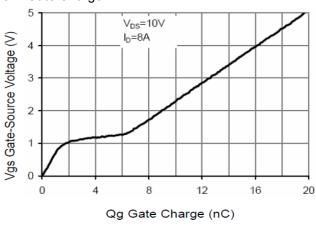
8. Source-Drain Diode Forward



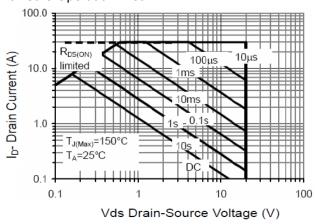
9. Gate Charge

2

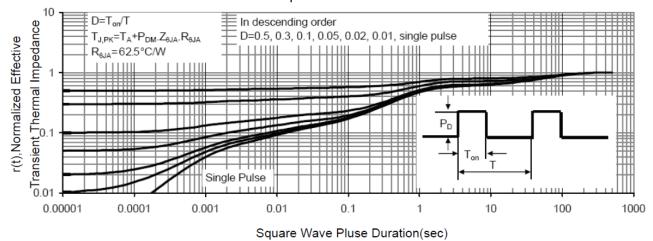
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10. Safe Operation Area



11. Normalized Maximum Transient Thermal Impedance



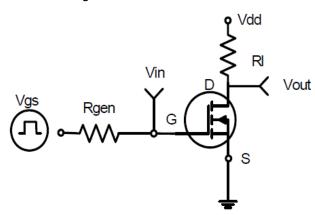
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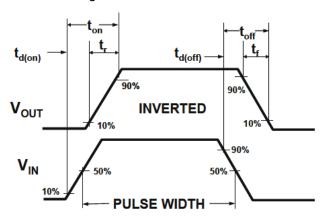


DETAILED INFORMATION

1. Switching Test Circuit



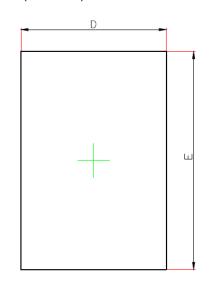
2. Switching Waveforms

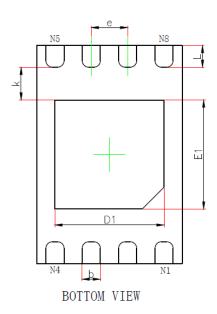


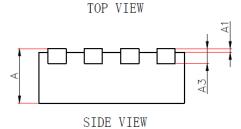
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PACKAGE INFORMATION

Dimension in DFN8 (Unit: mm)







Symbol	Min	Max		
А	0.700/0.800	0.800/0.900		
A1	0.000	0.050		
A3	0.203(REF)			
D	1.924	2.076		
Е	2.924	3.076		
D1	1.400	1.600		
E1	1.400	1.600		
k	0.200(MIN)			
b	0.200	0.300		
е	0.500 (TYP.			
L	0.224	0.376		

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