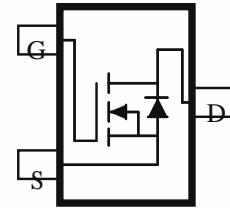
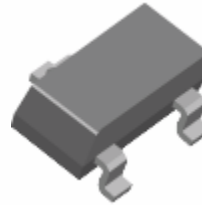


### N-Channel 30-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low  $r_{DS(on)}$  and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low  $r_{DS(on)}$  provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOT-23 saves board space
- Fast switching speed
- High performance trench technology

PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
30	0.085 @ $V_{GS} = 10V$	2.5
	0.125 @ $V_{GS} = 4.5V$	1.7



ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$ UNLESS OTHERWISE NOTED)			
Parameter	Symbol	Maximum	Units
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current <sup>a</sup>	$T_A=25^\circ C$	2.5	A
	$T_A=70^\circ C$	2	
Pulsed Drain Current <sup>b</sup>	$I_{DM}$	10	
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	0.46	A
Power Dissipation <sup>a</sup>	$T_A=25^\circ C$	1.25	W
	$T_A=70^\circ C$	0.8	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150	$^\circ C$

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 5$ sec	150	$^\circ C/W$
	Steady-State	200	

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

SPECIFICATIONS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Conditions	Limits			Unit
			Min	Typ	Max	
<b>Static</b>						
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1.0	1.5	3	V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = 8 V		4	100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 16 V, V <sub>GS</sub> = 0 V		7	1	μA
		V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55°C			10	
On-State Drain Current <sup>A</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 4.5 V	6			A
Drain-Source On-Resistance <sup>A</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.5 A		62	85	mΩ
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1.7 A		102	125	
Forward Transconductance <sup>A</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 3.0 A		3.5		S
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = 0.46 A, V <sub>GS</sub> = 0 V		0.65		V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 2.5 A		3.5	7	nC
Gate-Source Charge	Q <sub>gs</sub>			0.8	2	
Gate-Drain Charge	Q <sub>gd</sub>			1.0	2	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1MHz		720	1500	pF
Output Capacitance	C <sub>oss</sub>			165	400	
Reverse Transfer Capacitance	C <sub>rss</sub>			60	200	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 10 V, I <sub>D</sub> = 1 A, R <sub>G</sub> = 6 Ω, V <sub>GEN</sub> = 4.5 V		10	20	ns
Rise Time	t <sub>r</sub>			13	30	
Turn-Off Delay Time	t <sub>d(off)</sub>			14	30	
Fall-Time	t <sub>f</sub>			4	20	

## Notes

- Pulse test: PW ≤ 300μs duty cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.

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## Typical Electrical Characteristics

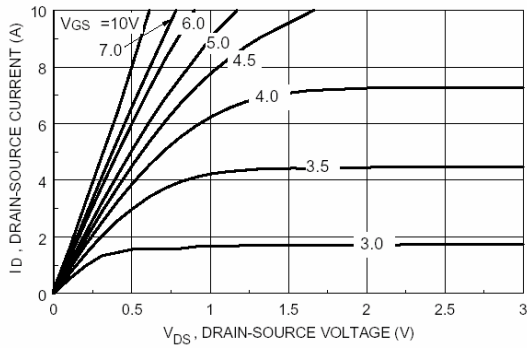


Figure 1. On-Region Characteristics

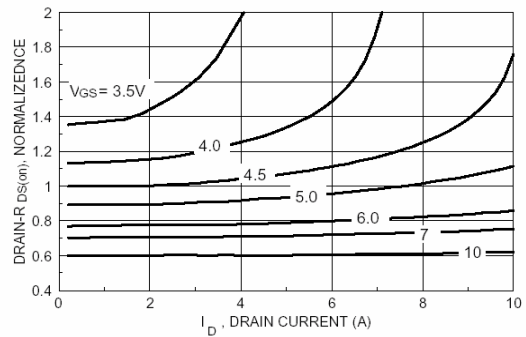


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage

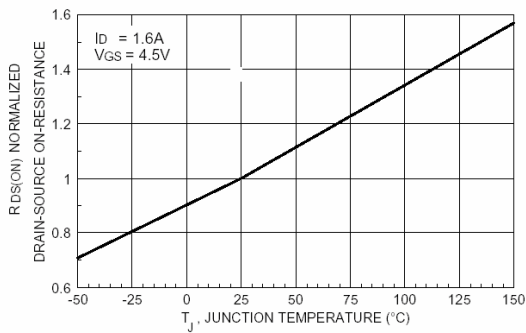


Figure 3. On-Resistance Variation with Temperature

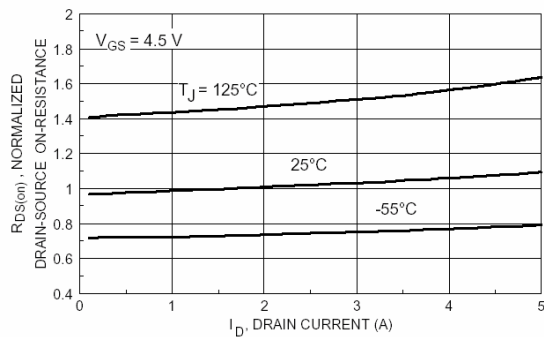


Figure 4. On-Resistance Variation with Drain Current and Temperature

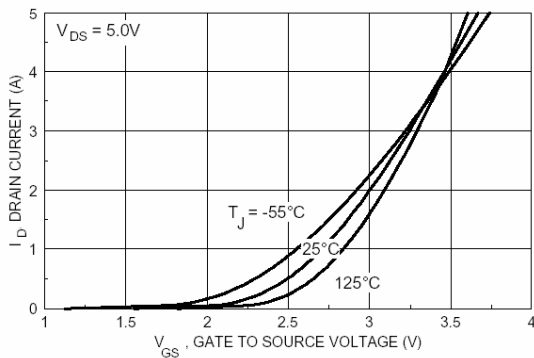


Figure 5. Transfer Characteristics

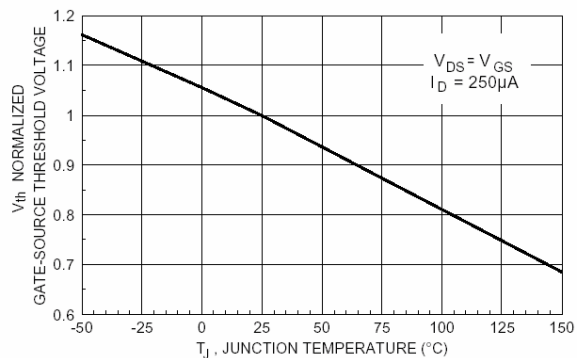


Figure 6. Gate Threshold Variation with Temperature

## Typical Electrical Characteristics

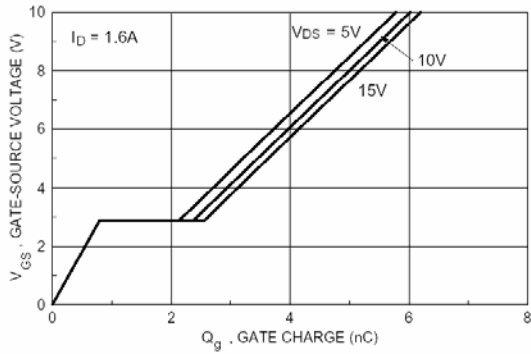


Figure 7. Gate Charge Characteristic

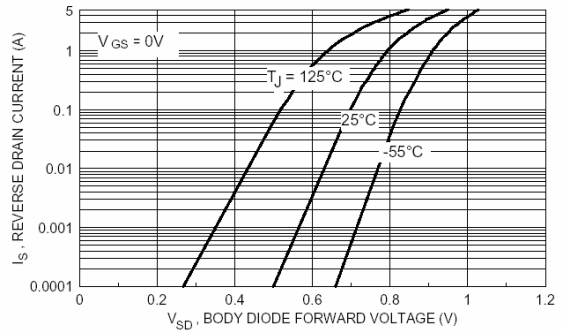


Figure 8. Capacitance Characteristic

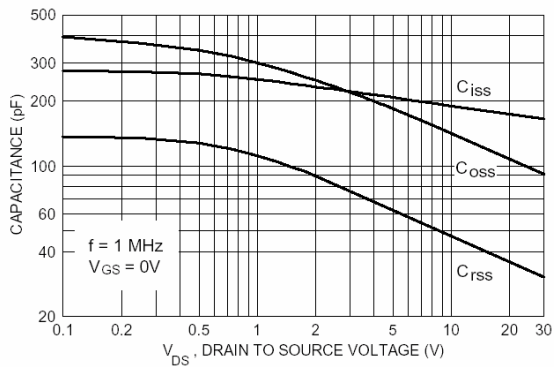


Figure 9. Maximum Safe Operating Area

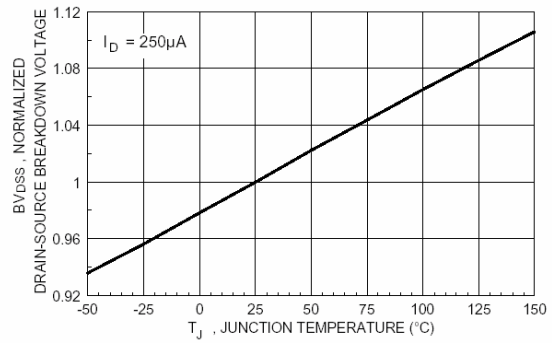


Figure 10. Breakdown Voltage Variation with Temperature

### Normalized Thermal Transient Impedance, Junction to Ambient

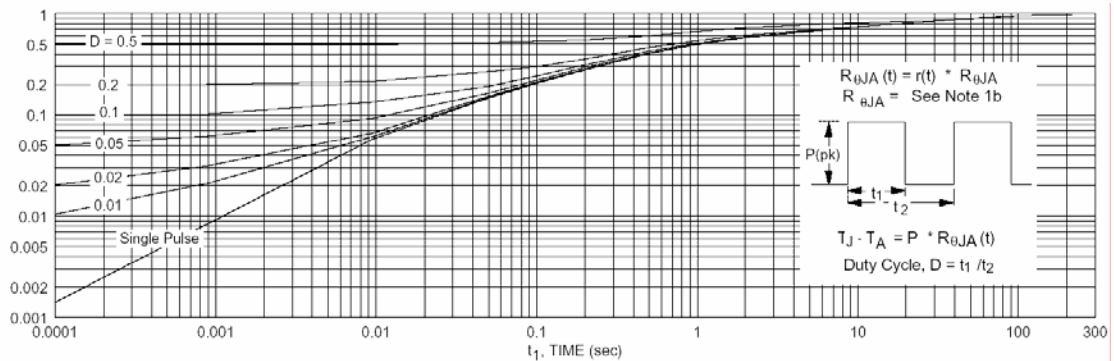
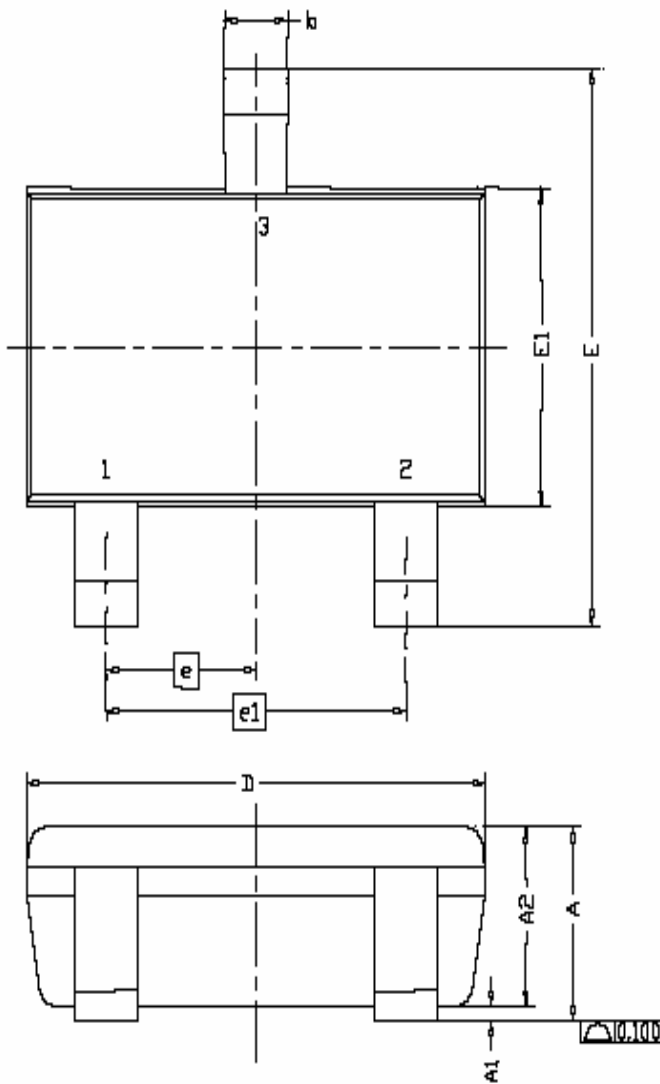


Figure 11. Transient Thermal Response Curve.

# Package Information



DIM.	MILLIMETERS		
	MIN	NOM	MAX
A	0.935	0.95	1.10
A1	0.01	---	0.10
A2	0.85	0.90	0.925
b	0.30	0.40	0.50
c	0.10	0.15	0.25
D	2.70	2.90	3.10
E	2.60	2.80	3.00
E1	1.40	1.60	1.80
e	0.95 BSC		
e1	1.90 BSC		
L	0.30	0.40	0.60
L1	0.60REF		
L2	0.25BSC		
R	0.10	---	---
θ	0°	4°	8°
θ1	7°NOM		

