

### Description

The G17 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

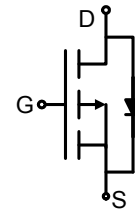
### General Features

$V_{DSS}$	$R_{DS(ON)}$ @4.5V(typ)	$I_D$
-18V	20m $\Omega$	-7A

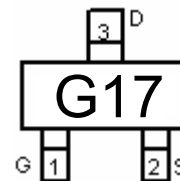
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

### Application

- PWM applications
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



SOT-23-3L top view

### Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-15	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current -Continuous	$I_D$	-7	A
Drain Current -Pulsed <sup>(Note 1)</sup>	$I_{DM}$	-20	A
Maximum Power Dissipation	$P_D$	2	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C

### Thermal Characteristic

Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	69	°C/W
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### Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-15	-18	-	V

Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-20V, V_{GS}=0V$	-	-	-1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>On Characteristics</b> (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.5	-0.65	-1.0	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=-4.5V, I_D=-6A$	-	20	26	m $\Omega$
		$V_{GS}=-2.5V, I_D=-5A$	-	28	40	
Forward Transconductance	$g_{FS}$	$V_{DS}=-5V, I_D=-6A$		17	-	S
<b>Dynamic Characteristics</b> (Note4)						
Input Capacitance	$C_{iss}$	$V_{DS}=-9V, V_{GS}=0V,$ $F=1.0MHz$	-	1200	-	PF
Output Capacitance	$C_{oss}$		-	390	-	PF
Reverse Transfer Capacitance	$C_{rss}$		-	300	-	PF
<b>Switching Characteristics</b> (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-6V, I_D=-1A,$ $R_L=6\Omega, V_{GEN}=-4.5V, R_g=6\Omega$	-	25	-	nS
Turn-on Rise Time	$t_r$		-	45	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	72	-	nS
Turn-Off Fall Time	$t_f$		-	60	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=-6V, I_D=-6A, V_{GS}=-4.5V$	-	15	-	nC
Gate-Source Charge	$Q_{gs}$		-	2	-	nC
Gate-Drain Charge	$Q_{gd}$		-	3.8	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	$V_{SD}$	$V_{GS}=0V, I_S=-1.0A$	-	-	-1.2	V
Diode Forward Current (Note 2)	$I_S$		-	-	7	A

### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics

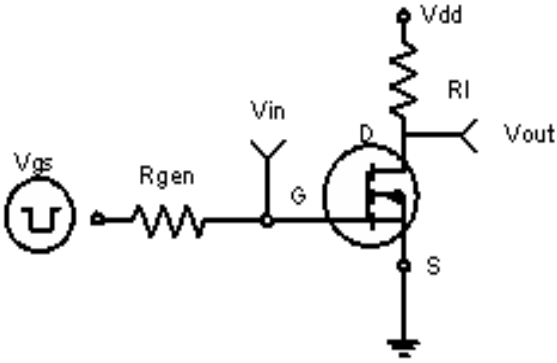


Figure 1: Switching Test Circuit

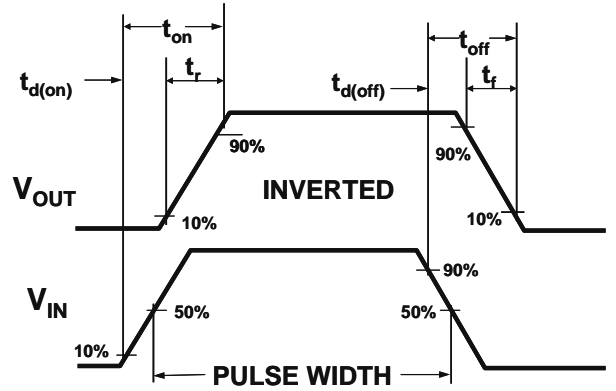


Figure 2: Switching Waveforms

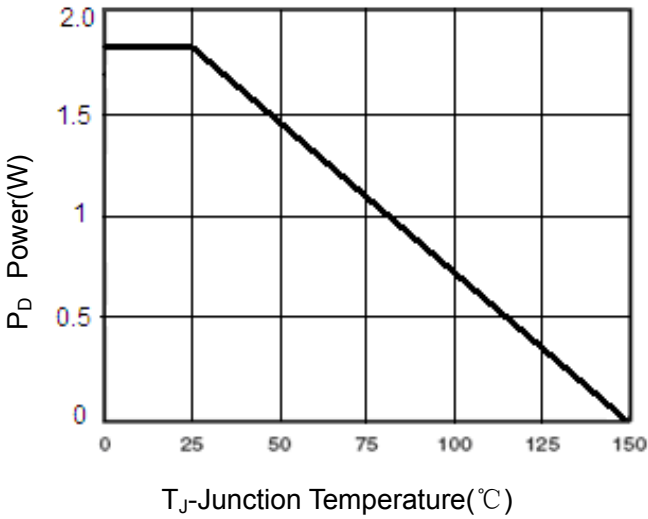


Figure 3 Power Dissipation

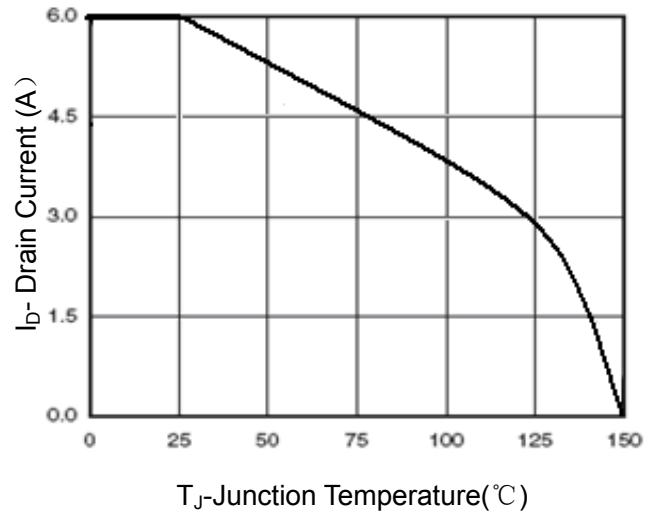


Figure 4 Drain Current

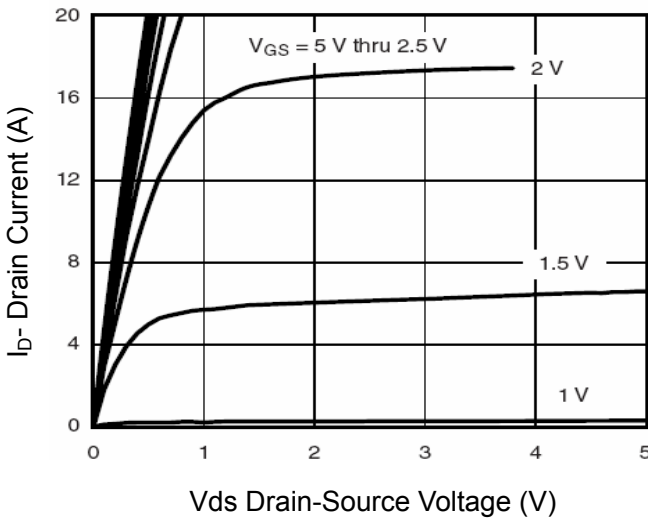


Figure 5 Output Characteristics

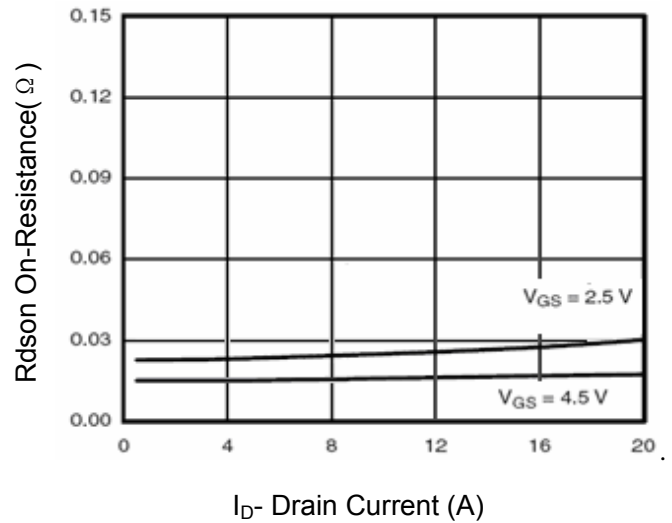


Figure 6 Drain-Source On-Resistance

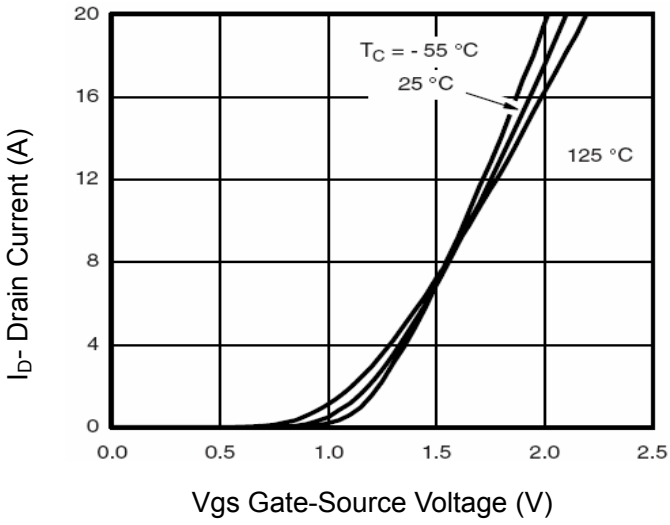


Figure 7 Transfer Characteristics

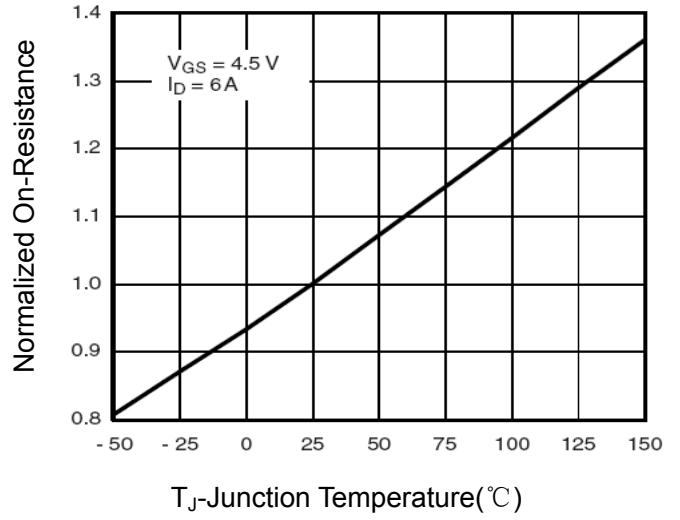


Figure 8 Drain-Source On-Resistance

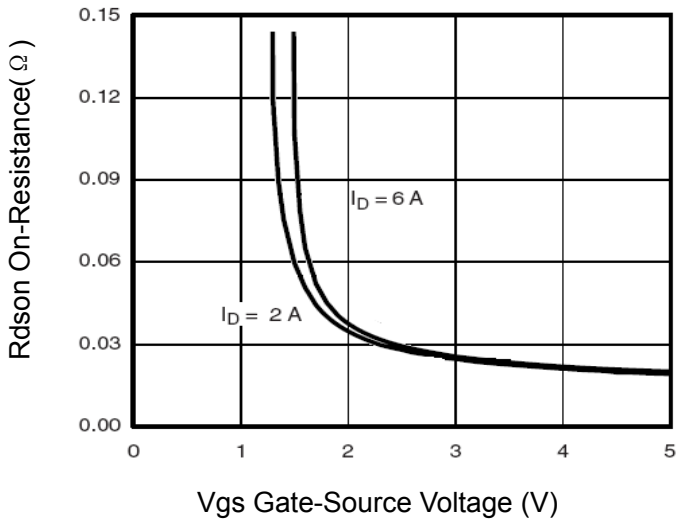


Figure 9  $R_{DS(on)}$  vs  $V_{GS}$

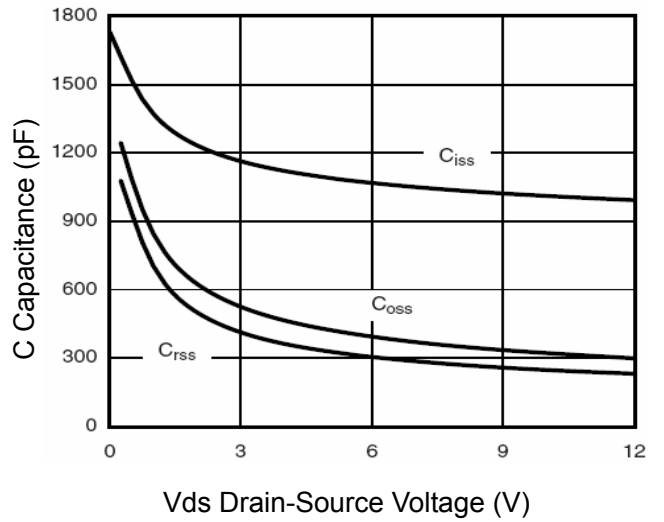


Figure 10 Capacitance vs  $V_{DS}$

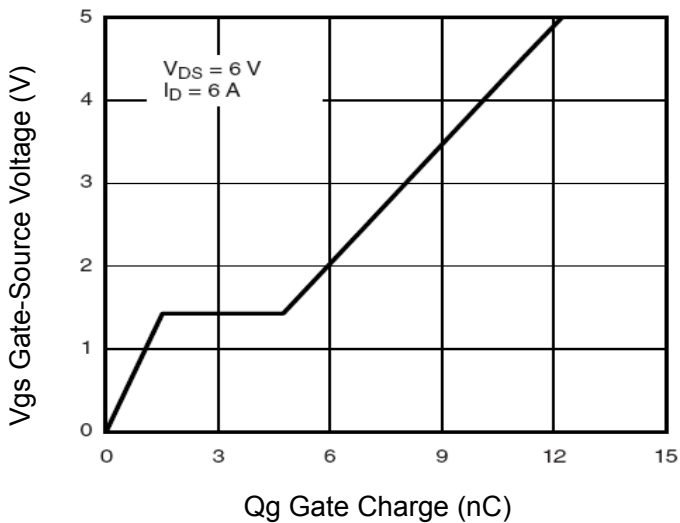


Figure 11 Gate Charge

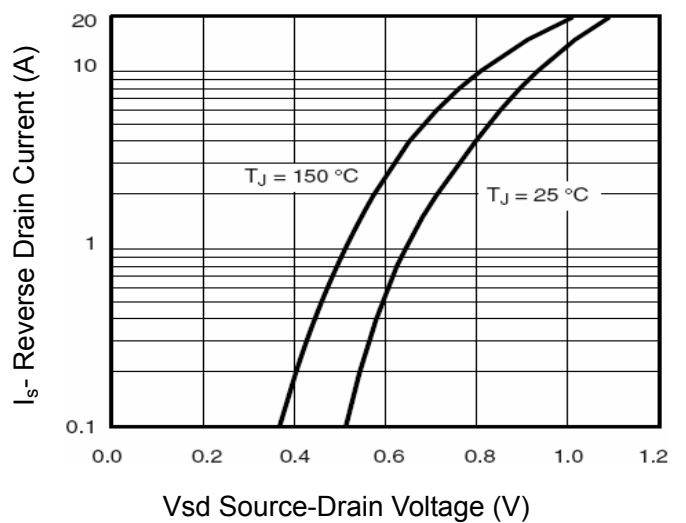


Figure 12 Source- Drain Diode Forward

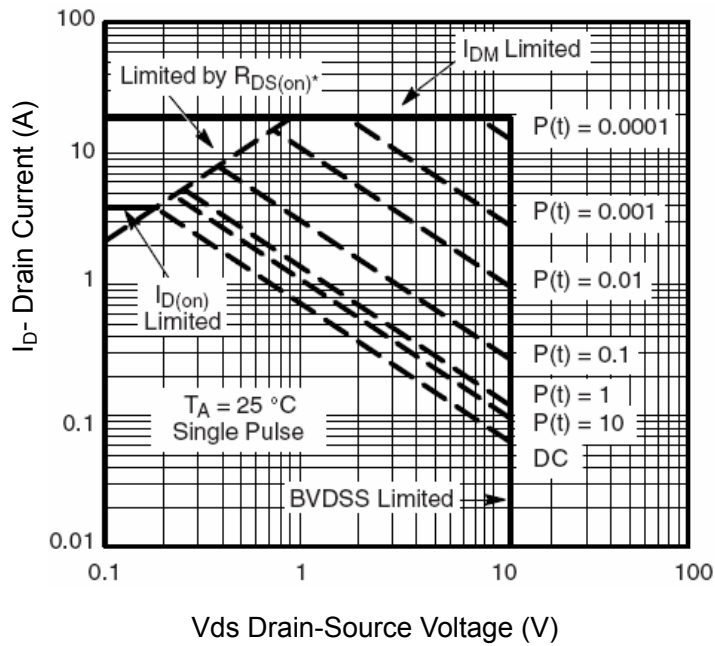


Figure 13 Safe Operation Area

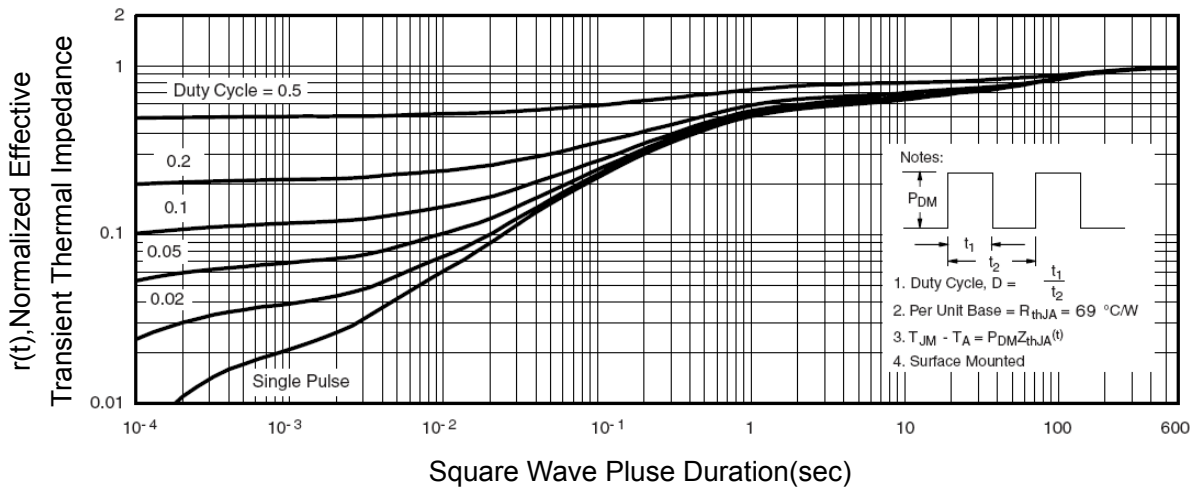


Figure 14 Normalized Maximum Transient Thermal Impedance