DA4J104K

Silicon epitaxial planar type

For high speed switching circuits

■ Features

- ullet Small reverse current I_R
- Low terminal capacitance C_t
- Contributes to miniaturization of sets, reduction of component count.
- Eco-friendly Halogen-free package

■ Basic Part Number

Double DA2J104 (Parallel)

Packaging

DA4J104K0R Embossed type (Thermo-compression sealing): 3000 pcs / reel (standard)

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Reverse voltage	V _R	80	V	
Maximum peak reverse voltage	V _{RM}	80	V	
Forward current *1	I_{F}	200	mA	
Peak forward current	I_{FM}	600	mA	
Non-repetitive peak forward surge current *2	I _{FSM}	1	A	
Junction temperature	Tj	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	

Note) *1: $I_F = 200$ mA achieved with a printed circuit board.

*2: 1 t = 1 s

■ Package

Code

SMini4-F3-B

Package dimension clicks here.→

• Pin Name

1: Anode-1 3: Cathode-2 2: Anode-2 4: Cathode-1

■ Marking Symbol: C1

■ Internal Connection

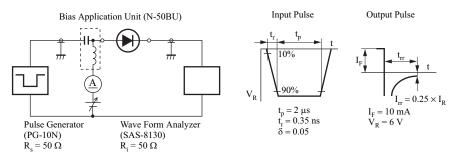


■ Electrical Characteristics $T_a = 25$ °C±3°C

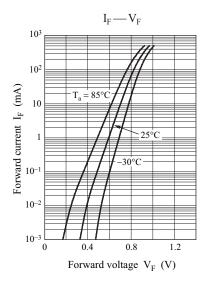
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Forward voltage	V _F	$I_F = 200 \text{ mA}$		0.90	1.10	V
Reverse voltage	V _R	$I_R = 100 \mu A$	80			V
Reverse current	I_R	$V_R = 80 \text{ V}$			500	nA
Terminal capacitance	C _t	$V_R = 0 \text{ V, } f = 1 \text{ MHz}$			4	pF
Reverse recovery time *	t _{rr}	$I_F = 10 \text{ mA}, V_R = 6 \text{ V}, I_{rr} = 0.25 \times I_R$			10	ns

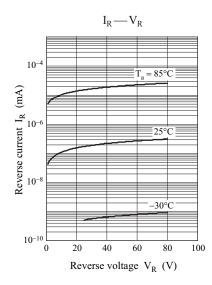
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

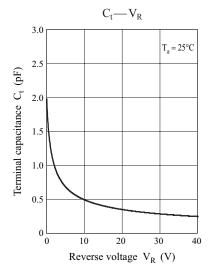
- 2. Absolute frequency of input and output is 100 MHz
- 3. *: t_{rr} measurement circuit



DA4J104K Panasonic







2 Ver. EED

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