



CHENMKO ENTERPRISE CO.,LTD

SURFACE MOUNT

Dual Enhancement Mode Field Effect Transistor

N-channel: VOLTAGE 100 Volts CURRENT 2.6 Ampere

P-channel: VOLTAGE 100 Volts CURRENT 2.0 Ampere

CHM7350JGP

Halogens free devices

APPLICATION

- * Servo motor control.
- * Power MOSFET gate drivers.
- * Other switching applications.

FEATURE

- * Small flat package. (SO-8)
- * Super high dense cell design for extremely low $R_{DS(ON)}$.
- * Lead free product is acquired.
- * High power and current handling capability.

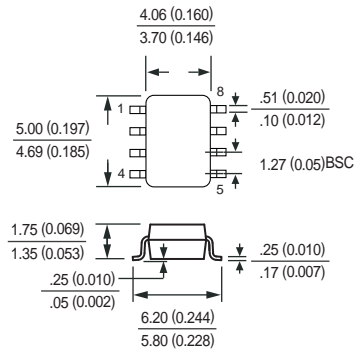
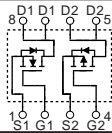
CONSTRUCTION

- * N-Channel & P-Channel Enhancement in the package



SO-8

CIRCUIT



Dimensions in millimeters

SO-8

Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	N-Channel	P-Channel	Units
V_{DSS}	Drain-Source Voltage	100	-100	V
V_{GSS}	Gate-Source Voltage	± 20	± 20	V
I_D	Maximum Drain Current - Continuous	2.6	-2.0	A
	- Pulsed (Note 3)	10	-8.0	
P_D	Maximum Power Dissipation	2000		mW
T_J	Operating Temperature Range	-55 to 150		$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150		$^\circ\text{C}$

- Note : 1. Surface Mounted on FR4 Board , $t \leq 10\text{sec}$
 2. Pulse Test , Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$
 3. Repetitive Rating , Pulse width limited by maximum junction temperature
 4. Guaranteed by design , not subject to production testing

Thermal characteristics

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 1)	62.5	$^\circ\text{C/W}$
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ELECTRICAL CHARACTERISTIC (CHM7350JGP)

N-Channel Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	100			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}$			1	μA
I_{GSSF}	Gate-Body Leakage	$V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$			+100	nA
I_{GSSR}	Gate-Body Leakage	$V_{GS} = -20\text{ V}, V_{DS} = 0\text{ V}$			-100	nA

ON CHARACTERISTICS (Note 2)

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	2		4	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 2.1\text{ A}$		150	190	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS} = 10\text{ V}, I_D = 4.5\text{ A}$		8		S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$		316		pF
C_{oss}	Output Capacitance			93		
C_{rss}	Reverse Transfer Capacitance			37		

SWITCHING CHARACTERISTICS (Note 4)

Q_g	Total Gate Charge	$V_{DS} = 80\text{ V}, I_D = 2.1\text{ A}$ $V_{GS} = 10\text{ V}$		12.4	16	nC
Q_{gs}	Gate-Source Charge			2		
Q_{gd}	Gate-Drain Charge			5.4		
t_{on}	Turn-On Time	$V_{DD} = 50\text{ V}$ $I_D = 1.0\text{ A}, V_{GS} = 10\text{ V}$ $R_{GEN} = 22\ \Omega$		12	25	nS
t_r	Rise Time			10	20	
t_{off}	Turn-Off Time			30	55	
t_f	Fall Time			18	35	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I_S	Drain-Source Diode Forward Current	(Note 1)			1.5	A
V_{SD}	Drain-Source Diode Forward Voltage	$I_S = 1.8\text{ A}, V_{GS} = 0\text{ V}$ (Note 2)			1.3	V

ELECTRICAL CHARACTERISTIC (CHM7350JGP)

P-Channel Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = -250\ \mu\text{A}$	-100			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -100\text{ V}, V_{GS} = 0\text{ V}$			-1	μA
I_{GSSF}	Gate-Body Leakage	$V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$			+100	nA
I_{GSSR}	Gate-Body Leakage	$V_{GS} = -20\text{ V}, V_{DS} = 0\text{ V}$			-100	nA

ON CHARACTERISTICS (Note 2)

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\ \mu\text{A}$	-1		-3	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = -10\text{ V}, I_D = -1.5\text{ A}$		250	320	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS} = -5\text{ V}, I_D = -1.5\text{ A}$		7		S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = -25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$		576		pF
C_{oss}	Output Capacitance			120		
C_{rss}	Reverse Transfer Capacitance			32		

SWITCHING CHARACTERISTICS (Note 4)

Q_g	Total Gate Charge	$V_{DS} = -80\text{ V}, I_D = -1.5\text{ A}$ $V_{GS} = -10\text{ V}$		16	21	nC
Q_{gs}	Gate-Source Charge			3.5		
Q_{gd}	Gate-Drain Charge			5.0		
t_{on}	Turn-On Time	$V_{DD} = -50\text{ V}$ $I_D = -1\text{ A}, V_{GS} = -10\text{ V}$ $R_{GEN} = 22\ \Omega$		14	30	nS
t_r	Rise Time			8.0	20	
t_{off}	Turn-Off Time			60	120	
t_f	Fall Time			20	40	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I_S	Drain-Source Diode Forward Current	(Note 1)			-1.4	A
V_{SD}	Drain-Source Diode Forward Voltage	$I_S = -1.4\text{ A}, V_{GS} = 0\text{ V}$ (Note 2)			-1.6	V