

Halogens free devices



**CHENMKO ENTERPRISE CO.,LTD**

**SURFACE MOUNT**

**Dual N-Channel Enhancement Mode Field Effect Transistor**

VOLTAGE 40 Volts CURRENT 0.3 Ampere

**CHM3545SGP**

**APPLICATION**

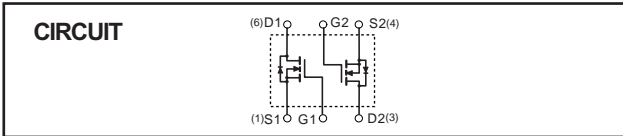
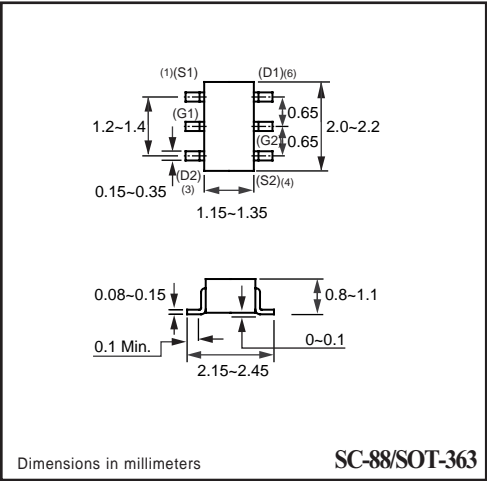
- \* Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- \* High saturation current capability. Direct Logic-Level Interface: TTL/CMOS
- \* Battery Operated Systems

**FEATURE**

- \* Small surface mounting type. (SC-88/SOT-363)
- \* High density cell design for low  $R_{DS(ON)}$  .

**CONSTRUCTION**

- \* Dual N-Channel Enhancement



**Absolute Maximum Ratings**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	CHM3545SGP	Units
$V_{DSS}$	Drain-Source Voltage	40	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Maximum Drain Current - Continuous	0.3	A
	- Pulsed	1.0	
$P_D$	Maximum Power Dissipation	350	mW
$T_J$	Operating Temperature Range	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$

**Thermal characteristics**

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	375	$^\circ\text{C/W}$
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## ELECTRICAL CHARACTERISTIC ( CHM3545SGP )

**Electrical Characteristics**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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### OFF CHARACTERISTICS

$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 150\ \mu\text{A}$	40			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 40\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
$I_{GSSF}$	Gate-Body Leakage	$V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$			+100	nA
$I_{GSSR}$	Gate-Body Leakage	$V_{GS} = -20\text{ V}, V_{DS} = 0\text{ V}$			-100	nA

### ON CHARACTERISTICS

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	1.0		1.3	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS}=5\text{V}, I_D=0.2\text{A}$		3.2	5	$\Omega$
		$V_{GS}=2.5\text{V}, I_D=0.02\text{A}$		7.5	10	

### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$		43		pF
$C_{oss}$	Output Capacitance			20		
$C_{rss}$	Reverse Transfer Capacitance			6		

### SWITCHING CHARACTERISTICS

$Q_g$	Total Gate Charge	$V_{DS}=30\text{V}, I_D=1\text{A}$ $V_{GS}=5\text{V}$		1.4	2.0	nC
$Q_{gs}$	Gate-Source Charge			0.8		
$Q_{gd}$	Gate-Drain Charge			0.5		
$t_{on}$	Turn-On Time	$V_{DD}= 30\text{V}$ $I_D = 0.5\text{ A} \cdot V_{GEN} = 4.5\text{ V}$ $R_G = 4.7\ \Omega$		5		nS
$t_r$	Rise Time			15		
$t_{off}$	Turn-Off Time			7		
$t_f$	Fall Time			8		

### DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

$I_S$	Drain-Source Diode Forward Current			0.12	A
$V_{SD}$	Drain-Source Diode Forward Voltage	$I_S = 0.12\text{ A}, V_{GS} = 0\text{ V}$		1.5	V