



**CHENMKO ENTERPRISE CO.,LTD**

**CHM1024VGP**

**SURFACE MOUNT**

**N-Channel Enhancement Mode Field Effect Transistor**

VOLTAGE 20 Volts CURRENT 700 mAmpere

Halogens free devices

**APPLICATION**

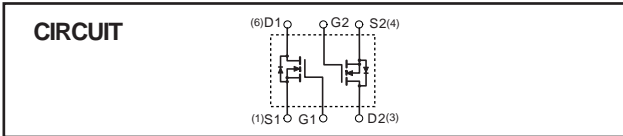
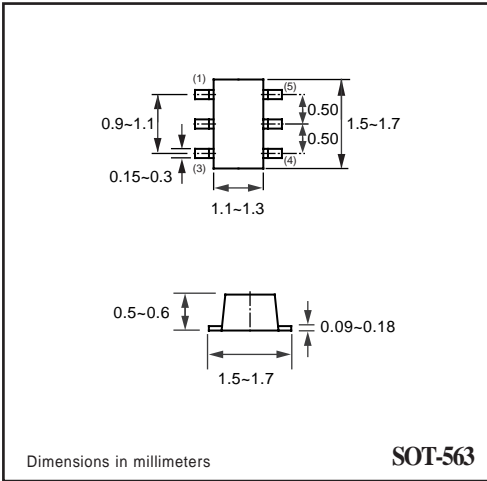
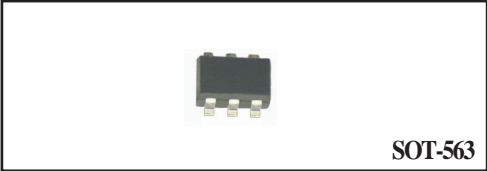
- \* Power Management in Note book
- \* Battery Powered System
- \* DC/DC Converter
- \* LCD Display inverter

**FEATURE**

- \* Small surface mounting type. (SOT-563)
- \* Low-Voltage Operation
- \* High-Speed Circuits

**CONSTRUCTION**

Silicon N-Channel MOSFET



**Absolute Maximum Ratings**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	CHM1024VGP	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage - Continuous	$\pm 12$	V
$I_D$	Drain Current - Continuous $T_A=70^\circ\text{C}$	700	mA
		400	mA
$I_{DM}$	Pulsed Drain Current	400	mA
$I_S$	Continuous Source Current(Diode Conduction)	1.0	A
$P_D$	Power Dissipation (Note2)	270	mW
$T_J$	Operating Temperature Range	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$

## ELECTRICAL CHARACTERISTIC ( CHM1024VGP )

**Electrical Characteristics**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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### OFF CHARACTERISTICS

$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\mu\text{A}$	20			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 20\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
					5	$\mu\text{A}$
$I_{GSSF}$	Gate - Body Leakage, Forward	$V_{GS} = 12\text{ V}, V_{DS} = 0\text{ V}$			100	nA
$I_{GSSR}$	Gate - Body Leakage, Reverse	$V_{GS} = -12\text{ V}, V_{DS} = 0\text{ V}$			-100	nA

### ON CHARACTERISTICS

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.4		1.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 4.5\text{ V}, I_D = 600\text{ mA}$		240	360	$\text{m}\Omega$
		$V_{GS} = 2.5\text{ V}, I_D = 500\text{ mA}$		300	420	
$g_{FS}$	Forward Transconductance	$V_{DS} = 10\text{ V}, I_D = 400\text{ mA}$		1.0		S

### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{DS} = 10\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$		70		$\text{pF}$
$C_{oss}$	Output Capacitance			20		
$C_{rss}$	Reverse Transfer Capacitance			8		

### SWITCHING CHARACTERISTICS (Note 4)

$Q_g$	Total Gate Charge	$V_{DS}=10\text{V}, I_D=0.6\text{A}$ $V_{GS}=4.5\text{V}$		1.06	1.38	nC
$Q_{gs}$	Gate-Source Charge			0.18		
$Q_{gd}$	Gate-Drain Charge			0.32		
$t_{on}$	Turn-On Time	$V_{DD}= 10\text{V}$ $I_D = 0.5\text{A}, V_{GS} = 4.5\text{ V}$ $R_{GEN} = 1\Omega$		18	26	nS
$t_r$	Rise Time			20	28	
$t_{off}$	Turn-Off Time			70	110	
$t_f$	Fall Time			25	40	

# RATING CHARACTERISTIC CURVES ( CHM1024VGP )

## Typical Electrical Characteristics

Figure 1. Output Characteristics

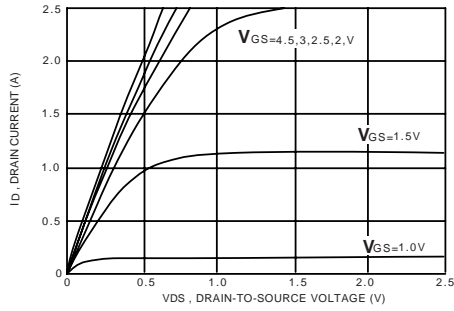


Figure 2. Transfer Characteristics

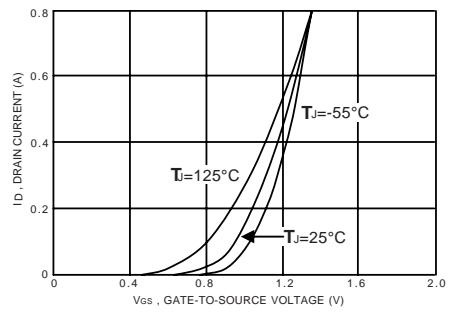


Figure 3. Gate Charge

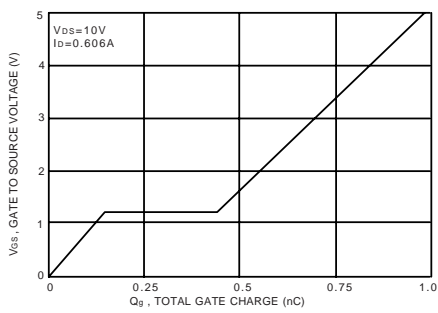


Figure 4. On-Resistance Variation with Temperature

