



CHENMKO ENTERPRISE CO.,LTD

CHM6503GP

SURFACE MOUNT

P-Channel Enhancement Mode Field Effect Transistor

VOLTAGE 30 Volts CURRENT 3.6 Ampere

Halogens free devices

APPLICATION

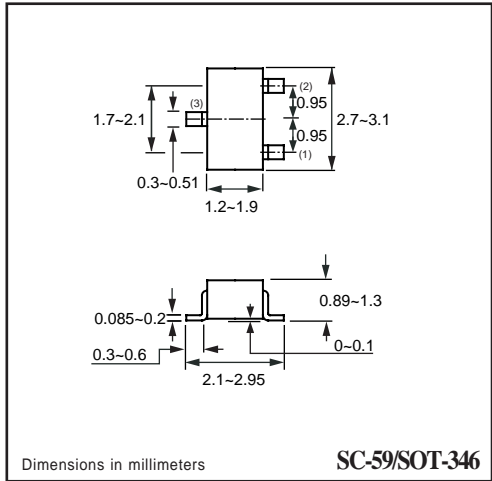
- * Servo motor control.
- * Power MOSFET gate drivers.
- * Other switching applications.

FEATURE

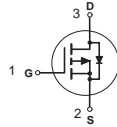
- * Small flat package. (SC-59)
- * High density cell design for extremely low Rds(ON).
- * Rugged and reliable.
- * High saturation current capability.

CONSTRUCTION

- * P-Channel Enhancement



CIRCUIT



Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	CHM6503GP	Units
V_{DSS}	Drain-Source Voltage	-30	V
V_{GSS}	Gate-Source Voltage	± 12	V
I_D	Maximum Drain Current - Continuous	-3.6	A
	- Pulsed (Note 3)	19	
P_D	Maximum Power Dissipation	1000	mW
T_J	Operating Temperature Range	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$

- Note : 1. Surface Mounted on FR4 Board , $t \leq 10\text{sec}$
 2. Pulse Test , Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$
 3. Repetitive Rating , Pulse width limited by maximum junction temperature
 4. Guaranteed by design , not subject to production testing

Thermal characteristics

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 1)	130	$^\circ\text{C/W}$
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ELECTRICAL CHARACTERISTIC (CHM6503GP)

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = -250\ \mu\text{A}$	-30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -24\text{ V}, V_{GS} = 0\text{ V}$			-1	μA
I_{GSSF}	Gate-Body Leakage	$V_{GS} = 12\text{ V}, V_{DS} = 0\text{ V}$			+100	nA
I_{GSSR}	Gate-Body Leakage	$V_{GS} = -12\text{ V}, V_{DS} = 0\text{ V}$			-100	nA

ON CHARACTERISTICS (Note 2)

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\ \mu\text{A}$	-0.45		-1.2	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS} = -10\text{ V}, I_D = -3.6\text{ A}$		43	50	$\text{m}\Omega$
		$V_{GS} = -4.5\text{ V}, I_D = -3.3\text{ A}$		51	65	
g_{FS}	Forward Transconductance	$V_{DS} = -5\text{ V}, I_D = -3.3\text{ A}$		14		S

Dynamic Characteristics

C_{ISS}	Input Capacitance	$V_{DS} = -15\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$		998		pF
C_{OSS}	Output Capacitance			115		
C_{RSS}	Reverse Transfer Capacitance			81		

SWITCHING CHARACTERISTICS (Note 4)

Q_g	Total Gate Charge	$V_{DS} = -0.5\text{ V}, I_D = -3.3\text{ A}$ $V_{GS} = -4.5\text{ V}$		10.8		nC
Q_{gs}	Gate-Source Charge			2.1		
Q_{gd}	Gate-Drain Charge			3.8		
t_{on}	Turn-On Time	$V_{DD} = -15\text{ V}$ $I_D = -3.3\text{ A}, V_{GS} = -4.5\text{ V}$ $R_{GEN} = 6\ \Omega$		40		nS
t_r	Rise Time			110		
t_{off}	Turn-Off Time			25		
t_f	Fall Time			12		

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I_S	Drain-Source Diode Forward Current	(Note 1)			-0.9	A
V_{SD}	Drain-Source Diode Forward Voltage	$I_S = -2.3\text{ A}, V_{GS} = 0\text{ V}$ (Note 2)			-1.1	V

RATING CHARACTERISTIC CURVES (CHM6503GP)

Typical Electrical Characteristics

Figure 1. Output Characteristics

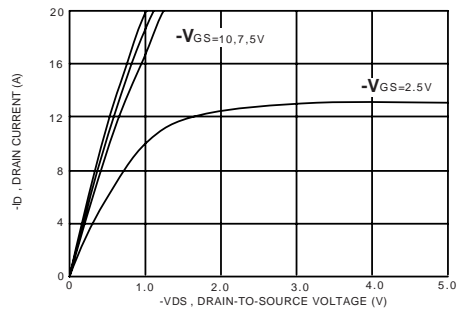


Figure 2. Transfer Characteristics

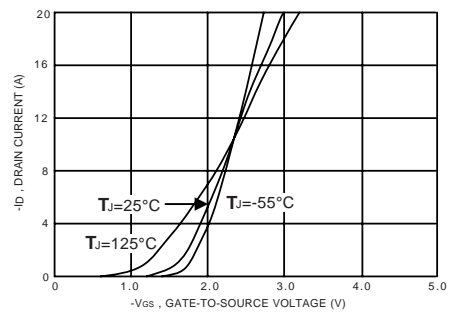


Figure 3. Gate Charge

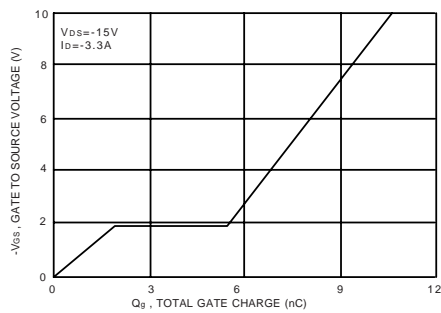


Figure 4. On-Resistance Variation with Temperature

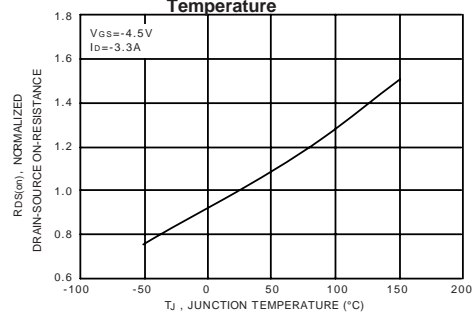


Figure 5. Gate Threshold Variation with Temperature

