



CHENMKO ENTERPRISE CO.,LTD

Halogens free devices

SURFACE MOUNT

P-Channel Enhancement Mode Field Effect Transistor

VOLTAGE 30 Volts CURRENT 13 Ampere

CHM3083JGP

APPLICATION

- * Servo motor control.
- * Power MOSFET gate drivers.
- * Other switching applications.

FEATURE

- * Small flat package. (SO-8)
- * Super high dense cell design for extremely low Rds(on).
- * High power and current handing capability.
- * Lead free product is acquired.

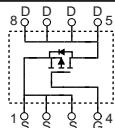
CONSTRUCTION

- * P-Channel Enhancement

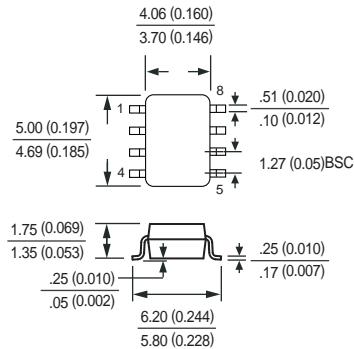
MARKING

- * 3083

CIRCUIT



SO-8



Dimensions in millimeters

SO-8

Absolute Maximum Ratings

T_A = 25°C unless otherwise noted

Symbol	Parameter	CHM3083JGP	Units
V _{DSS}	Drain-Source Voltage	-30	V
V _{GSS}	Gate-Source Voltage	±20	V
I _D	Maximum Drain Current - Continuous	-13	A
	- Pulsed (Note 3)	-52	
P _D	Maximum Power Dissipation	2500	mW
T _J	Operating Temperature Range	-55 to 150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec

2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%

3. Repetitive Rating , Pulse width limited by maximum junction temperature

4. Guaranteed by design , not subject to production testing

Thermal characteristics

R _{θJA}	Thermal Resistance, Junction-to-Ambient (Note 1)	50	°C/W
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2008-10

RATING CHARACTERISTIC CURVES (CHM3083JGP)

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}} = -30 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			-1	μA
I_{GSSF}	Gate-Body Leakage	$V_{\text{GS}} = 20 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			+100	nA
I_{GSSR}	Gate-Body Leakage	$V_{\text{GS}} = -20 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			-100	nA

ON CHARACTERISTICS (Note 2)

$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250 \mu\text{A}$	-1		-3	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}} = -10 \text{ V}, I_D = -13 \text{ A}$		8	10	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5 \text{ V}, I_D = -10 \text{ A}$		12.5	15.5	
g_{FS}	Forward Transconductance	$V_{\text{DS}} = -15 \text{ V}, I_D = -10 \text{ A}$		40		S

SWITCHING CHARACTERISTICS (Note 4)

Q_g	Total Gate Charge	$V_{\text{DS}} = -15 \text{ V}, I_D = -13 \text{ A}$ $V_{\text{GS}} = -5 \text{ V}$		37.9	50	nC
Q_{gs}	Gate-Source Charge			9.6		
Q_{gd}	Gate-Drain Charge			13.3		
t_{on}	Turn-On Time	$V_{\text{DD}} = -15 \text{ V}$ $I_D = -13 \text{ A}, V_{\text{GS}} = -10 \text{ V}$ $R_{\text{GEN}} = 6 \Omega$		18.9	37.6	nS
t_r	Rise Time			11.2	22.4	
t_{off}	Turn-Off Time			132	264	
t_f	Fall Time			63	126	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I_s	Drain-Source Diode Forward Current	(Note 1)			-1.3	A
V_{SD}	Drain-Source Diode Forward Voltage	$I_s = -1.3 \text{ A}, V_{\text{GS}} = 0 \text{ V}$ (Note 2)			-1.1	V

RATING CHARACTERISTIC CURVES (CHM3083JGP)

Typical Electrical Characteristics

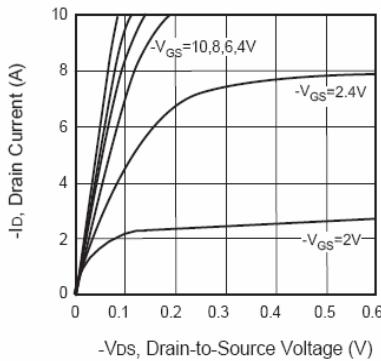


Figure 1. Output Characteristics

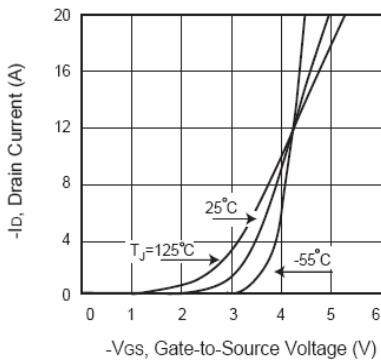


Figure 2. Transfer Characteristics

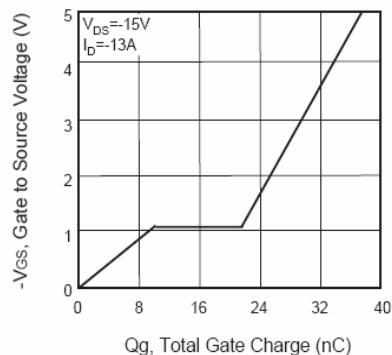


Figure 3. Gate Charge

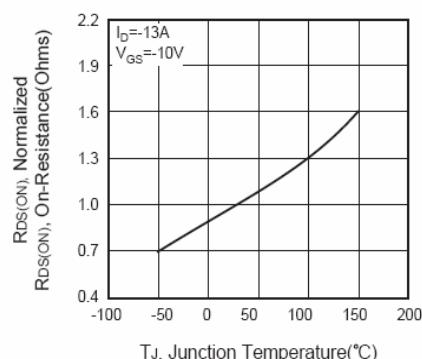


Figure 4. On-Resistance Variation with Temperature

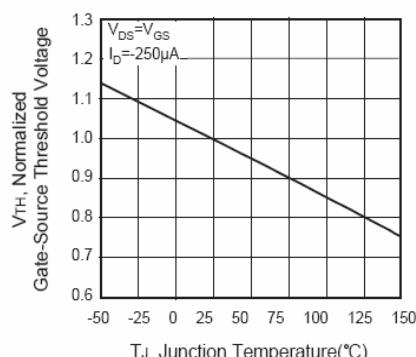


Figure 5. Gate Threshold Variation with Temperature