



CHENMKO ENTERPRISE CO.,LTD

SURFACE MOUNT

P-Channel Enhancement Mode Field Effect Transistor
VOLTAGE 30 Volts CURRENT 3.6 Ampere

CHM2313GP-A

Halogens free devices

APPLICATION

- * Servo motor control.
- * Power MOSFET gate drivers.
- * Other switching applications.

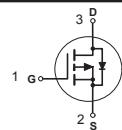
FEATURE

- * Small flat package. (SC-59)
- * High density cell design for extremely low R_{DSON}.
- * Rugged and reliable.
- * High saturation current capability.

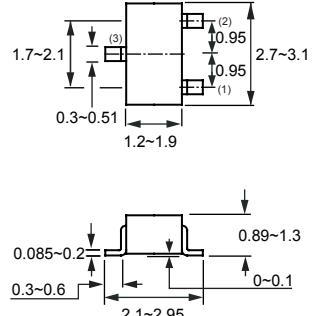
CONSTRUCTION

- * P-Channel Enhancement

CIRCUIT



SC-59/SOT-346



Dimensions in millimeters

SC-59/SOT-346

Absolute Maximum Ratings

T_A = 25°C unless otherwise noted

Symbol	Parameter	CHM2313GP-A	Units
V _{DSS}	Drain-Source Voltage	-30	V
V _{GSS}	Gate-Source Voltage	±20	V
I _D	Maximum Drain Current - Continuous	-3.6	A
	- Pulsed (Note 3)	14.4	
P _D	Maximum Power Dissipation	1250	mW
T _J	Operating Temperature Range	-55 to 150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec

2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%

3. Repetitive Rating , Pulse width limited by maximum junction temperature

4. Guaranteed by design , not subject to production testing

Thermal characteristics

R _{θJA}	Thermal Resistance, Junction-to-Ambient (Note 1)	100	°C/W
2011-08			

ELECTRICAL CHARACTERISTIC (CHM2313GP-A)

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}} = -30 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			-1	μA
I_{GSSF}	Gate-Body Leakage	$V_{\text{GS}} = 20\text{V}, V_{\text{DS}} = 0 \text{ V}$			+95	nA
I_{GSSR}	Gate-Body Leakage	$V_{\text{GS}} = -20\text{V}, V_{\text{DS}} = 0 \text{ V}$			-95	nA

ON CHARACTERISTICS (Note 2)

$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250 \mu\text{A}$	-1		-2.9	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}} = -10\text{V}, I_D = -3.6\text{A}$		50	59	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}, I_D = -2.0\text{A}$		75	88	
g_{FS}	Forward Transconductance	$V_{\text{DS}} = -15\text{V}, I_D = -3.6\text{A}$		4		S

SWITCHING CHARACTERISTICS (Note 4)

Q_g	Total Gate Charge	$V_{\text{DS}} = -15\text{V}, I_D = -10\text{A}$ $V_{\text{GS}} = -10\text{V}$		17	21	nC
Q_{gs}	Gate-Source Charge			3		
Q_{gd}	Gate-Drain Charge			3.5		
t_{on}	Turn-On Time	$V_{\text{DD}} = -15\text{V}$ $I_D = -1.0\text{A}, V_{\text{GS}} = -10 \text{ V}$ $R_{\text{GEN}} = 6 \Omega$		10	20	nS
t_r	Rise Time			6	12	
t_{off}	Turn-Off Time			46	90	
t_f	Fall Time			23	45	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I_s	Drain-Source Diode Forward Current	(Note 1)			-1.7	A
V_{SD}	Drain-Source Diode Forward Voltage	$I_s = -1.7\text{A}, V_{\text{GS}} = 0 \text{ V}$ (Note 2)			-1.2	V

RATING CHARACTERISTIC CURVES (CHM2313GP-A)

Typical Electrical Characteristics

Figure 1. Output Characteristics

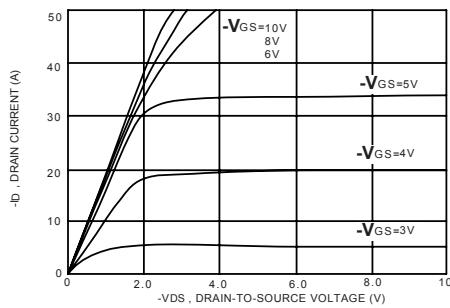


Figure 2. Transfer Characteristics

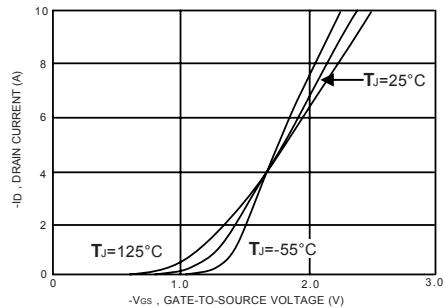


Figure 3. Gate Charge

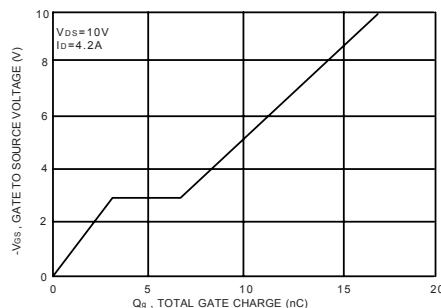


Figure 4. On-Resistance Variation with Temperature

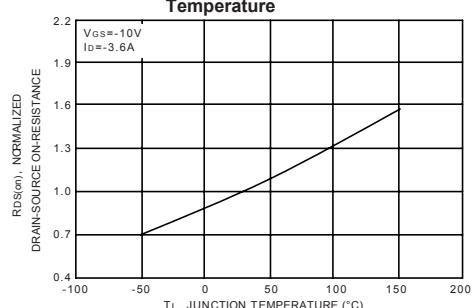


Figure 5. Gate Threshold Variation with Temperature

