



CHENMKO ENTERPRISE CO., LTD

SURFACE MOUNT

SWITCHING DIODE

VOLTAGE 80 Volts CURRENT 25 mAmpere

CHN1GP

APPLICATION

- * Ultra high speed switching

FEATURE

- * Small surface mounting type. (SC-74A)
- * Multiple diodes in one small surface mount package.
- * Suitable for high packing density.
- * Maximum total power dissipation is 150mW.
- * Peak forward current is 80mA.

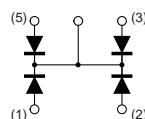
CONSTRUCTION

- * Silicon epitaxial planar

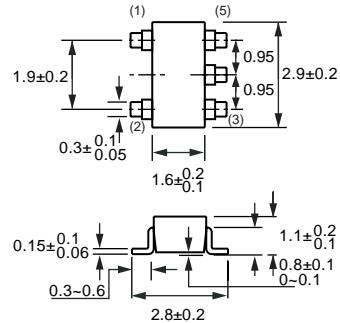
MARKING

- * D1

CIRCUIT



SC-74A



Dimensions in millimeters

SC-74A

MAXIMUM RATINGS (At TA = 25°C unless otherwise noted)

| RATINGS | SYMBOL | CHN1GP | UNITS |
|--|------------------|-------------|-------|
| Maximum Recurrent Peak Reverse Voltage | VR _{RM} | 80 | Volts |
| Maximum RMS Voltage | V _{RMS} | 56 | Volts |
| Maximum DC Blocking Voltage | V _D C | 80 | Volts |
| Maximum Average Forward Rectified Current | I _O | 25 | mAmps |
| Peak Forward Surge Current at 1uSec. | I _{FSM} | 0.25 | Amps |
| Typical Junction Capacitance between Terminal (Note 1) | C _J | 3.5 | pF |
| Maximum Reverse Recovery Time (Note 2) | T _{RR} | 4.0 | nSec |
| Maximum Operating Temperature Range | T _J | +150 | °C |
| Storage Temperature Range | T _{STG} | -55 to +150 | °C |

ELECTRICAL CHARACTERISTICS (At TA = 25°C unless otherwise noted)

| CHARACTERISTICS | SYMBOL | CHN1GP | UNITS |
|---|----------------|--------|-------|
| Maximum Instantaneous Forward Voltage at I _F = 5mA | V _F | 0.90 | Volts |
| Maximum Average Reverse Current at V _R = 70V | I _R | 0.1 | uAmps |

NOTES : 1. Measured at 1.0 MHz and applied reverse voltage of 6.0 volts.
2. Measured at applied forward current of 5mA and reverse voltage of 6.0 volts.
3. ESD sensitive product handling required.

2003-5

RATING CHARACTERISTIC CURVES (CHN1GP)

FIG. 1 - POWER DERATING CURVE

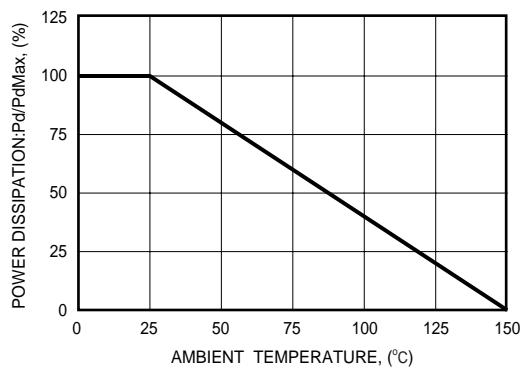


FIG. 2 - FORWARD CHARACTERISTICS

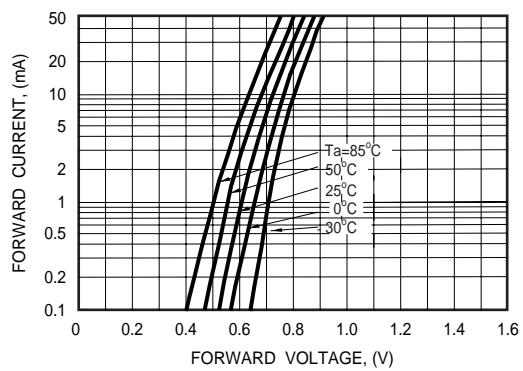


FIG. 3 - TYPICAL JUNCTION CAPACITANCE

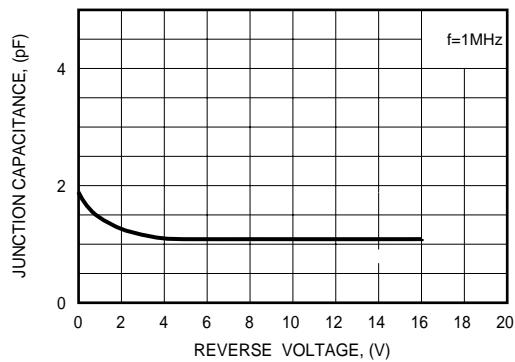


FIG. 4 - REVERSE CHARACTERISTICS

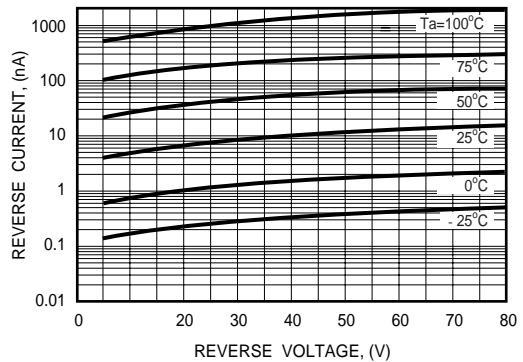


FIG. 5 - REVERSE RECOVERY TIME

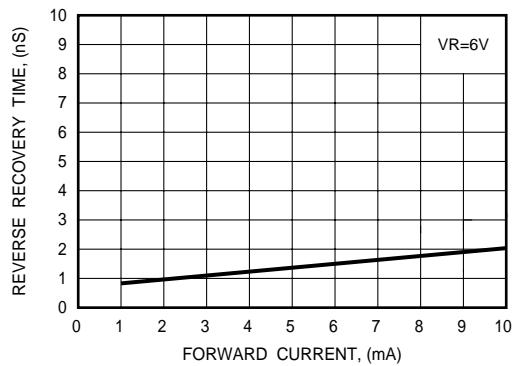


FIG. 6 - REVERSE RECOVERY TIME MEASUREMENT CIRCUIT

