



**CHENMKO ENTERPRISE CO.,LTD**

*Halogens free devices*

**SURFACE MOUNT  
SCHOTTKY DIODE ARRAY**

VOLTAGE 40 Volts CURRENT 175 mAmperes

**CH781UGP**

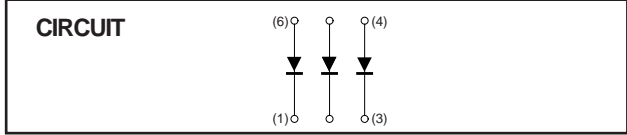
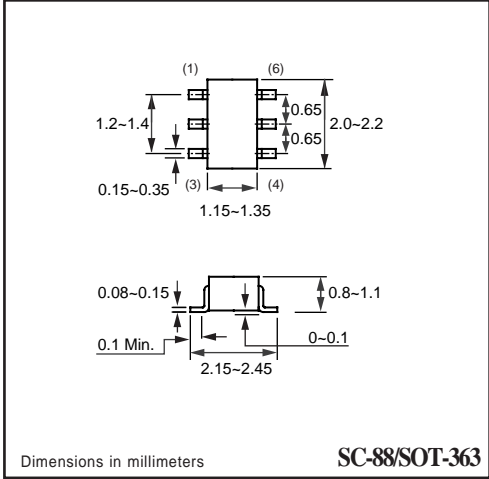
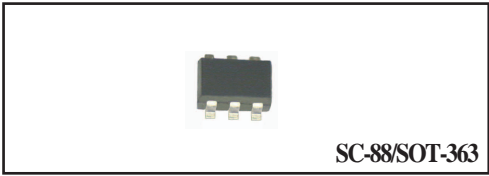
**APPLICATION**  
\* Ultra high speed switching

**FEATURE**  
\* Small surface mounting type. (SC-88/SOT-363)  
\* High speed. (TRR=8.0nSec Typ.)  
\* Suitable for high packing density.  
\* Maximum total power dissipation is 200mW.  
\*.Peak forward surge current is 1.0A.  
\* Schottky diode array .

**CONSTRUCTION**  
\* PN junction guard ring protection

**WEIGHT**  
\* 0.008 grams ( Approx.)

**MARKING**  
\* JF



RATINGS	SYMBOL	CH781UGP	UNITS
Maximum Recurrent Peak Reverse Voltage	VRRM	40	Volts
Maximum RMS Voltage	VRMS	28	Volts
Maximum DC Blocking Voltage	VDC	40	Volts
Maximum Forward Continuous Current (Note 1)	IFM	350	mAmps
Maximum Average Forward Rectified Current	Io	175	mAmps
Non-Repetitive Peak Forward Surge Current at t<1mSec.	IFSM	1.0	Amps
Typical Junction Capacitance between Terminal (Note 2)	CJ	50.0	pF
Typical Reverse Recovery Time (Note 3)	TRR	10.0	nSec
Typical Thermal Resistance	RθJA	500	°C/W
Maximum Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	TSTG	-55 to +125	°C

**ELECTRICAL CHARACTERISTICS** ( At TA = 25°C unless otherwise noted )

CHARACTERISTICS	SYMBOL	CH781UGP	UNITS
Maximum Instantaneous Forward Voltage	VF	@IF= 1mA	0.30
		@IF= 5mA	0.35
		@IF= 20mA	0.37
		@IF= 100mA	0.50
Maximum Average Reverse Current	IR	@VR= 10V	2.0
		@VR= 30V	5.0

NOTES : 1. The maximum rating of single diode ( D1 or D2 or D3 ). the maximum rating per diode are 75% of the ratings for single diode operation as using two or three diodes. 2003-04

2. Measured at 1.0 MHz and applied reverse voltage of 0.0 volts.

3. Measured at applied forward current of 200mA and reverse current of 200mA @IRR=0.1IR.

4. ESD sensitive product handling required.

## RATING CHARACTERISTIC CURVES ( CH781UGP )

FIG. 1 - FORWARD CHARACTERISTICS

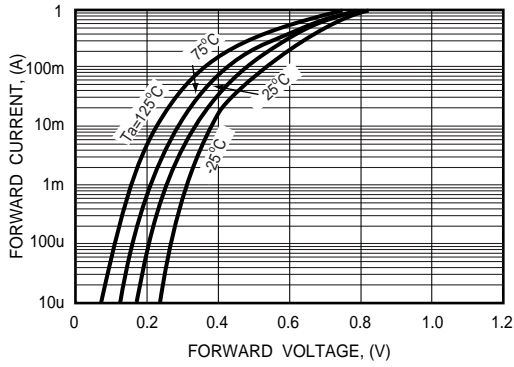


FIG. 2 - REVERSE CHARACTERISTICS

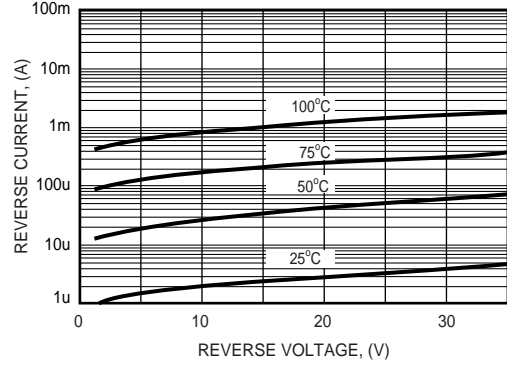


FIG. 3 - TYPICAL JUNCTION CAPACITANCE

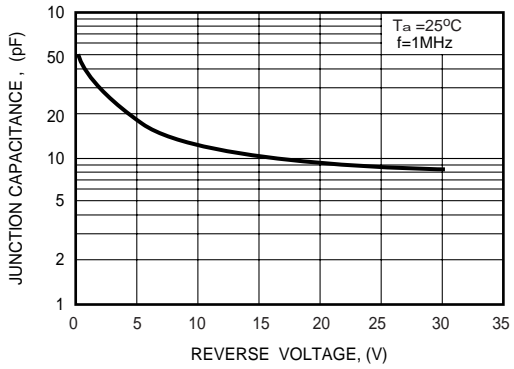


FIG. 4 - TYPICAL FORWARD CURRENT DERATING CURVE

